## REFERENCE

[1] Reston Condit and Dr. Douglas W. Jones, Stepping Motors Fundamentals, -AN907-Microchip Technology, 2004.
[2] Hazem I. A1, Robust QFT Controller Design for Positioning a Permanent Magnet Stepper Motors, International Journal of Computer and Electrical Engineering, Vol. 1, No. 1, April 2009.
[3] Marc Bodson, John N. Chiasson, Robert T.Novotnak and Ronald B. Rekowski, High Performance Nonlinear Feedback Control of a permanent Magnet Stepper Motor, IEEE Transactions on Control Systems technology, VOL. 1, NO. 1, pp5-14, March1993.
[4] Tunable laser, http://en.wikipedia.org/wiki/Tunable_laser.
[5] Tunable laser, http://www.opticsjournal.com/tutorial.htm.
[6] Laser Beam, http://web.princeton.edu/sites/ehs/laserguide/index.htm.
[7] Laser types, http://en.wikipedia.org/wiki/List_of_laser_types.
[8] Laser property, http://www.worldoflasers.com/laserproperties.htm.
[9] Nandha Kumar Thulasiraman, Haider A.F. Mohamed and YeapSoo Cheng, $A$ reconfigurable wireless stepper motor controller based on FPGA implementation, IEEE Symposium on Industrial Electronics and Applications, October 2010.
[10] Zhaojin Wen, Weihai Chen, ZhiyueXu and Jianhua Wang, Analysis of TwoPhase Stepper Motor Driver Based on FPGA, IEEE International Conference on Industrial Informatics, 2006.
[11] Wang Bangji, Liu Qingxiang, Zhou Lei, Zhang Yanrong, Li Xiangqiang and Zhang Jianqiong, Velocity Profile Algorithm Realization on FPGA for Stepper Motor Controller, IEEE International Conference, August 2011.
[12] Moussa Bendjedia, Youcef Ait-Amirat, Bernard Walther, and Alain Berthon, Position Control of a Sensorless Stepper, IEEE Transactions on Power Electronics, VOL. 27, N0. 2, February 2012.
[13] Kenneth Wang-Hay Tsui, Norbert Chow Cheung and Kadett Chi-Wah Yuen, Novel Modeling and Damping Technique for Hybrid Stepper Motor, IEEE Transactions on Industrial Electronics, VOL. 56, NO. 1, pp 202-211, January 2009.
[14] P. Acarnley, Stepping Motors - A Guide to Modern Theory and Practice, $4^{\text {th }}$ ed. London, U.K.: The Institution of Engineering and Technology, 2002. ISBN (10 digit) 085296417 X .
[15] Alexandru Morar, Stepper motor modeling for the dynamic simulation, Researchgate, VOL 44, November 2003.
[16] M. S. M. Elksasy and Hesham H.Gad, A new Technique for Controlling Hybrid Stepper Motor through Modified PID Controller, EBSCO HOST International Journal of Electrical\& Computer Sciences, VOL 10, No. 2, pp29-39, April 2010.
[17] Wonhee Kim, Donghoon Shin, and Chung Choo Chung, Microstepping Using a Disturbance Observer and a Variable Structure Controller for Permanent Magnet Stepper Motors, IEEE Transactions on industrial Electronic, 2012.
[18] Anish NK, Deepak Krishnan, S. Moorthi and M.P. Selvan, FPGA Based Microstepping Scheme for Stepper Motor in Space-Based Solar Power Systems, IEEE International Conference on Industrial and Information Systems (ICIIS), pp 1-5, August 2012.
[19] Reston Condit, Stepper Motor Control Using the PIC16F684, -AN906 Microchip Technology, 2004.
[20] Microsteping, http://www.stepperworld.com/Tutorials/pgMicrostepping.htm.
[21] Kim Otten, USB Embedded Host Stack, -AN1140 - Microchip Technology, 2008.
[22] Slave microcontroller, http://ww1.microchip.com/downloads/en/DeviceDoc/70135G.pdf, dsPIC30F4011 datasheet.
[23] Motor Driver, www.allegromicro.com/~/media/Files/Datasheets/A3995Datasheet.ashx, A3995 datasheet, A3995 datasheet.
[24] David L.jones, PCB Design Tutorial, 2004.
[25] Buck converter, http://www.ti.com/product/tps5450, TPS5450 Datasheet. Unincruity of Moreluwil, Bri Lanki Encironic Theses \& Diskrtalons WWWlbimratik

## APPENDIX A: VOLTAGE CONVERTER DESIGN

## Converter requirement:

| Maximum input voltage $\left(V_{\text {in(max) }}\right)$ | $=12.5 \mathrm{~V}$ |
| :--- | :--- |
| Minimum input voltage $\left(V_{\text {in(min) }}\right)$ | $=11.5 \mathrm{~V}$ |
| Output voltage $\left(V_{\text {out }}\right)$ | $=5 \mathrm{~V}$ |
| Output ripple $\left(V_{p p(\text { max })}\right)$ | $=0.5 \mathrm{~V}$ |
| Output Current $\left(I_{\text {out }}\right)$ | $=1.2 \mathrm{~A}$ |

The basic design is done by SwitcherPro software depending on the basic requirement. Figure A. 1 shows the schematic of the software design.


Figure A.1: Basic schematic get from the software
C 1 and C 9 capacitors are replaced with 220 uF capacitors. The total system cost is reduced due to this replacement. Also, the input ripple is reduced, because the capacitor has less ESR and high capacitance. The input voltage ripple is calculated as follows.

$$
\begin{aligned}
\Delta V_{\text {in }} & =\frac{\left(I_{\text {OUT }} \times 0.25\right)+\left(I_{\text {OUT }} \times E S R_{\text {in( } \max )}\right)}{C_{B U L K} \times F_{S W}} \\
\Delta V_{\text {in }} & =\text { Voltage ripple } \mathrm{C}_{\text {BULK }}=\text { Input capacitance } \quad F_{S W}=\text { Switching frequency } \\
E S R_{\max } & =\text { Maximum ESR of input capcitor } \\
\Delta V \text { in } & =\frac{(1.2 \times 0.25)+(1.2 \times 900 \mathrm{~m} \Omega)}{220 \mu \times 500 \mathrm{k}} \\
& =0.0125 \mathrm{~V} \\
V_{p k-p k} & =2 \Delta V_{\text {in }} \\
& =0.025 \mathrm{~V}
\end{aligned}
$$

The voltage ripple is lesser than the required level, so this capacitor is suitable for this design. This capacitor can tolerate up to 25 V , therefore, it can withstand the input voltage.

Inductor (L1) replaced with the $68 \mu \mathrm{H}$. The inductor can tolerate up to 1.5 A . Also, it reduces the current ripple in the output side and it eliminates the spikes in the output side. The RMS current and the maximum current are calculated as follows.

$$
\begin{aligned}
& I_{L(R M S)}=\sqrt{\left(I_{\text {out (max) }}\right)^{2}+\frac{1}{12}\left(\frac{V_{\text {out }}\left(V_{\text {in (max) }}-V_{\text {OUT }}\right)}{V_{\text {In(MAX) }} L_{\text {OUT }} F_{S W(M I N)}}\right)^{2}} \\
&=\sqrt{(1.2)^{2}+\frac{1}{12}\left(\frac{5 \times(12.5-5)}{12.5 \times 68 \mu \times 500 k}\right)^{2}} \\
&=1.44 A \\
& I_{L(P K)}=I_{\text {OUT }(M A X)}+\left(\frac{V_{\text {OUT }}\left(V_{I N(M A X)}-V_{\text {OUT }}\right)}{1.6 V_{I N(M A X)} L_{O U T} F_{S W(M I N)}}\right) \\
&=1.2+\left(\frac{5 \times(12.5-5))}{1.6 \times 12.5 \times 68 \mu \times 500 k}\right) \\
& I_{\text {Inincilat }} \\
&=1.255 A \\
& I_{L(R M S)}=\text { RMS current through the inductor } \\
& I_{L(p k)}= \text { Peck current through the inductor } \\
& L_{O U T}=\text { Output inductance }
\end{aligned}
$$

$\mathrm{I}_{\mathrm{L}(\mathrm{RMS})}$ is lesser than the inductor maximum RMS current (1.5A). Also, $\mathrm{I}_{\mathrm{L}(\mathrm{PK})}$ is lesser than the saturation current of the inductor (1.4A). The calculation is done at maximum current. Therefore, this inductor is suitable for the design.

C 2 capacitor is replaced with $330 \mu \mathrm{~F}$, because the output filtering is increased and the cost wise both are same. The cutoff frequency for output filleting can be calculated as follow. The maximum rated voltage is 6.3 V ; therefore it can tolerate the output voltage ( 5 V ).

$$
\begin{aligned}
F_{L C} & =\sqrt{\left(\frac{85}{3357 L_{\text {out }} C_{\text {OUT }}}\right)} \\
& =1062.24 \mathrm{~Hz}
\end{aligned} \quad \begin{aligned}
F_{L C}= & \text { Output filter cut off frequency } \\
C_{\text {OUT }} & =\text { Output capacitor capacitance }
\end{aligned}
$$

$\mathrm{F}_{\mathrm{LC}}$ is very lesser than the switching frequency $(500 \mathrm{kHz})$. Therefore, this capacitor and inductor value can produce a good filtering on the output side. The maximum allowable output capacitor ESR is calculated as follows.

$$
\begin{aligned}
E S R_{M A X} & =\left(\frac{85 \times V_{\text {OUT }}}{2 \pi C_{\text {OUT }} F_{L C}^{2}}\right) \\
& =182 m \Omega \\
E S R_{M A X} & =\text { Output capacitor maximum ESR }
\end{aligned}
$$

ESR maximum is greater than the C2 capacitor ESR ( $50 \mathrm{~m} \Omega$ ). Therefore, it can be used for this application. Output voltage ripple for the selected components can be calculated as follows.


$$
\begin{aligned}
V_{P P(M A X)} & =\frac{E S R \times V_{\text {OUT }}\left(V_{\text {in }(M A X)}-V_{\text {OUT }}\right)}{N_{C} V_{I N(M A X)} L_{\text {OUT }} F_{S W}} \\
& =\frac{50 \mathrm{~m} \Omega \times 5 \times(12.5-5)}{1 \times 12.5 \times 68 \mu \times 500 k} \\
& =0.0044 \mathrm{~V}
\end{aligned}
$$

$$
E S R=\mathrm{ESR} \text { of the selected capacitor }
$$

$\mathrm{V}_{\mathrm{pk} \text {-pk }}$ is lesser than the required level $(0.5 \mathrm{~V})$. Therefore, it satisfies the requirement. The component which was selected for this converter design can satisfy the specification.

## APPENDIX B: MECHANICAL CALCULATION

## Acceleration of the crystal:-

The maximum speed of the motor is $0.157 \mathrm{rad} / \mathrm{s}$. The time taken for the acceleration is $100 \mu \mathrm{~s}$. The speed of the motor and the load inertia are very low. Therefore, the motor can start the rotation immediately. The maximum acceleration time is equal to the minimum microsteps delay. The actual delay is more than this.

$$
\begin{aligned}
\text { Acceleration } & =\frac{0.157}{100 \mu} \\
& =1570.8 \mathrm{rad} / \mathrm{s}^{2}
\end{aligned}
$$

## Inertia of the crystal:-

Figure B. 1 shows the load which is used for the application. The mass of the load is 0.3 kg . The inertia for solid cuboids was calculated, but the load is not complete cuboids solid. Therefore, the actual inertia is lesser than the calculated value.


Figure B.1: Crystal mounts

$$
\begin{aligned}
\text { Load inertia } & =\frac{\mathrm{m}\left(\mathrm{w}^{2}+d^{2}\right)}{12} \\
& =\frac{0.3\left(0.01^{2}+0.03^{2}\right)}{12} \\
& =2.5 \times 10^{-5} \mathrm{kgm}^{2}
\end{aligned}
$$

The crystal and the holder mass and the dimensions are lesser than this load. Therefore, the inertia of the crystal is lesser than the calculated value.

Starting torque:-

$$
\begin{aligned}
\text { Starting torque } & = \\
& =J \alpha \\
& =0.000025 \times 1570.8 \\
& 0.0392 \mathrm{Nm}
\end{aligned}
$$

## APPENDIX C: QEI MODULE DESCRIPTION

The incremental quadrature encoder generates the 3 pulse output. Those signals are QEA, QEB and index. The encoder generates the pulses depending on the angular position of the motor. The index signal is used to identify the reference $\left(0^{\text {th }}\right.$ position) of the motor. The index pulse generated position is considered as the reference point. Therefore, the index signal of the encoder is used to create the absolute position reading. There is a $90^{\circ}$ phase shift between QEA and QEB signals. Figure C. 1 shows the waveform of these signals. The encoder generates 10,000 pulses per revolution.


180
Figure C.1: Encoder pulse
The QEI module in the microcontroller is using these signals and counts the position. The QEI module increased and decreased the counter depending on the rotation direction. It uses the 16 bit counter for the position counting. There are 2 different modes of counting in QEI module. Those are x 2 and x 4 mode. Figure C. 2 shows the counter changing points in the waveform for each mode.

The QEI module can detect 4 positions within the one clock period in x 4 mode. The encoder resolution is higher than the x 2 mode. The QEI module can detect 40,000 positions in one revolution. Therefore, the encoder resolution is $0.009^{\circ}$, so x 4 mode is selected for this design. The index pulse width covers the 2 encoder positions in $x 4$ mode. The QEI module can select one position as the index position according to the configuration. We can select QEA and QEB logic value for the index position detection.


Figure C.2: Mode of counting

## Example:-

The QEA and QEB should be logic 1 for detecting the index point. If the index pulse and another 2 signal are higher than only the index position will be detected.


Figure C.3: Index detection
Table C.1: Encoder specification

| Operation voltage | $3.3-28 \mathrm{~V}$ |
| :---: | :---: |
| Input current | 100 mA |
| Output format | incremental |
| Output type | Open collector |
| Pulses | 10000 pulse per revolution |
| Index | One per revolution |
| Max Shaft speed | 8000 RPM |
| Bore size | 5 mm |
| Max Acceleration | $1 \times 10^{5} \mathrm{rad} / \mathrm{s}^{2}$ |
| Starting torque | 0.001 Nm |

## Digital filter



Figure C.4: Filter structure


Figure C.5: Waveform of the filtering
Figure C. 4 shows the filter structure. Figure C. 5 shows the waveform of the filtered signal. $T_{C Y}$ clock is used to control the digital filtering. The period of the $T_{c y}$ clock is $3.2 \mu \mathrm{~s}$. The state of the QE signal is decided after $4 \mathrm{~T}_{\text {cy }}$ clock pulse. The filter creates the $12.8 \mu \mathrm{~s}$ phase delay. It eliminates the short pulses generated by the noise. The pulses which have the period less than the $12.8 \mu$ s are removed.

## APPENDIX D: MOTOR SPECIFICATION

## Motor specification:

- Maximum current per winding 0.21A.
- Resistance per winding 57.1 $\Omega$.
- The inductance of the winding is 32 mH .

$$
\begin{aligned}
\text { Maximum power loss per winding } & =\left(\mathrm{I}^{2} \mathrm{R}\right) * 2 \\
& =\left(0.21^{2}\right) * 57.1 * 2 \\
& =4.568 \mathrm{~W}
\end{aligned}
$$

The system is not battery powered. Therefore, this loss is not a problem. The motor case is made of metal, therefore the heat transfer to the environment quickly. The motor is not damaged due to the heat.

## Resonance vibration compared with the standard stepper motor



Figure D.1: Vibration comparison between the motor


Figure D.2: Torque vs. speed

