

**VOLTAGE PROFILE IMPROVEMENT IN MV  
DISTRIBUTION LINE: COST OPTIMAL ALLOCATION  
OF REACTIVE POWER COMPENSATORS**

Ediriweera Patabendige Anusha Sampath

(109252U)



University of Moratuwa, Sri Lanka.  
Electronic Theses & Dissertations  
[www.lib.mrt.ac.lk](http://www.lib.mrt.ac.lk)  
Degree of Master of Science

Department of Electrical Engineering

University of Moratuwa  
Sri Lanka

December 2014

**VOLTAGE PROFILE IMPROVEMENT IN MV  
DISTRIBUTION LINE: COST OPTIMAL ALLOCATION  
OF REACTIVE POWER COMPENSATORS**

Ediriweera Patabendige Anusha Sampath

(109252U)



University of Moratuwa, Sri Lanka.  
Electronic Theses & Dissertations  
[www.lib.mrt.ac.lk](http://www.lib.mrt.ac.lk)

Dissertation submitted in partial fulfillment of the requirements for the degree of  
Master of Science

Department of Electrical Engineering

University of Moratuwa  
Sri Lanka

December 2014

## DECLARATION

“I declare that this is my own work and this dissertation, does not incorporate without acknowledgement any material previously submitted for a Degree or Diploma in any other University or institute of higher learning and to the best of my knowledge and belief it does not contain any material previously published or written by another person except where the acknowledgement is made in the text.

Also, I hereby grant to University of Moratuwa the non-exclusive right to reproduce and distribute my dissertation, in whole or in part in print, electronic or other medium. I retain the right to use this content in whole or part in future works (such as articles or books)”.

Signature:

Date

(E.P. Anusha Sampath)



University of Moratuwa, Sri Lanka.  
Electronic Theses & Dissertations  
[www.lib.mrt.ac.lk](http://www.lib.mrt.ac.lk)

The above candidate has carried out research for the Masters Dissertation under my supervision.

Signature of the supervisor:

Date

(Dr. Asanka S.Rodrigo)

## ABSTRACT

The distribution power lines have some power quality problems such as voltage drop voltage sag, voltage fluctuation, voltage flicker, transient voltages etc. Those are some common problems in the distribution systems all over the world. Those problems are occurred due to reasons such as loading pattern, length of the line, consumer behavior in the feeder etc.

Considering Sri Lanka, in recent history there was no much power requirement and the demand of the electricity supply is less. At present distribution system is expanded to the whole country and provides the electricity to each house hold. Additionally due to growing of economy in the country the industries, factories are also established and the demand growth is occurred very fast. Therefore the Power quality problems were raised in the distribution system rapidly in past few years.

To minimize the voltage violation of the regulation, the several temporary solutions were adopted to the system in time to time by implementing the short term planning proposals. But they were not much suitable solutions to mitigate the voltage violation of the system permanently.

This research was done to enhance the system voltage by applying the reactive power compensators along the distribution feeders. Analysis of the whole network of Sri Lanka by simulating is a very difficult task and therefore the most critical feeder of the down south network of the electricity is selected as a case study of the research. The same procedure is followed by another feeder also as derivation of the results.

For this drastic problem, this study is carried out to find the solution by placing of reactive compensators in the distribution feeder to improve the voltage level within the required limit of  $\pm 6\%$  of 33 kV as the CEB planning guide lines.

The Synrgce software is used to prepare a model and to analyze the feeder. Loading data of the transformers and conductor data in the feeder is required to prepare the model. This model is a mathematical model and by using load flow technique the model is analyzed. In this analysis, a range of voltage drops were recorded by adding capacitor banks to the line and there by the required sizes and locations of the capacitor banks were recorded as in the relevant voltage drop. Study about the capacitor bank types and installation methods were carried out to get the most effective solution. The results obtain from the study is to install one switching capacitor bank of 3600 kVar ,and two fixed capacitor banks of 150 kVar and one 300 kVar fixed capacitor bank in the feeder which improve the voltage level within the required level of the regulation of CEB.

As a Conclusion of this study, the voltage level improvement can be carried out with reactive power compensator placement to the medium voltage distribution line economically. The total capacitor bank sizes installed in the distribution feeder should be less than the required reactive power in that feeder. The Fixed capacitor banks should be installed at the most affected load ends with validation of off peak loads also. After selecting the fixed capacitor banks sizes the rest of the reactive power requirement should be installed with capacitor banks having switching facility and the harmonic damping reactors should be installed at the load center. The power utilities can implement the capacitor bank installation techniques to improve the voltage level up to regulated level in the distribution network. It is an economically viable solution where thoes capacitor banks can be used with any other feeder enhancement in the future and they will provide the additional benefits of improving power factor, reducing feeder losses etc.

## ACKNOWLEDGEMENT

My Sincere thanks go to Dr. Asanka S Rodrigo, Senior Lecturer of Department of Electrical Engineering University of Moratuwa Sri Lanka, for his great insights, perspectives, guidance and sense of humor.

I would like to thank Professor Lukas, Professor Ranjith Perera, Professor Karunadasa, Mr.Wijepala for their constructive criticism during project progress presentations which helped me to make necessary improvements.

My sincere thanks go to the officers in Post Graduate Office, Faculty of Engineering, University of Moratuwa, Sri Lanka for helping in various ways to my academic works and other relevant things in the time with excellent cooperation and guidance. My gratitude is also extended to the people who serve in the Department of Electrical Engineering Office.

My special thanks go to Dr. Wjeekoon, Chief Engineer – Transmission Planning, Ceylon Electricity Board for reviewing my thesis with his valuable comments.



University of Moratuwa, Sri Lanka  
Electronic Theses & Dissertations  
[www.lib.mrt.ac.lk](http://www.lib.mrt.ac.lk)

I also thank Eng. L.C.A. Pushpakumara, Chief Engineer – Distribution Maintenance, Southern Province, Ceylon Electricity Board for the help given to carry out the thesis work successfully.

I would like to thank Mrs C. Gamlath, Electrical Engineer –Transmission Projects, and Mr. B.M.A.T Priyadarshana, Electrical Engineer –Energy Management, Ceylon Electricity Board for the help given to make a success in this project and also my friends, colleagues who have not been mentioned here personally in making this research a success. My thanks are also extended to, people in my working place, who support me to make this educational process a success.

Lastly, I would like to thank my family. The constant inspiration and guidance kept me focused and motivated. May be I could not have done this without your support.

## TABLE OF CONTENTS

Declaration	i
Abstract	ii
Acknowledgement	iii
Table of Contents	iv
List of Figures	vii
List of Tables	ix
List of Abbreviations	x
1. Introduction	1
1.1 Background	1
1.2 Present Situation	2
1.2.1 Voltage Criteria	2
1.3 Proposals for the Voltage Quality Problems in the System	2
1.3.1 Short Term proposals	3
1.3.2 Long Term proposals	3
1.4 Motivation	4
2. Statement of the Problem	6
2.1 Identification of the Problem	6
2.2 Objective of the Study	8
2.3 Importance of the Study	9
2.4 Scope of work	9
3. Methodology	11
3.1 Introduction	11
3.2 Concept of Capacitor Bank placement	11
3.3 Capacitor Bank Compensation Type	14
3.3.1 Fixed Type	14
3.3.2 Switched Type	14
3.3.3 Automated Type	15

3.4 Capacitor Bank placement	15
3.4.1 Individual Capacitor Bank Placement	15
3.4.2 General Capacitor Bank Placement	16
3.4.3 Group Capacitor Bank Placement	16
3.5 Protection of Capacitors	17
3.5.1 Fuse Protection	17
3.5.1.1 Internally Fused Capacitors	17
3.5.1.2 Externally fused Capacitors	18
3.5.2 Fuse-less Capacitors	18
3.5.3 Harmonic Problem and Transient when the Switching capacitors	18
3.5.3.1 Damping Reactors	19
3.5.3.2 Detuned Reactors	19
3.5.3.3 Harmonic Filters	19
3.5.4 Quick Discharge Reactors	19
4. Case Study	20
4.1 Introduction of the Case Study	20
4.2 Simulation Software (Synergee Software)	22
4.3 Data Collection of Existing Distribution Feeder	22
4.3.1 Behavior of the Existing Feeder	22
4.3.2 Peak Hours	23
4.4 Data Collection for the Case Study	24
4.4.1 Line and Conductor data of the Feeder	24
4.4.2 Loading data of each transformer in the feeder	25
4.4.3 Types of Loads connected to the Transformer	25
4.4.4 Power Injection to the Feeder	25
4.4.5 Identify the Optimal Configuration of the Feeder	25
4.5 The Model	26
4.5.1 Load Characteristics	26
4.6 Simulation with Load Flow and Network Analysis	29
4.7 Capacitor Size Selection	30
4.8 Capacitor Location Selection	34

4.8.1 One Capacitor Bank Placement (Option 1)	35
4.8.2 Two Capacitor Bank placement (Option 2)	39
4.8.3 Three Capacitor Bank placement (Option 3)	41
4.8.4 Four Capacitor Bank placement (Option 4)	43
4.8.5 Another Four Capacitor Bank placement (Option 5)	45
4.8.6 Five Capacitor Bank Placement (Option 6)	47
4.8.7 Six Capacitor Bank placement (Option 7)	49
4.9 Technical and Economical Analysis	52
4.9.1 Technical Analysis	52
4.9.1.1 Voltage Drop Comparison	52
4.9.1.2 Loss Comparison	53
4.9.2 Economical Analysis	54
4.9.3 Results	55
4.10 Summary of the other feeder Analysis	58
5. Recommendation	60
5.1 Recommendation of Main Critical Feeder	60
5.2 Recommendation of Second Feeder	60
5.3 General Recommendation for any Feeder	61
6. Discussion	63
6.1 Discussion	63
7. Conclusion	66
7.1 Conclusion	66
Reference List	67
Appendix 1: Network Diagram of the Southern Province	69
Appendix 2: Transformer Load Data and planning Process	70
Appendix 3: Voltage Levels in each Options	84



## LIST OF FIGURES

	Page	
Figure 3.1	Single Line diagram for Feeder without capacitor banks	12
Figure 3.2	Phasor Diagram for the feeder without capacitor banks	12
Figure 3.3	Single Line diagram for Feeder with capacitor banks	13
Figure 3.4	Phasor Diagram for the feeder with capacitor banks	13
Figure 3.5	Improvements of the Capacitor Bank Placement	14
Figure 3.6	Individual Capacitor placement	15
Figure 3.7	General Capacitor placement	16
Figure 3.8	Group Capacitor placement	16
Figure 3.9	Typical Internally fused capacitor unit	17
Figure 3.10	Typical Fuseless Capacitor unit	18
Figure 4.1	Schematic diagram of feeder arrangement	20
Figure 4.2	Single Line diagram of feeder arrangement	21
Figure 4.3	Feeder Behavior of the Kataragama Feeder	24
Figure 4.4	Model of the Kataragama Feeder	28
Figure 4.5	Load Flow Analysis of Existing Feeder	29
Figure 4.6	Voltage drop Profile with 1200 kVar Capacitor Bank	30
Figure 4.7	Voltage drop Profile with 2400 kVar Capacitor Bank	31
Figure 4.8	Voltage drop Profile with 3600 kVar Capacitor Bank	32
Figure 4.9	Voltage drop Profile with 4800 kVar Capacitor Bank	33
Figure 4.10	4200 kVar Capacitor location selection with voltage drop	35
Figure 4.11	4200 kVar Capacitor location selection with voltage drop	36
Figure 4.12	4200 kVar Capacitor location selection with total voltage level improvement	37
Figure 4.13	Voltage Profile of the 4200 kVar Capacitor Bank Placement (Option 1)	38
Figure 4.14	Two Capacitor Bank placement with simulation Results	39
Figure 4.15	Voltage Profile of the 3000 kVar and 1350 kVar Capacitor Banks Placement (Option 2)	40
Figure 4.16	Three Capacitor Bank Placement	41

Figure 4.17	Simulation Results with Three Capacitor Bank Placement	41
Figure 4.18	Voltage Profile of the Three Capacitor Bank Placement (Option 3)	42
Figure 4.19	Four Capacitor Bank Placement	43
Figure 4.20	Simulation Results for the Four Capacitor Bank Placement	43
Figure 4.21	Voltage Profile of the Four Capacitor Bank Placement (Option 4)	44
Figure 4.22	Four Capacitor Placement	45
Figure 4.23	Results of the simulation with Four Capacitor Bank Placement	45
Figure 4.24	Voltage Profile of the Four Capacitor Bank Placement (Option 5)	46
Figure 4.25	Five Capacitor Banks Placement	47
Figure 4.26	Simulation Results with Five Capacitor Bank placement	47
Figure 4.27	Voltage Profile of the Five Capacitor Bank Placement (Option 6)	48
Figure 4.28	Six Capacitor Bank Placement	49
Figure 4.29	Simulation Results with Six capacitors Bank Placement	50
Figure 4.30	Voltage Profile of the Six Capacitor Bank Placement (Option 7)	51
Figure 4.31	Percentage Voltage Drop with each options	53
Figure 4.32	Percentage Loss for the each option	53
Figure 4.33	Cost involvements with each option	55
Figure 4.34	Summary of the Analysis Result	55
Figure 4.35	Summary of the Voltage profile with existing and capacitor selection options	57
Figure 4.36	Feeder arrangement in synergee software	58
Figure 4.37	Summary of the analysis result in second feeder	59
Figure 5.1	Final Selected Locations and Sizes of the Capacitor Banks	60
Figure 5.2	Final Selected locations and sizes of the capacitor banks in the Second feeder	61

## LIST OF TABLES

	Page	
Table 1.1	Acceptable Voltage Levels	2
Table 4.1	Collected Load Data	23
Table 4.2	Conductor Parameters	24
Table 4.3	% load components	27
Table 4.4	Selected capacitor bank sizes to install in the feeder to improve voltage profile	34
Table 4.5	Summary of the seven Methods feeder data and existing feeder data	52
Table 4.6	Cost involvements for the each option	54
Table 4.7	Feeder Data – Matara GSS-Galle Feeder	58
Table 4.8	Summary Results and Cost involvements for the each option	59
Table A2.1	Load Reading data of the Transformers in the Kataragama Feeder	70
Table A2.2	Planning Process	80
Table A3.1	Voltage Levels of the existing feeder and the capacitor placement of the feeder	84

## LIST OF ABBREVIATIONS

Abbreviation	Description
CEB	Ceylon Electricity Board
IEEE	Institute of Electrical and Electronic Engineers
MV	Medium Voltage
LV	Low Voltage
GSS	Grid Sub Station
LBS	Load Break Switch
AR	Auto Reclouser
R	Line Resistance
I	Line Current
$V_s$	Sending End Voltage
$V_r$	Receiving End Voltage
$V_0$	Nominal Voltage (33kV)
$V_1$	Node Voltage
PF	Power Factor
$\lambda$	No.of Capacitor Banks
C	Cost of Capacitor Bank



University of Moratuwa, Sri Lanka.  
Electronic Theses & Dissertations  
[www.lib.mru.ac.lk](http://www.lib.mru.ac.lk)