Bibliography

[1] Ray Andraka, *A survey of cordic algorithms for fpga based computers*, International Symposium on Field Programmable Gate Arrays, 1998, pp. 199–200.

[2] Didier Cottet Gerhard Troster Andreas Romer, Rolf Enzler, *Reconfigurable fpga processor*, Tech. report, Swiss Federal Institute of Technology Electronics Laboratory CH-8092 Zurich, 2000.

[3] Andrew Brooksy, Glenn Dickinsy, Alexander Zelinskyy, Jon Kie erz, and Samer Abdallahz, *A high performance camera platform for real time active vision*, In Field and Service Robotics, 1998, pp. 527–532.

[4] Milos D. Ercegovac and Tomas Lang (eds.), *Radix-4 square root without initial pla*, vol. 39, IEEE Transaction on Computers, August 1990.

[5] Reza Hazemian (ed.), *Square rooting algorithms for integer and floating-point numbers*, vol. 39, IEEE Transaction on Computers, August 1990.

[6] Douglas W. Jones, *Control of stepping motors*, http://www.cs.uiowa.edu/jones/step/.

[7] Helder Araujo Jorge Batista, Paulo Peixoto, *Visual behaviors for real-time control of a binocular active vision system*, Tech. report, Institute of System and Robotic, Department of Electrical Engineering University of Coinmbra, Portugal, 2000.

[8] Angkul Kongmunvattana and Prabhas Chongstitvatana, *A fpga-based behavioral control system for a mobile robot*, IEEE APCCAS, 1998.

[9] Yamin Li and Waming Chu, *A new non-restoring square root algorithm and its vlsi implementations*, IEEE International Conference on Computer Design, 1996, p. 538.

[10] W.W. Mayol, B. Tordoff, and D.W. Murray, *Towards wearable active vision platforms*, Tech. report, Department of Engineering Science, University of Oxford., 2000.

[11] M.G.B.Sumanasena, *Multiple degree of freedom stereo camera platform for active vision: Instruction set architecture*, Master's thesis, University of Moratuwa, Department of Electronic and Telecommunication, 2006.

[12] Kourosh Pahlavan, *Active robot vision and primary ocular processes*, Computational Vision and Active Perception Laboratary, May 1993, pp. 527–532.

[13] J. Arjun Prabhu and Gregory B. Zyner, *167 mhz radix-8 divide and square root using overlapped radix-2 stages*, IEEE Symposium on Computer Arithmetic, 1995, p. 155.

[14] P M Sharkey, D W Murray, S Vandevelde, I D Reid, and P F McLauchlan, *A modular head/eye platfom for real-time reactive vision*, Mechatronics Journal (1993), 517–535.

[15] Harley Truong, Samer Abdallah, Sebastien Rougeaux, and Alexander Zelinsky, *A novel mechanism for stereo active vision*, Australian Conference on Robotics and Automation, 2000, pp. 161–166.

[16] U.Mayer-Baese, *Digital signal processing with field programmable gate arrays*, Springer-Verlang, 2001.

[17] Keith Underwood, *Fpgas vs. cpus: Trends in peak floatingpoint performance*, Tech. report, Sandia National Laboratories, 2000.

[18] Y.Kuniyoshi, N. Kita, S. Rougeaux, and T.Suehiro, *Active stereo vision system with foveated wide angel lenses*, Asian Conf. on Computer Vision, Singapore, 1995, pp. 527–532.