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## APPENDIX A: SPECIFICATIONS AND CODING

### A1.1 Precision Instrumentation Amplifier AD524

#### FEATURES

Low noise	: 0.3 $\mu\text{V}$ p-p at 0.1 Hz to 10 Hz
Low nonlinearity	: 0.003% (G = 1)
High CMRR	: 120 dB (G = 1000)
Low offset voltage	: 50 $\mu\text{V}$ Low offset
Voltage drift	: 0.5 $\mu\text{V}/^\circ\text{C}$
Gain bandwidth product	: 25 MHz

- ⊗ Pin programmable gains of 1, 10, 100, 1000
- ⊗ Input protection, power-on/power-off
- ⊗ No external components required
- ⊗ Internally compensated
- ⊗ MIL-STD-883B and chips available
- ⊗ 16-lead ceramic DIP and SOIC packages and 20-terminal leadless chip carrier available
- ⊗ Available in tape and reel in accordance with EIA-481A standard
- ⊗ Standard military drawing also available

#### GENERAL DESCRIPTION

The AD524 is a precision monolithic instrumentation amplifier designed for data acquisition applications requiring high accuracy under worst-case operating conditions. An outstanding combination of high linearity, high common-mode rejection, low offset voltage drift, and low noise makes the AD524 suitable for use in many data acquisition systems.

The AD524 has an output offset voltage drift of less than 25  $\mu\text{V}/^\circ\text{C}$ , input offset voltage drift of less than 0.5  $\mu\text{V}/^\circ\text{C}$ , CMR above 90 dB at unity gain (120 dB at G = 1000), and

maximum nonlinearity of 0.003% at  $G = 1$ . In addition to the outstanding dc specifications, the AD524 also has a 25 kHz bandwidth ( $G = 1000$ ). To make it suitable for high speed data acquisition systems, the AD524 has an output slew rate of 5 V/ $\mu$ s and settles in 15  $\mu$ s to 0.01% for gains of 1 to 100.

As a complete amplifier, the AD524 does not require any external components for fixed gains of 1, 10, 100 and 1000. For other gain settings between 1 and 1000, only a single resistor is required. The AD524 input is fully protected for both power-on and power-off fault conditions.

The AD524 IC instrumentation amplifier is available in four different versions of accuracy and operating temperature range. The economical A grade, the low drift B grade, and lower drift, higher linearity C grade are specified from  $-25^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ . The S grade guarantees performance to specification over the extended temperature range  $-55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ . The AD524 is available in a 16-lead ceramic DIP, 16-lead SBDIP, 16-lead SOIC wide packages, and 20-terminal leadless chip carrier.

## **PRODUCT HIGHLIGHTS**

1. The AD524 has guaranteed low offset voltage, offset voltage drift, and low noise for precision high gain applications.
2. The AD524 is functionally complete with pin programmable gains of 1, 10, 100, and 1000, and single resistor programmable for any gain.
3. Input and output offset nulling terminals are provided for very high precision applications and to minimize offset voltage changes in gain ranging applications.
4. The AD524 is input protected for both power-on and power-off fault conditions.
5. The AD524 offers superior dynamic performance with a gain bandwidth product of 25 MHz, full power response of 75 kHz and a settling time of 15  $\mu$ s to 0.01% of a 20 V step ( $G = 100$ )

## **A1.2 Microprocessor Programmable Universal Active Filters (MAX260/MAX261/MAX262)**

The MAX260/MAX261/MAX262 CMOS dual second order universal switched-capacitor active filters allow microprocessor control of precise filter functions. No external components are required for a variety of band pass, low pass, high pass, notch, and all pass configurations. Each device contains two second-order filter sections that place center frequency, Q, and filter operating mode under programmed control. An input clock, along with a 6-bit  $f_0$  program input, determine the filter's center or corner frequency without affecting other filter parameters. The filter Q is also programmed independently. Separate clock inputs for each filter section operate with a crystal, RC network, or external clock generator. The MAX260 has offset and DC specifications superior to the MAX261 and MAX262 and a center frequency ( $f_0$ ) range of 7.5 kHz. The MAX261 handles center frequencies to 57 kHz, while the MAX262 extends the center frequency range to 140 kHz by employing lower clock-to- $f_0$  ratios. All devices are available in 24-pin DIP and small outline packages in commercial, extended, and military temperature ranges.

### **FEATURES**

- ⊠ Filter Design Software Available
- ⊠ Microprocessor Interface
- ⊠ 64-Step Center Frequency Control
- ⊠ 128-Step Q Control
- ⊠ Independent Q and  $f_0$  Programming
- ⊠ Guaranteed Clock to  $f_0$  Ratio-1% (A grade)
- ⊠ 75kHz  $f_0$  Range (MAX262)
- ⊠ Single +5V and  $\pm 5V$  Operation

### A1.3 Arduino DUE Technical Specification

Microcontroller	AT91SAM3X8E
Operating Voltage	3.3V
Input Voltage (recommended)	7-12V
Input Voltage (limits)	6-20V
Digital I/O Pins	54(of Which 16 Provide PWM output)
Analog Input Pins	12
Analog Output Pins	2(DAC)
Total DC Output Current on all I/O lines	130mA
DC Current for 3.3V Pin	800mA
DC Current for 5V Pin	theoretical 1A, realistic 800mA
Flash Memory	512KB all available for the user applications
SRAM	96KB (64+32KB)
Data Flash	2 Mbit (250KB)
Clock Speed	84MHz

## A1.4 InstanTel Micromate Specification

### Tri axial geophone

Table 5-1 Tri axial geophone Specification

Range	Up to 254 mm/s (10 in/s)
Response Standard	ISEE Seismograph Specification or DIN 45669-1
Resolution	0.00788 mm/s (0.00031 in/s)
Frequency Range (ISEE/DIN)	2 to 250 Hz, within zero to -3 dB of an ideal flat response / 1 to 315 Hz
Accuracy (ISEE/DIN)	+/- 5% or 0.5 mm/s (0.02 in/s), whichever is larger, between 4 and 125 Hz / DIN 45669-1 Standard
Transducer Density	2.2 g/cc (137 lbs/ft <sup>3</sup> )
Maximum Cable Length (ISEE/DIN)	1,000 m (3,250 ft.)