



OPTIMIZING THE. USE OF BREAKER SWITCHED CAPACITORS IN CEB SYSTEM

A dissertation submitted to the
Department of Electrical Engineering, University of Moratuwa
in partial fulfillment of the requirements for the
Degree of Master of Science

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Abstract

Ceylon Electricity Board (CEB) as many other utilities uses breaker switched capacitor banks for voltage support and reactive power compensation in grid substations. At present it has a 320Mvar installed capacity and 70Mvar more to be come in next few years. The main intentions of the use of capacitor banks is to give voltage support at the substation level, reduction of losses in power transformers and transmission lines, and to release the capacity constraints in transformers and lines.

CEB uses power factor regulation for switching these capacitor banks. The general view of the system control center (SCC) who operates the network is that this concept does not allow economical utilization of capacitor banks and sometimes they need to manually switch on them overriding the auto controller and vice versa. Underutilizing an economical reactive power source is a factor to consider. Therefore, the objective of this research is to study the technical feasibility of connecting maximum available capacitor banks in each sub station and by doing so, to propose a better switching policy than the existing one.

The research was planned as a case study, selecting a typical grid sub station in CEB and then, the results are expected to be extrapolated to a general concept, to suit the whole CEB network. First, actual substation data was collected, logged and analyzed. The possibilities of connecting more capacitor banks, under such real time system characteristics were studied in a computer simulation model. PSCAD is the simulation software used for the network simulations. The impacts due to additional banks on the system conditions, technical constraints, non violation of general standards and economics were studied using the results from the simulations. The results were compared with actual data measurement by forcing the simulated conditions for the maximum utilization, in the real system.



The analysis revealed that the present switching concept does not fully fit for CEB network. The possibilities of further utilization of already installed capacitor banks, was identified. Instead of present switching criteria, reactive power based control and voltage based control schemes were evaluated. Although the present criterion has a comparatively high utilization factor, it also seems that banks are not utilized at mostly required periods. As per the observations, reactive power controlled capacitor bank switching criteria is more useful compared to loss reduction in the system. When comparing the voltage control based switching, the switching pattern is similar to the pattern with reactive power control based switching in the day time. -During night time it gets closure to the requirement that SCC actually needs. However, complex algorithms are necessary to coordinate the two control loops, AVR and capacitor bank controller when-using such voltage control schemes. When two independent controls try to control same parameter, it leads to an unnecessary switching or simply, hunting the tap changer and capacitor banks.

Finally, as the conclusion of the research, multi functional switching scheme based on voltage and reactive power was proposed for the switching policy of the capacitor banks in the CEB network.

DECLARATION

The work submitted in this dissertation is the result of my own investigation, except where otherwise stated.

It has not already been accepted for any degree, and is also not being concurrently submitted for any other degree.

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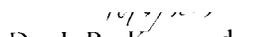


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1. Introduction

1.1 Background

The nature of all electrical loads connected to power system is such that they are inherently inductive which consume reactive power and therefore the system has to generate reactive energy. Although the reactive power does not produce any usable output, each network operator has to live with it. Therefore power utilities around the globe have to invest on required reactive energy which in turn does not give compensation. Not only the generators have to produce this ineffective energy, the same shall be transmitted to the end user as well. The ultimate result of these is to introduce losses, capacity constraints in transmission and distribution networks and voltage drops. That is why most of the power utilities around the world are trying to generate its reactive power requirements as close as possible to the load centres. In general, many utilities describe this as the concept of reactive power compensation in the technical vocabulary.

Apart from generating reactive power from the costly generators, compensation can be done with variety of sources. Using static var compensators, synchronous condensers, breaker switched capacitor banks are common among these. Breaker switched or fixed capacitor banks, especially those at distribution level are still most effective whereas the cost is concerned. They are comparatively economical and installation is also easy. Retrofitting and later additions according to match load characteristics are comparatively flexible.

The application of capacitor banks and its controlling philosophy is different from location to location. For an end consumer it is used as a power factor corrector that helps to reduce his demand and avoid penalties from the energy supplier. For a distribution company, the capacitors installed at intermediate locations on distribution line reduce line losses hence increases line capacities and improve the bus voltage. For a transmission company, the intention is not only to reduce losses or increase line capacities but also to give voltage support which is an inherent system problem under heavily loaded conditions and to further differ investment costs on improving lines and substation capacities. At generation buses, capacitor banks also can be used for voltage support though it is rare. Depending on the location and requirement, the controlling philosophy of the capacitor banks will differ. Generally, as mentioned earlier, the distribution capacitor banks are controlled for local requirements. In many cases the control consists of switches that are opened and closed in a seasonal basis or some other local requirement

Ceylon Electricity Board, also adopting to this general practice of using breaker switched capacitor banks, at present has an installed capacity of about 320Mvar of Breaker switched Capacitor (BSC) banks located at various substations in the system. BSCs' of further 70Mvar is to be added by year 2010 at different new locations. Almost all the capacitor banks in CEB network are connected to the 33kV load bus in the relevant grid except Pannipitiya in which capacitors are connected to the 33kV tertiary winding of the 220 / 132 / 33 kV inter bus transformer. In all locations, the control philosophy of the switching of the BSC units is based on the power factor regulation at 33kV transformer incoming feeder.

The capacitor banks installed at Grid sub station level in CEB are controlled according to the power factor regulation. This philosophy of switching the capacitor banks in grid substations does not either ease the distribution feeder capacity or reduce the feeder losses. If those were expected then the capacitors could have been closer to the loads. However, lagging Var

injection or in other words leading Var consumption at 33kV bus level improves the voltage stability and releases the power transformers at the substation. If the utility expects latter two cases, the switching of the capacitor banks shall be based on reactive power or voltage. In case of voltage, the banks should be switched considering the voltage measurements at the point of interconnection. If release the capacity constraint or minimize losses are concerned, then the capacitors shall fully utilized to minimize drawing var from remote generation. If the utility controls them in indirect way like power factor, then it should check whether the requirements are best met with or the available resources are fully utilized [1].

When analyzing the load profile, the data shows that the system load has an early morning peak, a mid daytime peak and a night time peak. Power factor during morning and night peak gets improved since the rise of load during those periods is mainly lighting loads. The daytime load is mainly commercial and industrial therefore the power factor badly decreases. Voltage at day time mainly decreases due to reactive power and at night peak, due to IZ drop further to reactive power. Voltage improves in mid night till early morning but considerable reactive base load exists. Power factor improves after around 17.00hrs leaving capacitor banks gradually switching off. Frequent occasions of manual re-closing of banks shut off by the capacitor controllers are also observed and utilization sometimes drops to 50% even before the night peak starts.

CEB's switching criteria of those capacitor banks has not been evaluated in the past. The system has grown up and whether the present switching criteria is economical or not for CEB, is in question. CEB neither has performed such a study nor they have checked the possibilities of maximizing the use of their capacitor banks. It is worth to discuss several factors in this case. When controlled with power factor regulation, there are situations where some of the capacitor banks on the distribution system are kept unused, while having an acute problem of heavy reactive power requirement in transmission system. This happens mostly when power across the company's transmission system does not coincide with load conditions in locations where the capacitor banks are fixed. In some situations, the power factor may be within acceptable limits but the voltages are below the nominal or onload tap changer is forced on higher taps to take care of the voltage. The substation level capacitor bank can directly serve to give voltage support or var support, without depending on power factor regulation which is an indirect measure of voltage or var requirement.

Addition of reactive power at substation level has to be done without violating the system regulations. The voltage rise due to reactive power injection has to be considered. Such a voltages rise at the bus bar at which the capacitors are connected should not violate its' continuous maximum rating. The On-load tap changer (OLTC) current switching capacities have to be considered during negative var transferring conditions. Impacts on voltage distortion and harmonic resonance conditions have to be monitored and they should not beyond the specified limits.

1.2 Objectives

Taking all these into Consideration, the main objective of the research study is to look in to the possibilities of exploiting the maximum utilization of BSC banks already installed in the system without violating the permitted regulations and other technical limitations. In this regard following points will be studied in this study.

- To check the applicability of present switching criteria
- To check the possibility of connecting maximum capacitor banks installed without violating technical constraints

- To check the possibility of optimizing the present switching parameters, if present switching criteria is acceptable.
- To design and propose a suitable switching criteria for the capacitors by means of network simulation and practical implementation.

1.3 Scope of work

- Evaluation of extent of present utilization of capacitor banks by précised data collection using data loggers and using the daily data sheets by selecting a typical substation.
- Studying the technical constraints of,
 - a. Voltage rise at 33kV bus bar due to addition of capacitor banks
 - b. Effects of resonance when adding capacitor banks to bus bars with load harmonics
 - c. Effects to voltage distortion caused by load harmonics, when adding more capacitor banks
 - d. Capability of On Load Tap Changer to handle switching current during back feeding the excess leading reactive power to the system through power transformers

by network modelling and simulating under the relevant operating conditions.

- Considering above technical constraints, identify the possibilities of maximum utilization of the capacitor banks by maintaining them in switched “ON” condition as much as possible during the periods when transmission system needs reactive power.

2.1 Shunt Capacitors

Use of capacitor banks in utility substations as a source of reactive power is not new to electricity transmission and distribution. They are comparatively inexpensive, easy and quick to install, and can be deployed at any location. Therefore, this is one of the most economical way of generating the reactive power requirement and maintaining the voltage stability in power systems in comparison to the other similar devices such as static var compensators, STATCOM devices etc.

Capacitor Banks consist of individual capacitor units where such a unit is a combination of shunt or series set of capacitor elements. Depending on the bank size, those units are again connected in series or parallel to give the required size. In medium and high voltage levels, sizing of the capacitors in parallel combinations in banks generally has to consider the discharge energy through a shorted parallel capacitor in the same group.

These capacitors banks are fixed or switched type according to a local requirement. The switched type capacitor banks give a poor regulation due to step wise connections. Typical applications of capacitor banks at different locations are shown in figure 1.1

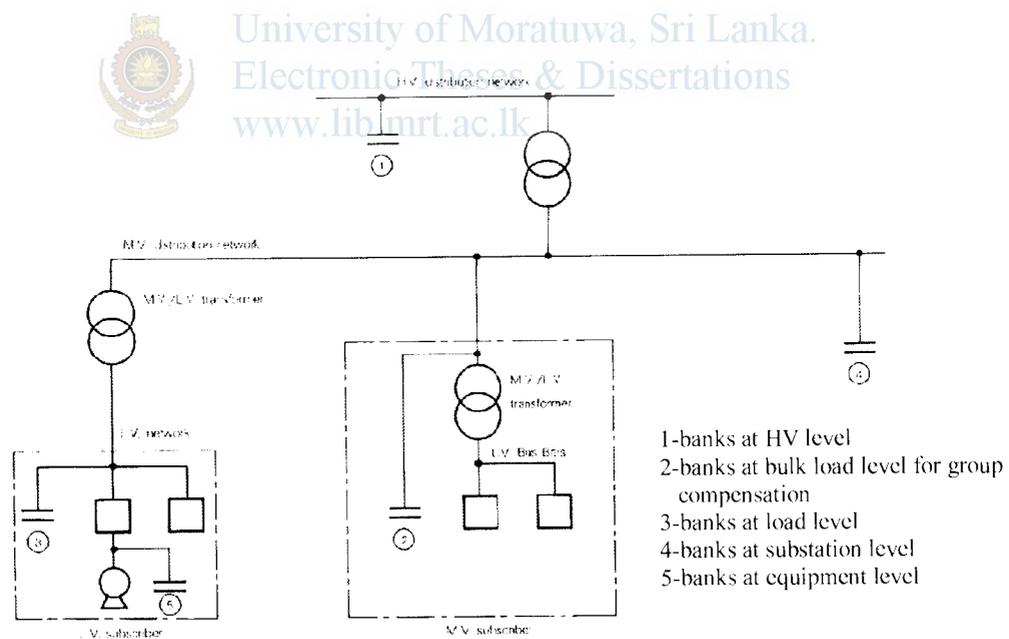


Figure 2.1 : Typical arrangement of Capacitor banks in a utility systems

2.2 Different types of Capacitor banks

Different types of capacitor banks are available in the present market. Metal enclosed type, pad mounted type, stack-rack banks and pole tip mounted types are the most common type in utility applications. Metal enclosed type banks specifically made for indoor installations. Pad

mounted capacitor banks are also enclosed in a metal enclosure and commonly used for areas where accessible to public.

Normally, metal enclosed and pad mounted units come with factory assembled and tested hence the installation is very easy. Those banks significantly reduce unnecessary human interference such as trespassing and tampering. They do not need a fence around it. However their initial cost is high compared to other types and only available up to a certain voltage level.

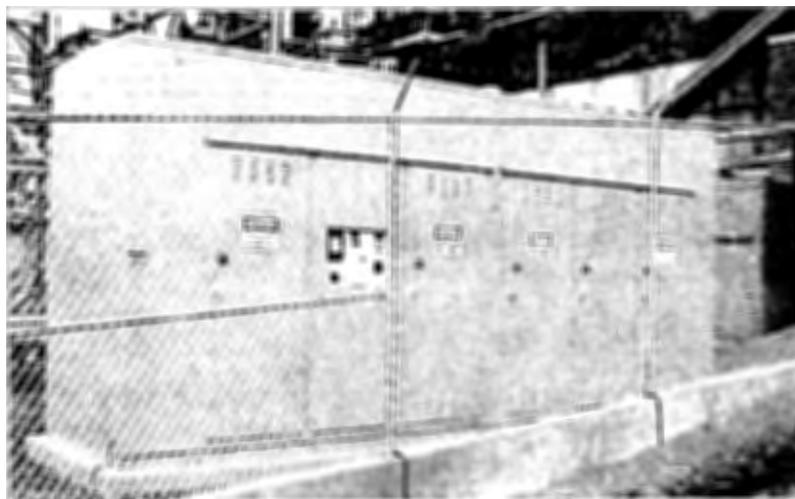


Figure 2.2: Typical pad mounted Capacitor bank



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Stack rack capacitor banks are commonly used in the utility sub stations. The initial cost of these is comparatively low and all components are visible. The components are easily replaceable and also easily expandable.



Figure 2.3: Typical stacked rack Capacitor bank

The pole tip mounted banks are commonly used in distribution networks for improving the voltage profile in distribution lines. Those are available as smaller banks and eliminate the need for space. The maintenance and component replacement is little difficult.



Figure 2.4: Typical pole tip mounted Capacitor bank

2.3 Controlling philosophy

The switching of the breaker switched capacitor banks in utility substations depends on the local requirements of each utility. Basic need for such a control is to regulate the bus voltage, reduce the losses in lines and transformers and to avoid system constraints. Depending on those, the controlling parameter may be different, and may be one of such as voltage, Var, time, temperature or power factor. Some of these parameters are directly represent the system parameters but some, for example power factor can be used as an indirect measurement for var, losses etc.,



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2.3.1. Temperature control

This is not a true indicator of the system status and an indirect measure only. The control effectiveness depends on how well the load characteristics are known. Not useful in cases where those characteristics change often. Temperature control does not require any current sensors.

2.3.2. Time control

Some what better parameter for controlling and has to be based on load characteristics. Ever changing characteristics of the system load profiles does not allow the optimal controlling when time based control is used. Time control does not require any current sensors. Both time and temperature controlling need only simple and inexpensive controllers.

2.3.3. Current control

Current control is not an efficient control because it responds to total line current, and assumptions must be made about the load power factor. Current controls require current sensors.

2.3.4. Power factor control

The power factor is an indirect measure of the var load or the line or transformer losses of the system and always depends on the real power at the time of measurement. For

same power factor, the actual amount of var load depends or changes according to the real power. These measurements require both current and voltage sensors. Generally, power factor regulation or control is advisable for bulk consumer loads, to avoid low load power factors which are penalized by the utility companies. Power factor improvement by capacitor banks in the substation does not reduce the distribution line losses and neither eliminates distribution line constraints. It will release the transformer capacities, reduces transmission line losses and improve the voltage profile. However power factor is an indirect measure for all these. Therefore, power factor controlled capacitor banks may not be fully utilized in most of the time unless the setting parameters are carefully assessed.

We have noticed occasions where the capacitor banks kept unconnected due to power factor being within limits while the loads consume reactive power than minimum switchable steps. Specially, during low voltage profiles, since the power factor does not consider bus voltage, the capacitors may be kept unconnected if power factor is within the limits. This means that as a result of the switched capacitors they will reduce transmission line losses and improves the bus voltage, but power factor is not a measure of the need for the above. So, for a utility substation, power factor correction is not the best control criteria for switching.

The above will be explained using system data in a later chapter.

2.3.5. Var control

Var control is the natural means to control capacitors because the latter adds a fixed amount of leading Var to the system regardless of other conditions, and loss reduction depends only on reactive current. Since reactive current at any point along a feeder is affected by downstream capacitor banks, this kind of control is susceptible to interaction with downstream banks. In a system like CEB, there are no switchable capacitor banks along the distribution feeder so that this problem will not arise. However, in multiple capacitor feeders, the furthest downstream banks should go on-line first and off-line last. Var controls require current sensors and typically costly.

2.3.6. Voltage control

Voltage control is used to regulate voltage profiles on the bus on which the capacitors are connected to. However, while doing this it may not consider the reduction of system losses since lagging or leading low power factors always increase the currents through its components. Voltage control requires no current sensors.

Considering above parameters for switching the capacitor banks, we can define two concepts of control philosophies. First is single variable switching that considers only one measuring parameter. Second concept is multi variable and Boolean switching. In the latter case multiple parameters are measured and the decision for switching is done depending on the optimal situation considering both parameters. The fact we have to consider is the cost of the controllers.

2.4 CEB's Present Configuration

Ceylon Electricity Board has installed number of breaker switched capacitor banks in various Grid substations in the system. All of such capacitor banks are installed at the 33kV level and there are no capacitor banks at the transmission level. The reason for this is due to lower costs

at low voltage levels than at higher voltage levels. The selection of locations has been done considering the system planning studies done by CEB and considering the voltage, MW and Mvar profiles at different locations in the system.

Generally, the banks are equipped with the inrush limiting reactors as well as detuning reactors in most cases. However there are banks without those reactors as well. The banks with detuning reactor are called as the filter banks because they are meant for eliminating the switching inrush, reduce resonance effects and to filter 5th harmonics in the system loads. The other banks are sometimes having inrush limiting reactors and sometimes there are no such reactors.

In the present system, the typical step size of each bank is 5Mvar. This may slightly change with the presence of the reactor. The Total capacity is changing from 10Mvar to 30Mvar. In Pannipitiya, the bank size is 100 Mvar and therefore 4 x 5 and 4 x 20 banks are available. In Athurugiriya 2 x 10 Mvar banks are available. The *Appendix 1(a)* gives the details of capacitor banks available in the CEB system.

CEB's general concept in fixing the capacitor banks is such that it uses symmetrical banks for each bus section in the 33kV bus sections. Each bus section has an individual controller to switch the particular bank. This arrangement has been changed in some of the substations later due to new additions of transformers. *Appendix 1(b)* shows the arrangement of capacitor banks in Panadura grid substation. *Appendix 1(c)* shows the CEB transmission network with the connected capacitor banks. The figure 2.5 shows one 5Mvar stacked rack type capacitor bank at Panadura GSS.



Figure 2.5: 5Mvar stacked rack type capacitor bank – Panadura Grid sub station

For switching the capacitor banks, each bus section has an individual controller. This works as an independent controller when the bus section is open and if the bus section is in ON position, the set of controllers are arranged to work one as a master and the other as a slave. In independent operation, the controller switches the banks assigned to it, typically two. First is always the filter bank and compensator bank later. In the master slave mode, the master will control all the banks if the communication between the controllers is established. If not each controller becomes independent. In master slave mode, the banks assigned to slave are identified by the master and those units are switched once the master's own banks are switched ON.

The switching criteria used in CEB are the power factor regulation. The controller evaluates the power factor of the 33kV transformer incomer feeder using voltage and current analogue signals and switches the first filter bank when the power factor is below a certain specified limit. Generally, this limit is 0.9800. The next banks are switched on as per the same condition considering the calculated power factor. Since there are two types of controllers in CEB system switching off schemes is different. One type of controller switches off the banks when the power factor becomes leading 0.98. The other type does not use power factor for switching off. It calculates the reactive power calculated with power factor and the real power at the time of measurement and reactive power with the set power factor and real power at the time of measurement. If the $((1+\text{hysteresis})^* \text{ difference})$ is greater than minimum step of the banks, then a bank is switched off. Therefore in this kind of controllers, the switching off is depends on a reactive power limit.

During all this period, the automatic voltage regulator of the transformer stays reacting independently to adjust the LV side bus voltage. There is no coordination between the cap bank controller and voltage regulator.



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3. Problems Due to Capacitor banks in Substations

Although the capacitor banks are used by many of the utilities in their substations for local requirements such as var control, power factor control, or voltage control, the presence of same creates considerable operational difficulties in the network. However those difficulties are not reasons for any utility to refrain from using them because their application gives more benefits than those difficulties. Although, a complete removal of them from the network is difficult, there are ways to minimize them. The cost involved in minimizing them can be justified with the savings.

Switching transients (voltage & current), harmonic resonance and increasing voltage distortion at the point they are connected, are the most important factors to be discussed [2]. Switching of the capacitor banks into and out of the system creates heavy switching transients. The inherent quality of energy storing in the capacitors and inductors is the main reason causing the oscillation in both voltage and currents in the system.

The other problems are created by the harmonics in the system loads. The harmonic currents containing multiple frequency current components force the RLC networks to resonate at certain frequencies creating unusual high currents through its components.

Voltage distortion is an impact going together with the resonance. The high currents drawn at resonance create heavily distorted voltage at all levels in the system. Even for very insignificant harmonic current levels, the resulting distortion in the system is very high. Distorted system voltages create severe mal functionalities causing adverse effects and therefore, are not acceptable by the standards and regulations.

3.1 Switching inrush

Switching of capacitors in power networks that consists of capacitive, inductive and resistive components creates oscillator transients in the system. In such cases, the redistribution of energy associated with circuit components take place to meet new system conditions. Since such redistribution of energy in inductors and capacitors does not happen instantaneously, this will lead to oscillatory transients until the situation damps to the steady condition.

Capacitors switching transients are experienced during single bank switching (energization inrush), back to back switching and during switching off. The transients are also caused during the faults in other feeders while capacitors are in service in the substations (out rush transients).

The presence of such transients were studied during the simulations of the models developed using PSCAD. With the theoretical calculation for single bank switching condition as given below, acceptance of developed model for such a transient study can be justified.

The figure 3.1 below shows a simplified model of a single bank capacitor switching where it represents an equivalent voltage source and reactance. By considering the typical circuit notations, the above simplified circuit was analyzed as follows [3].

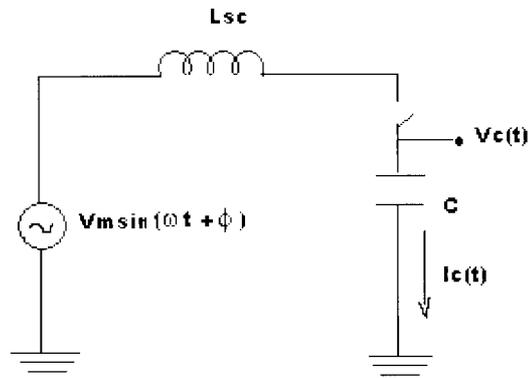


Figure: 3.1 Model for a single bank switching

Assuming that X_{sc} is much less than X_c , the steady state voltage across C can be approximated to $V_m \sin(\omega t + \Phi)$. But capacitor voltage can not be changed instantaneously therefore there must be a transient oscillation term to adjust the initial condition voltage across C . This voltage across C is given as

$$V_c(t) = V_m \sin(\omega t + \Phi) - (V_m \sin \Phi) \cos \omega_0 t$$

The associated current will be

$$I_c(t) = \omega C V_m \cos(\omega t + \Phi) + (V_m \sin \Phi) \sqrt{C/L_{sc}} \sin \omega_0 t \quad \text{where } 1/\sqrt{L_{sc}C} = \omega_0$$

It can be shown that the maximum value of inrush current for switching at voltage maximum can be approximated to

$$I_{rated} (X_c/X_{sc})^{0.5}$$

Where I_{rated} is the rated rms current of capacitor, X_{sc} is the short circuit reactance at the point of application of capacitor

With these approximations, for the selected substation having,

- $X_{sc} = 5.1 \text{ohm}$ (equivalent source impedance)
- Inrush reactor of 0.003H ($\approx 0.9425 \Omega$)
- Two transformers in parallel ($\approx .05 \text{pu}$)
- Single capacitor bank of $14.6 \mu\text{F}$ ($\approx 218.02 \Omega$),

the per unit representation will calculate the maximum rms switching current at point of maximum voltage can be high as eight times.

The figure 3.2 and 3.3 shows the simulation results for same conditions with no load connected and gives same results as calculations.

The sub station selected for the case study was modelled with PSCAD gives the following transient results for current and voltages for single bank switching. This was simulated under

no load conditions and for breaker closing at a voltage peak point. The peak switching currents peak steady current ratio is around 10.

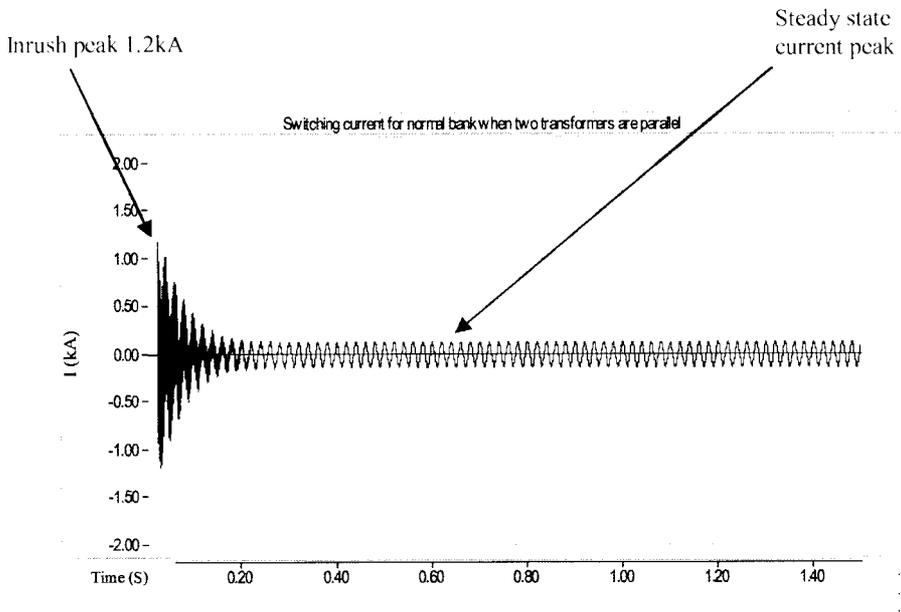


Figure: 3.2 Inrush current in normal bank switching - Panadura GSS Simulation results

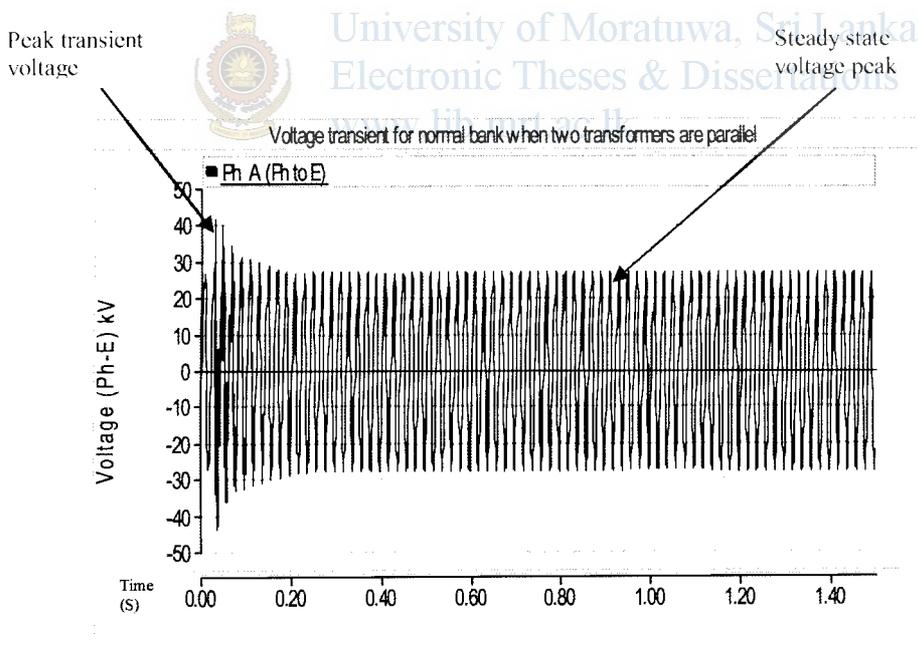


Figure: 3.3 Voltage transient - Normal bank switching - Panadura GSS Simulation results

The figure 3.3 shows the voltage transient during the same single bank switching instance and we can see that it goes about to two times the steady state voltage peak.

In general, the degree of the transient may rise even up to 2.0p.u in voltage and 10p.u. in the current [4]. The frequencies of these transients are in the order of 200 to 800Hz. The extent of the transient depends on the fault level of the location of the capacitor, system impedance, capacitance of the capacitor etc. These conditions are clearly visible in the selected substation.

Interestingly, the switching of the filter bank with a comparatively large reactor reduces these impacts. The figures 3.4 & 3.5 show the switching transients of filter bank switching.

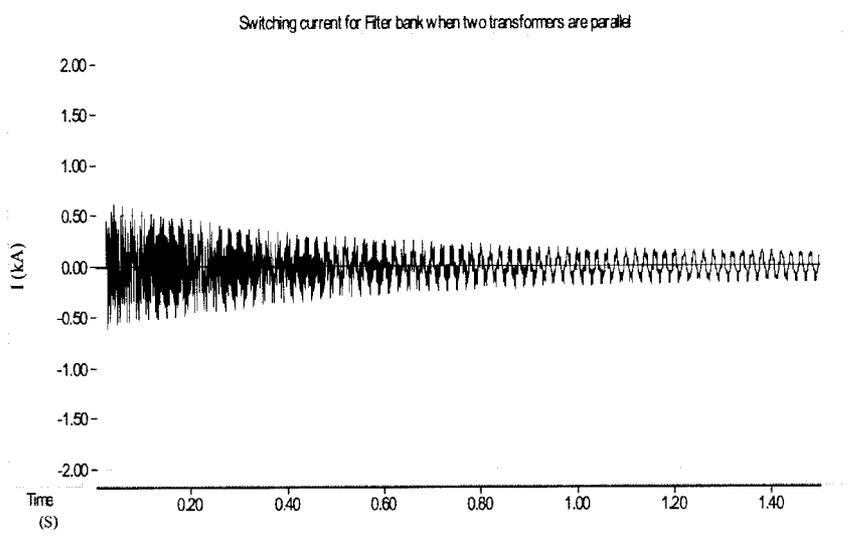


Figure: 3.4 Inrush current in filter bank switching - Panadura GSS -Simulation results

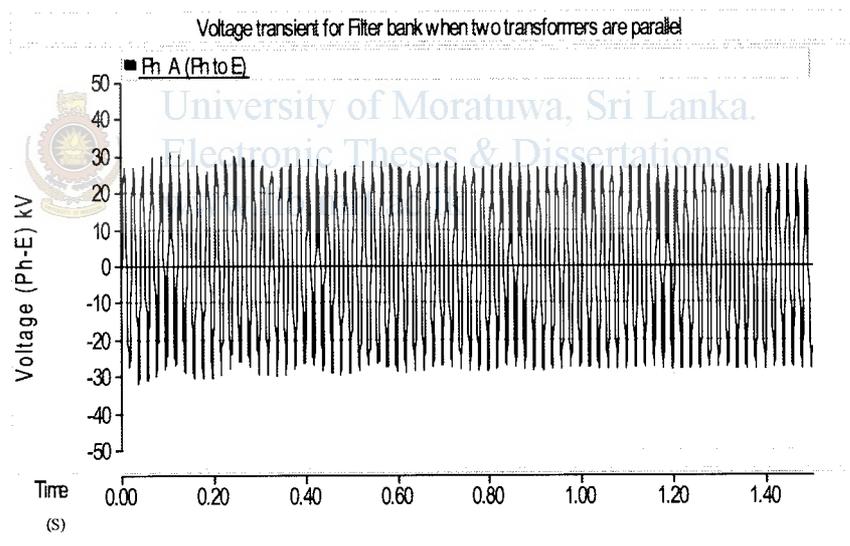


Figure: 3.5 Voltage transient in filter bank switching - Panadura GSS -Simulation results

The above figures indicate that the rise in voltage and current are considerably reduced due to the large inductor. This again raises a question to think about the suitability of the inrush reactor in the normal bank. However CEB has a practice to first switch the filter banks so that the impacts are less.

Back to back switching is the incident where a capacitor bank is energized with already energized capacitor bank. The above mentioned problem of high valued high frequency inrush current and over voltages is made more severe by the presence of already energized parallel banks. The similar conditions are modelled and simulated in the PSCAD model and results are shown in figures 3.6 and 3.7

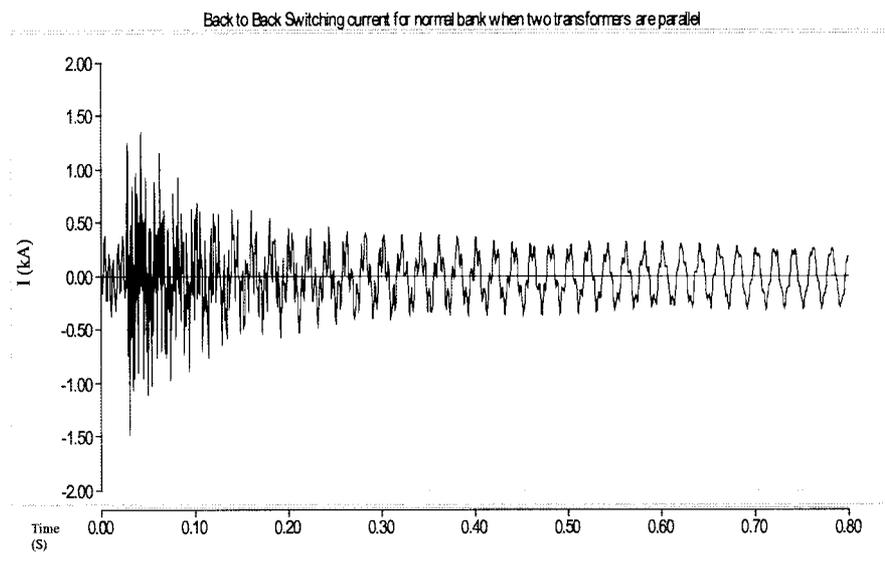


Figure: 3.6 Inrush current in back to back switching - Panadura GSS -Simulation results

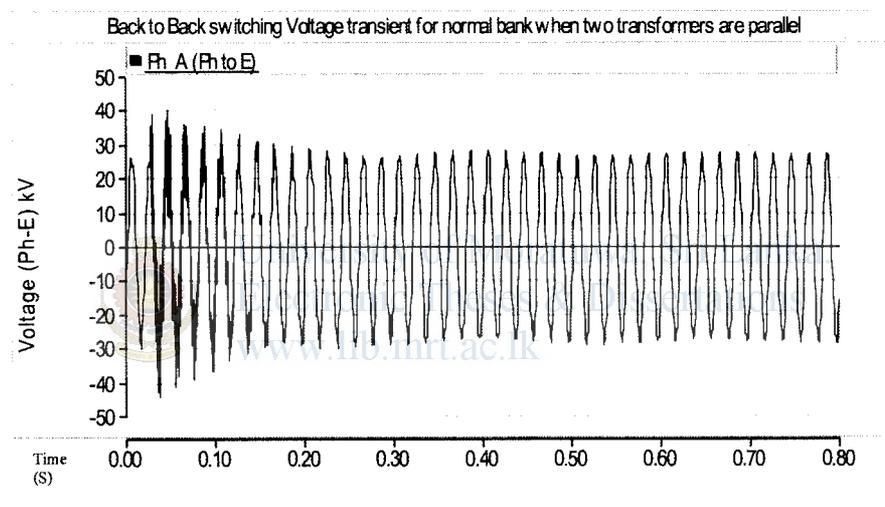


Figure: 3.7 Voltage transients in back to back switching - Panadura GSS -Simulation results

All these transients with high amplitudes and frequencies adversely affect the life of the breakers and capacitors. The short time rating of breaker as well as the capacitor must be sufficient to withstand this high frequency inrush current which may last for several a.c. cycle. High frequency current flow in capacitor causes considerable thermal overloading in the capacitors. Also, an examination of the voltage equation will show that, in the extreme case of voltage maximum switching, the instantaneous voltage of the capacitor may reach a maximum of $2xV_m$ in the first few a.c. cycles. This will lead to severe strain on the capacitor dielectric leading to a loss of life of the capacitor.

The switching transients also can interfere in the others parts of the network and may cause insulation damages, equipment damages, mal tripping of protection relays, metering errors, tripping of equipment etc.

The switching off transients, specially the voltages across the breaker tips also harmful to the breakers and capacitors. The PSCAD model was run to see theses effects as well and the figure 3.8 indicates how the voltage between breaker tips behaves during the capacitor switching off.

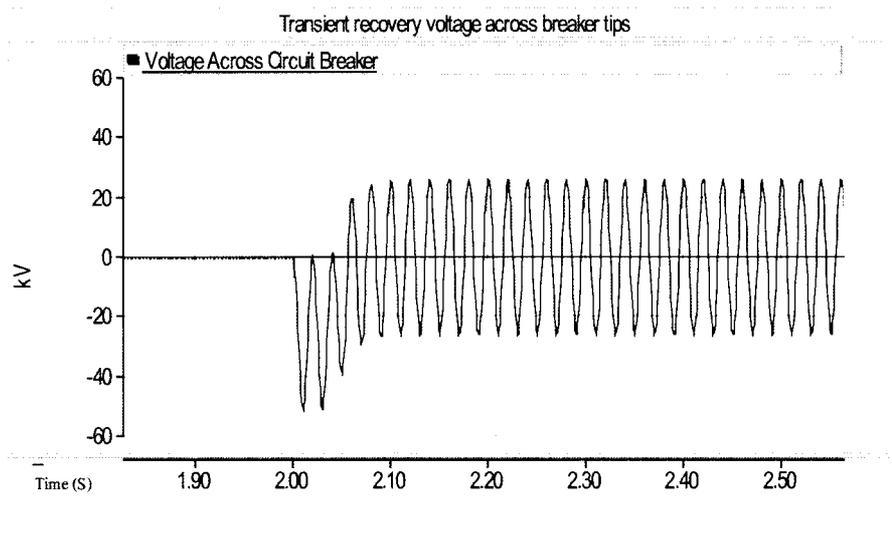


Figure: 3.8 Voltage transients across CB during bank opening - Panadura GSS -Simulation results

High frequency transients occur during the switching causes problems for circuit breakers. Specially, SF6 circuit breakers have considerable impact due to this. Then, during capacitive switching, high voltage possible up to $2.0p.u$ may appear between the pole tips of the circuit breaker. This may cause restrike if the breaker cannot bare such a high transient recovery voltage.

All these effects in transients suggest that the regular or frequent switching of capacitor banks is a problem to the network. In CEB system, in some of the banks, inrush limiting reactors are fixed but in some cases it is not available. Therefore, it is better if the network can be operated with minimum switching operations of the capacitor banks.

3.2 Harmonic resonance

Presence of non-linear load that takes non-sinusoidal current from a sinusoidal supply voltage creates multiple frequency current components in the system loads. Hence, the power network currents can contain harmonics if there are nonlinear loads. Loads like saturated transformers and machines, welding units, arc furnaces, rectifier and inverter units, battery charging equipment, thyristor controlled power converters and motor control equipment, static VAR compensators etc. are nonlinear and introduce harmonic currents into the distribution network and elsewhere.

These harmonic currents flowing in the line impedance produces harmonic voltages along with the fundamental frequency voltage at all points in the system. The effects due to tooth ripple in generation & machines, variation in air gap reluctance in a synchronous machine, non-sinusoidal flux distribution in a generator, magnetizing inrush of transformers etc. also create harmonic voltages in the system [5] [6].

Irrespective to the reasons for them to be present, when they are close or equal to the order of the resonant frequency of the network, then large harmonic currents may be circulated between the supply and the capacitor equipment. These currents as same as switching transients will do the same adverse effects to the equipment. The figure 3.9 shows a typical frequency scan plot obtained by the PSCAD model run for two different cap bank transformer configurations in Panadura substation.

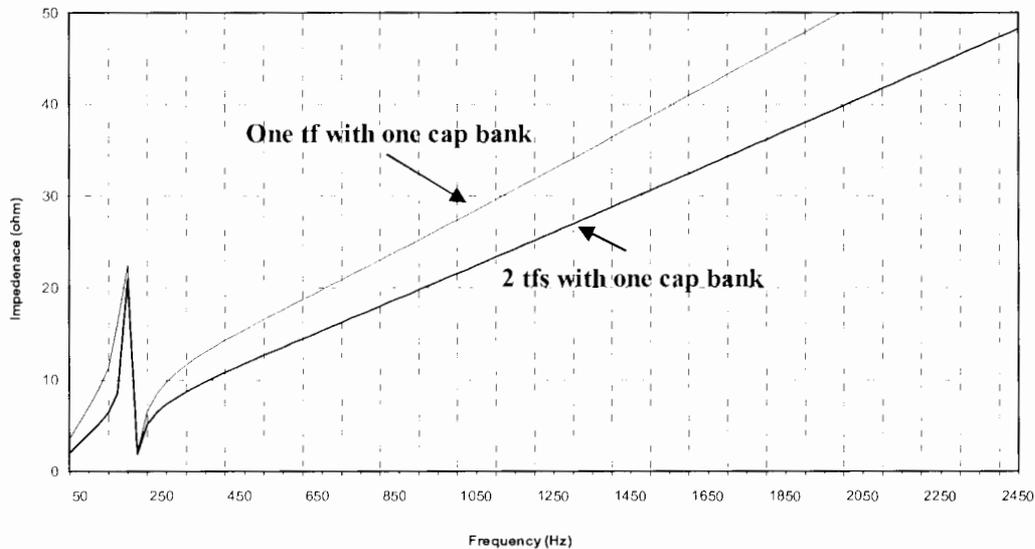


Figure: 3.9 Frequency scan obtained from PSCAD model for panadura GSS

The figure clearly shows two distinct frequencies where one shows very high impedance and the other like short circuit. Most system loads have harmonics closer to those frequencies.

3.3 Voltage distortion

The other severe problem due to these harmonics is the voltage distortion at the point where the capacitors are connected. As in figure 3.9 at the frequencies at which high impedances are formed, very smaller harmonic current can introduce a high harmonic voltage. The result is such that it distorts the system voltage at the point of capacitor connection.

Voltage distortion beyond critical limits will create unnecessary malfunctions in the system. Where the capacitors are concerned, the high voltage harmonics over stress the insulation of capacitors and may cause even blowing them. To avoid such occurrences when installing capacitor banks in sub stations, the utility has to invest on the detuning reactors as well. The selection of those reactors should be done following a study of the real system.

The figure 3.10 shows an example for measured distortion levels at Panadura GSS for station maximum load with all banks are in ON position, under worst harmonic level content of the substation (For around 16% THD). The initial high distortion is due to voltage source time constant in the simulation and no need to consider.

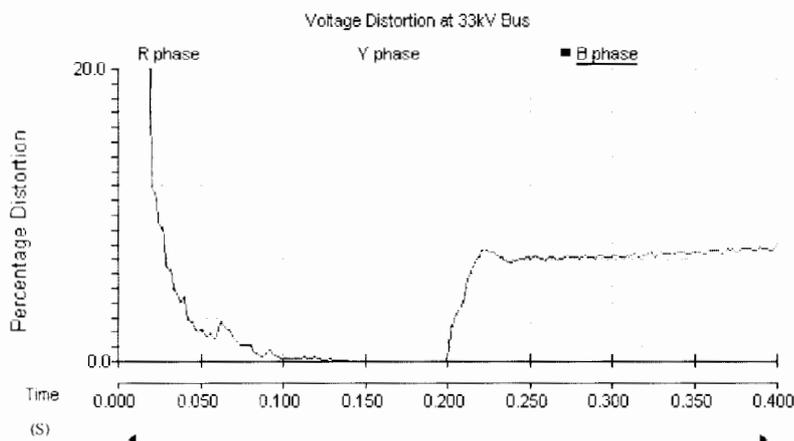


Figure: 3.10 Voltage distortion at 16% I_{THD} at maximum average load and with all banks in ON position

4. Case Study for Panaduara Substation

A detailed study on the real system behaviour is an important necessity, not only in deciding a policy to switch the capacitor banks in a system but also to evaluate and existing such criteria. However, studying the total system is practically impossible in a live system. There are lots of operational difficulties for precise data collection and measurements in an operating system. However, a case study is a sufficient and satisfactory solution for a research like this. Such a sample study has to be selected to represent the total system as a whole. On the other hand, the duration during which the data collection and measurements is done, shall cover a substantial duration to represent the actual system variations. The general practice of such a study is to have one week duration.

Considering the data available in the system control centre of CEB, and taking the fact of locating amidst balanced domestic and industrial load area, Panadura grid substation was selected as a pilot station and the research was based on the findings for the selected sub station. Factors like convenience in fixing equipment, flexibilities in supervision etc, made the selection further easy. The load curves both real and reactive, were compared with the system behaviour and found satisfactorily matching and representing the system as a whole.

4.1 Substation details

Sub station capacity	- 2 x 31.5 transformers
Incoming feeders	- Connected to Pannipitiya Matugama lines as a T connection / Double circuit connection
No of feeders	- 6
No of capacitor banks	- 4 x 5 Mvar
Maximum average night peak	- 46MW +27Mvar
Minimum average load	- 19MW +12Mvar

4.2 Measurements & Collection of sub station data

Two on line data loggers were used for recording data, one at 33kV voltage level and other at 132kV level. Following inputs were connected and recorded, firstly for 8 days cycle without connecting the capacitor banks. The recorded data is as per the format given in *Appendix 2(a)*.

33kV side measurements	- MW and Mvar - 33kV bus voltage - Power factor at 33kV incomer –Transformer 1 - Tap position of on load tap changer – transformer 1 - Load side harmonics
------------------------	--

132 kV side measurements	- MW & Mvar - Power Factor at 132kV bus bar - 132kV bus voltage
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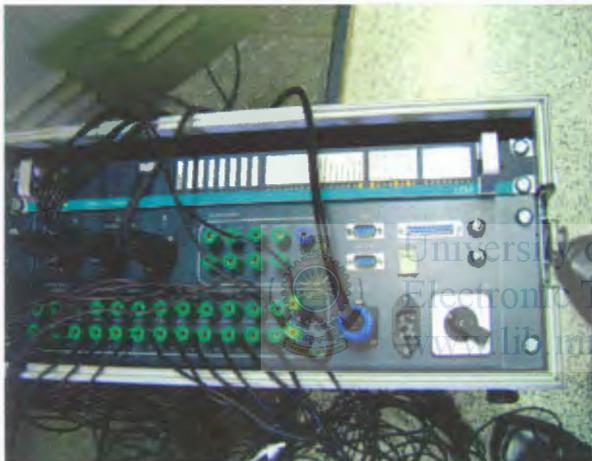
Measured data was fed and simulated to a model of the substation developed with PSCAD which is the simulation software used for the network simulations. Following the results of these simulations, the same measurements were done with all four capacitor banks forcibly connected to the system.

4.3 Measuring devices and data loggers

The following standard data logging equipment with their sensing equipment were used in measuring and recording the data.

- LEM Qwave Primium - power quality Analyzer
- Ellite 4 – Pholyphase power meter

Figure 4.1 and 4.2 shows the equipment and their sensing devices used for the data logging and recording.



4.1 (a) Ben analyzer connected for 33kV measurements



4.1 (b) Analyzer connected for 132kV measurements



4.1 (c) Sensing equipment

Figure: 4.1 Data recording equipment



Figure: 4.2 Sensing equipment (Contd)

4.4 Behaviour of the power factor in the system

The figures 4.3(a) and 4.3(b) show the pattern of the power factor in the substation load. It shows a regular daily pattern with two peaks. One peak can be observed around 6.00hrs in the morning, during the morning load peak. The second peak is during the night peak time.

Power factor - Panaduara GSS (20th to 28th Jan 2009)
Measured at 33kV bus and 132 kV bus

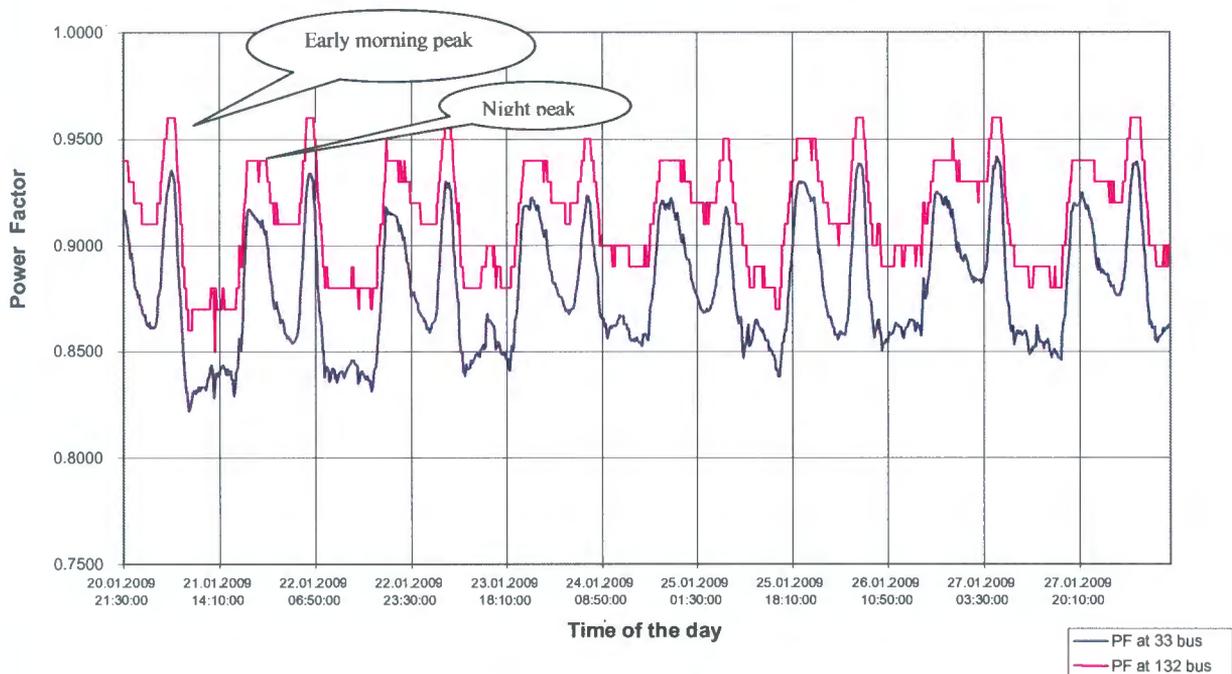


Figure: 4.3(a) Pattern of the power factor measured at 33kv & 132kv levels over total measurement period

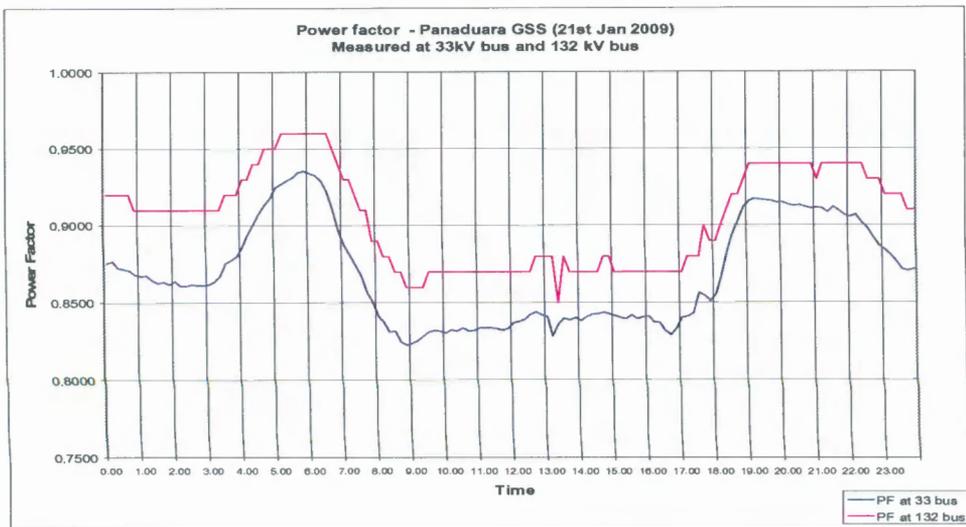


Figure: 4.3(b) Pattern of the power factor measured at 33kv & 132kv levels on 21st January 2009

The power factor improves during these two time slots because the load rises are mainly due to lighting loads. This can be further clarified by observing the real and reactive power consumptions. Obviously, during these periods, there must be a voltage drop due to rise in loads. The figures 4.4(a) and 4.4(b) shows this clearly. We cannot see such a drop in 33kV voltage level because of the AVR which regulates the secondary side voltage.


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 Comparison of 132kV Voltage & Phase angle measured at 33 bus
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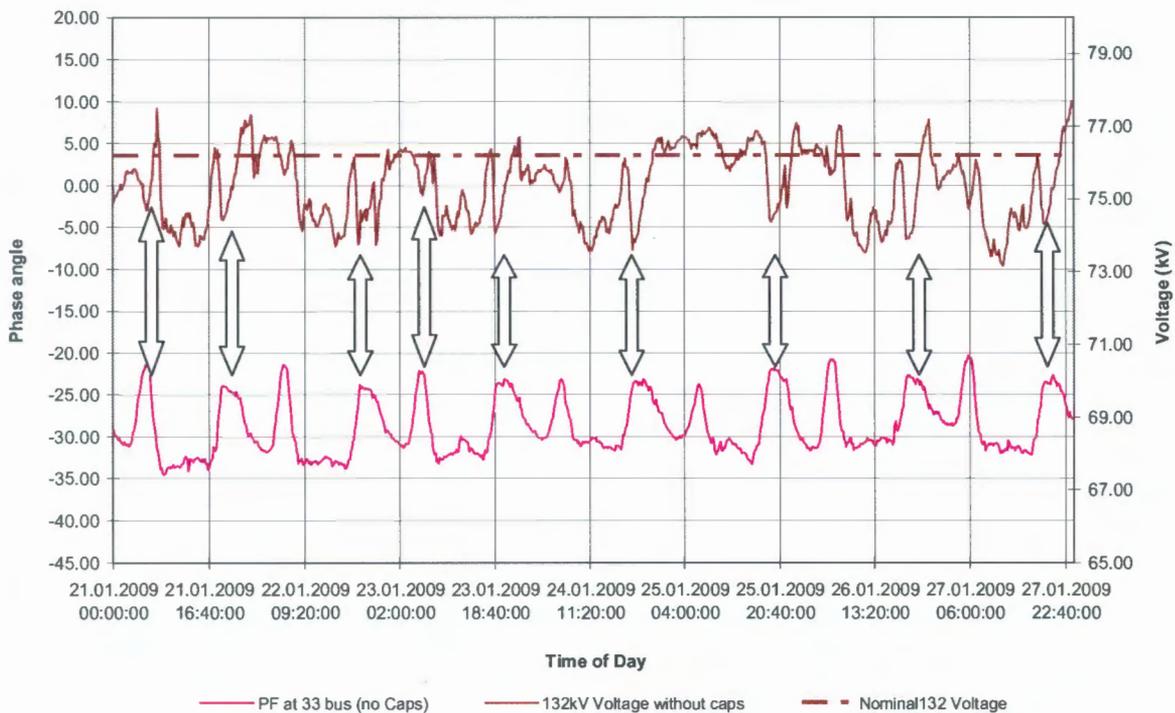


Figure: 4.4(a) Comparison of 132kV voltage and power factor over total measurement period

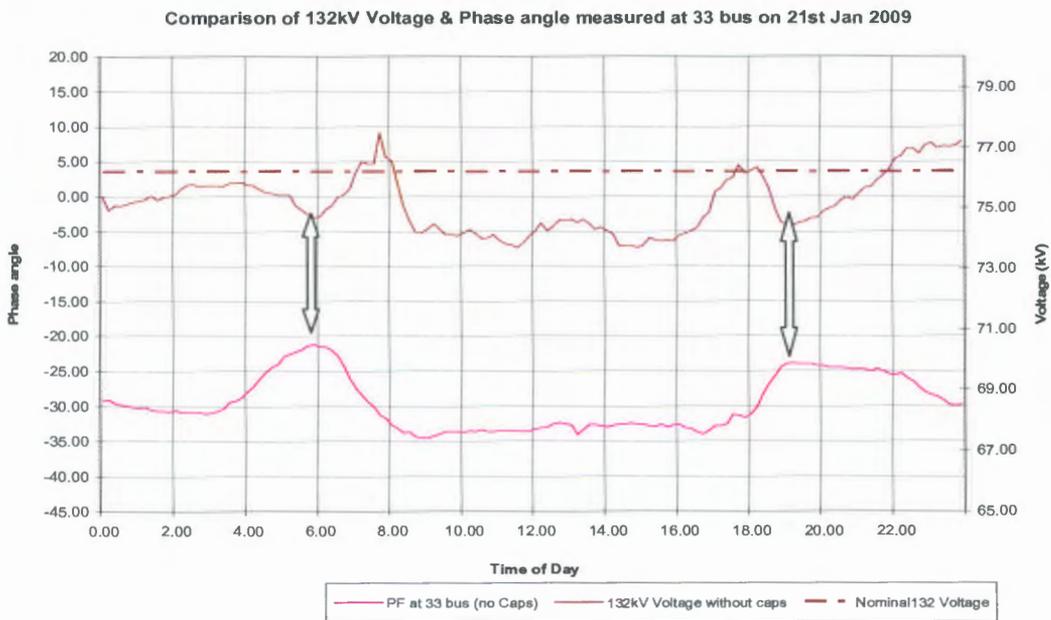


Figure: 4.4(b) Comparison of 132kV voltage and power factor on 21st January 2009

As the figures 4.4(a) & 4.4(b) suggest, the voltage drops during day time is mainly due to heavy reactive load and due to lighting load during night and morning. For a switching criteria based on power factor, contradictorily power factor goes high during night and morning peaks causing tendency to switch off the capacitors but bus voltage goes down. Due to this, we may deliberately ignore a possibility of improving bus voltage due to gradual disconnecting capacitor banks during night peak or delay in picking up the banks in the morning. In other words, either it is possible to keep some capacitor banks for extended time or some banks can be connected bit earlier.

During day time the both real and reactive loads are high so that voltage drops again due to this. The low power factor initiates to switch on the capacitors. As the figure 4.3, 4.4 and 4.5 reveal, power factor stabilizes during day time and show a flat profile. This means that the real and reactive loads have similar slopes during up and down. The point that has to consider is that if the first come banks correct the power factor then the others will not come even if there is a possibility of compensating more reactive power.

Not only that but there is another important feature in the power factor, real and reactive load curves. During mid night, there is another region where the power factor becomes worse and reactive power remains some what constant while real power still dropping down. There is a possibility of over compensating during such a period.

We can see this very clearly in figures 4.6 (a to h) since the graphs are exaggerated on daily basis. The flat regions of the power factor are clearly seen in these figures. Power factor during these flat areas are approximately close but the real and reactive power loads show large variation. Due to these considerations, the switching of capacitor banks based on power factor is not the best criteria for a sub station in CEB system.

Comparison of power factor and Real / Reactive power loads

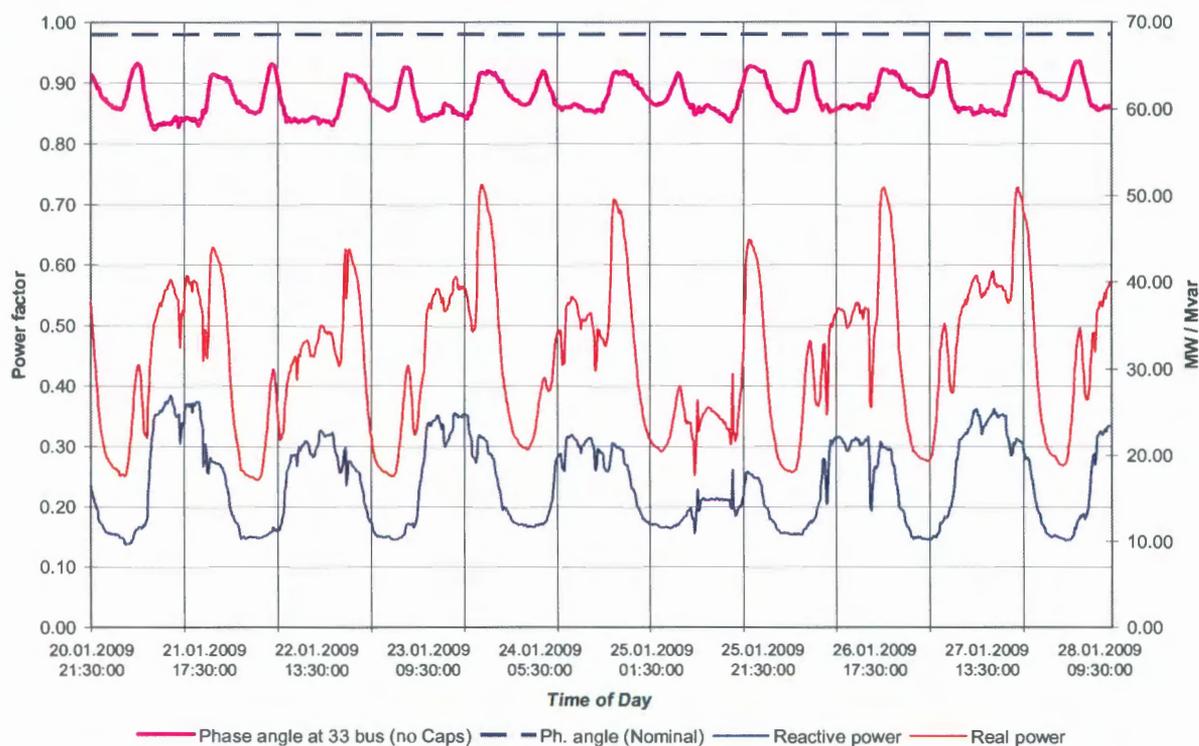


Figure: 4.5 Comparison of real and reactive power with power factor

4.5 Switching pattern of capacitor banks in the Substation

The behaviour of the capacitor banks in the substation with the present switching criteria was observed. The capacitor controller installed at Panadura grid sub station behaves as follows [7].

Controller - POCOS reactive power controller
 Type - RPC-A-064-111-S000 M

Switching ON criteria;
 If measured power factor < 0.98

Switching OFF criteria;
 If $Q_{komp} > Q_{set} * (1 + \text{Hysteresis}/100)$

Where Q_{komp} = Difference in reactive power calculated from the actual $\cos \phi$ value and set $\cos \phi$ value
 Hysteresis = A setting value defined by user (set value 10%)
 Q_{set} = Lowest switchable power

Although the controller at this sub station switch on the banks based on power factor, it does not switch off the banks based on the same principle. Therefore, this kind of controllers installed in CEB system is not purely power factor based control. But in ABB and ASEA capacitor controllers used in some of the other substations, the switching off criteria is also

based on leading power factor. The switching off criteria based on reactive power avoids the hunting of capacitor bank switching and considerably improves the utilization of capacitor banks.

The switching pattern of the capacitor banks for the measuring period based on above criteria is shown in figures 4.6 and 4.7. The figures 4.6 (a) to (g) show the switching pattern in master slave mode (bus coupler closed) while the figures 4.7 (a) to (g) show the same with bus coupler off position (Independent mode).

By observing the switching patterns, the following factors are noticed.

- There is a distinct difference in switching pattern under two operating conditions (at bus coupler ON and OFF)
- In the independent mode, two capacitor banks (one for each transformer) are in ON state through out the day. In the master slave mode, most of the time three capacitors are in ON position.
- In master slave mode, since three banks are ON in the mid night time, i.e. approximately after the night peak time and up to around 6.00 hrs morning, the sub station operates at a high leading power factor.
- In the real situation, during the period with lightly loaded lines especially in mid night, the line capacitances dominate and Ferranti effects causes high voltages at load ends. Addition of capacitors during such a period at substation makes the situation worst. Due to this, the control centre sometimes instructs to switch off the capacitor banks manually. Under such circumstances, due to operational difficulties, the operators switch off the controllers and hence all the banks are switched off. When operating a transmission network, this kind of leading reactive power compensation is also necessary. Although this is an un-economical situation as far as the sub station is considered, it is unavoidable. The situations like this once again prove that the power factor regulation is not the best switching criteria for CEB sub stations.
- Daily switching pattern shows that even at times where all four banks can be switched on, there are occasions where the controller switches only three banks especially during daytime. This may be due to the flat profile of the power factor during such periods. Both real and reactive power increases in the same proportion keeping the power factor unchanged. The controller does not consider reactive power increase if there is no decrease in power factor below limits.
- When the banks are switched off at nights manually to avoid voltage rises and again put into auto mode in the next morning, then the switching pattern disrupts and become even more uneconomical.
- The table 4.1 below shows a segment of data record that shows certain time slots in which only 3 banks are connected but still the other bank also can be connected.

Date & Time	Station Load		1 bank ON		2banks ON		3Banks ON		4 Banks ON	
	MW	Mvar	Unserv ed Var/TF	P.F after banks						
22.01.2009 09:00:00	29.78	19.18	7.09	-0.9029	4.59	-0.9556	2.0908	-0.9903	-0.41	0.9996
22.01.2009 09:10:00	30.43	20.00	7.50	-0.8970	5.00	-0.9500	2.4981	-0.9868	0.00	1.0000
22.01.2009 09:20:00	30.53	19.94	7.47	-0.8982	4.97	-0.9508	2.4725	-0.9871	-0.03	1.0000
22.01.2009 09:30:00	30.71	19.89	7.45	-0.8998	4.95	-0.9518	2.4461	-0.9875	-0.05	1.0000
22.01.2009 09:40:00	31.21	20.00	7.50	-0.9013	5.00	-0.9523	2.4981	-0.9874	0.00	1.0000
22.01.2009 09:50:00	31.34	20.07	7.53	-0.9012	5.03	-0.9521	2.5343	-0.9872	0.03	-1.0000
22.01.2009 10:00:00	30.88	19.84	7.42	-0.9013	4.92	-0.9528	2.4188	-0.9879	-0.08	1.0000
22.01.2009 10:10:00	28.69	18.73	6.86	-0.9021	4.36	-0.9567	1.8643	-0.9917	-0.84	0.9990

3 Banks ON and still 2.53 Mvar /transformer, drawn from source. Therefore if other bank is energized all var requirement can be fed.

Situation if 4th bank is connected

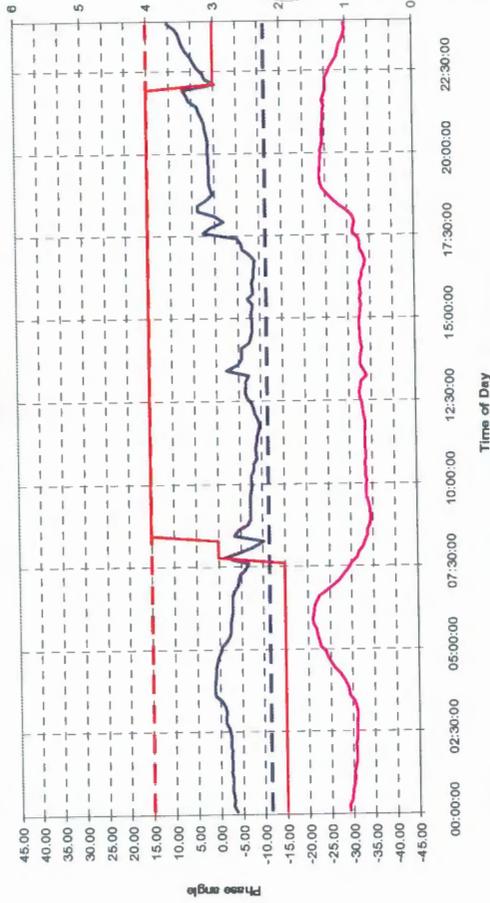
Table: 4.1 Extract from the data measurement

The figure 4.6, shows that there are occasions where the fourth capacitor bank does not automatically operates under power factor regulation control. However, independent mode operation seems to be more economical than master slave mode under same switching control in day time but during night time it may be not. However, the feeder loads are not identical so that the bus coupler has to be kept closed all the time. Therefore, most of the time the capacitor bank controllers are in master slave mode and utilization is not optimized.

4.6 Uncompensated reactive power

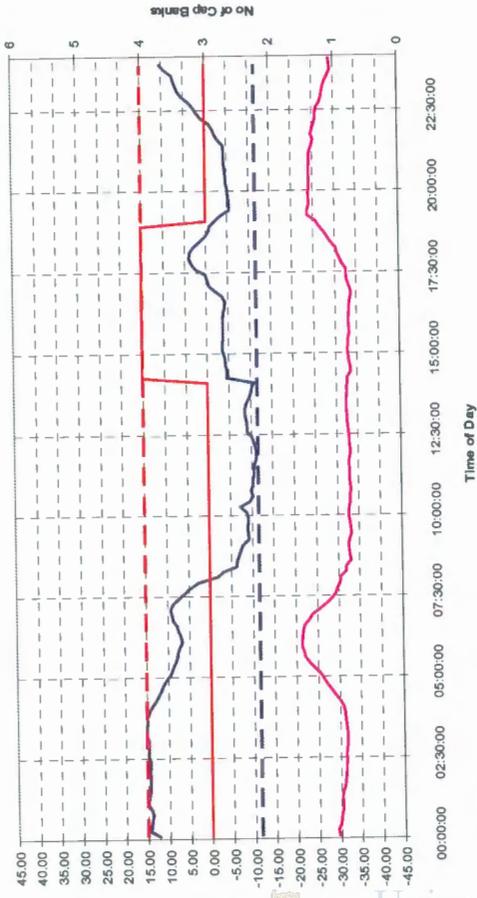
The best operational criterion for the substation is to operate its loads close as possible to unity power factor as far as the losses are concerned. The data measured and recorded shows that there are occasions where reactive loads could be further compensated by the capacitor banks while they are not fully utilized, to minimize line and transformer losses. The breaker switched capacitors operates in steps and hence they give poor regulation. Low power factors whether lagging or leading gives same effects as far as losses are concerned. However, under the conditions where transmission network needs reactive power, operate with leading power factor can be considered.

Utilization of Cap Banks (21.02.09)



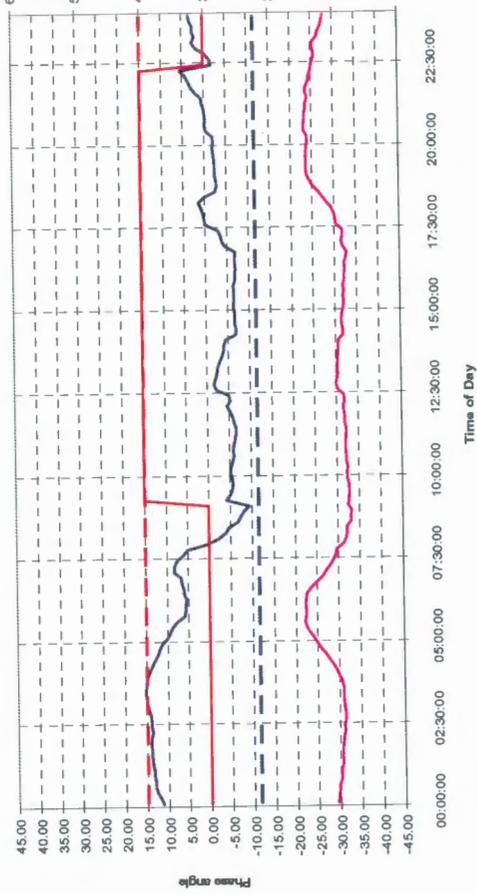
4.6(a)

Utilization of Cap Banks (22.02.09)



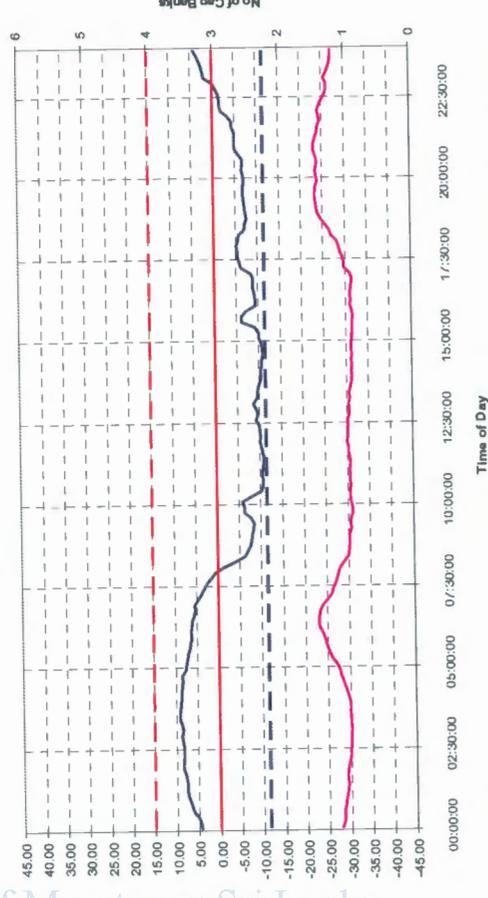
4.6(b)

Utilization of Cap Banks (23.02.09)



4.6(c)

Utilization of Cap Banks (24.02.09)

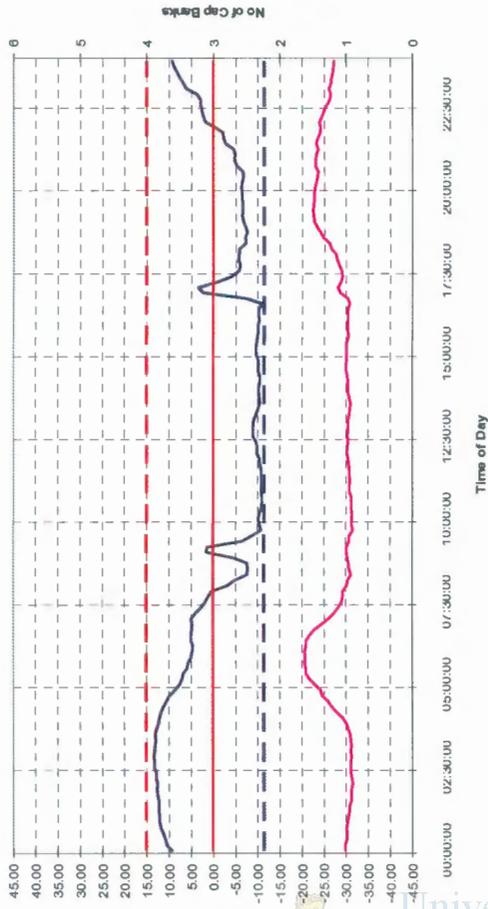


4.6(d)

— Phase Angle at 33 bus (no Caps)
--- Ph. angle (Nominal) of 33 bus
--- No of Cap Banks
--- Phase Angle with Cap Banks (calculated)

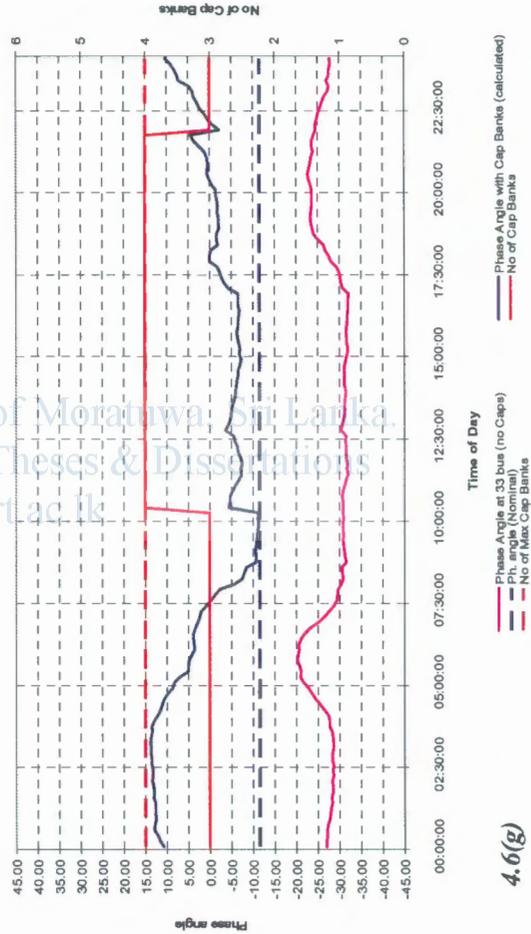
Figure: 4.6(a to d) Utilization of cap banks under master slave mode

Utilization of Cap Banks (26.02.09)



4.6(f)

Utilization of Cap Banks (27.02.09)

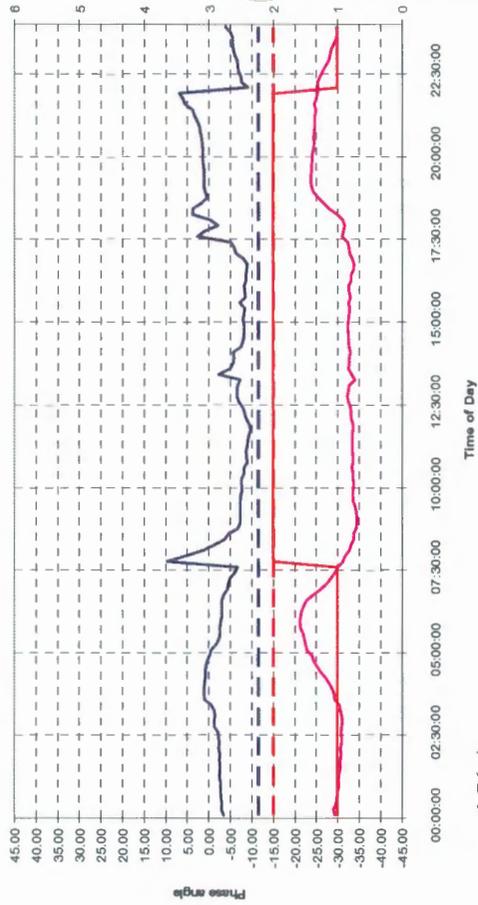


4.6(g)

Figure: 4.6 (e to g) Utilization of cap banks under master slave mode

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Utilization of Cap Banks (21.02.09)



4.7(a)

Utilization of Cap Banks (22.02.09)



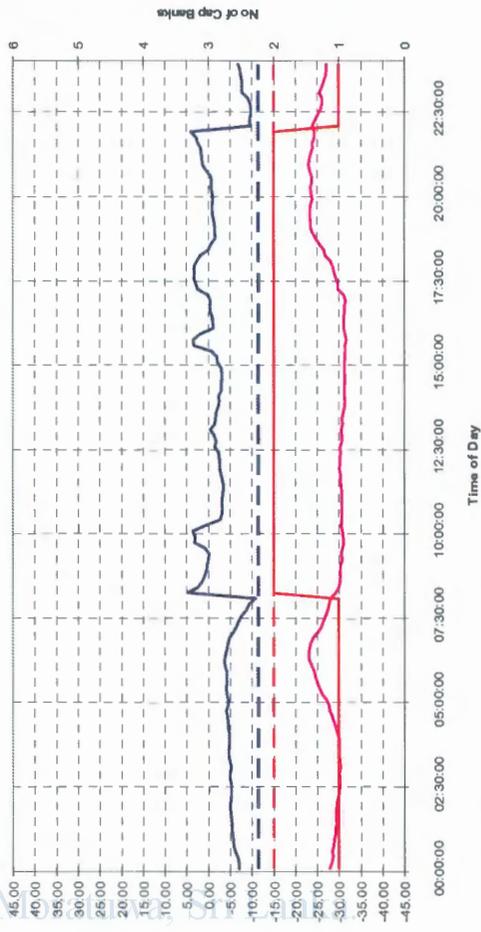
4.7(b)

Utilization of Cap Banks (23.02.09)



4.7(c)

Utilization of Cap Banks (24.02.09)



4.7(d)

Phase Angle at 33 bus (no Cape)
 Ph. angle (Nominal)
 No of Max Cap Banks
 Phase Angle with Cap Banks (calculated)
 No of Cap Banks

Figure: 4.7 (a to d) Utilization of cap banks under independent mode

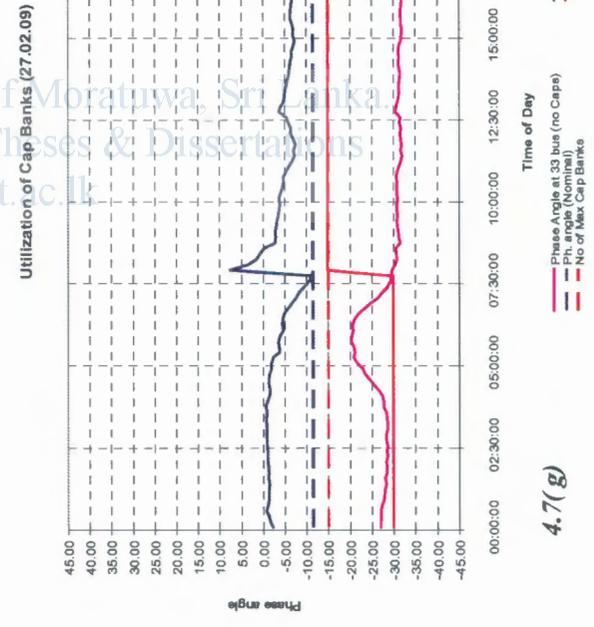
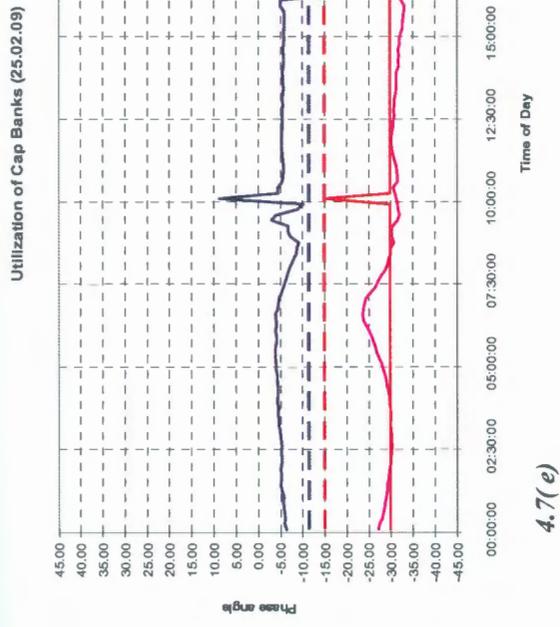
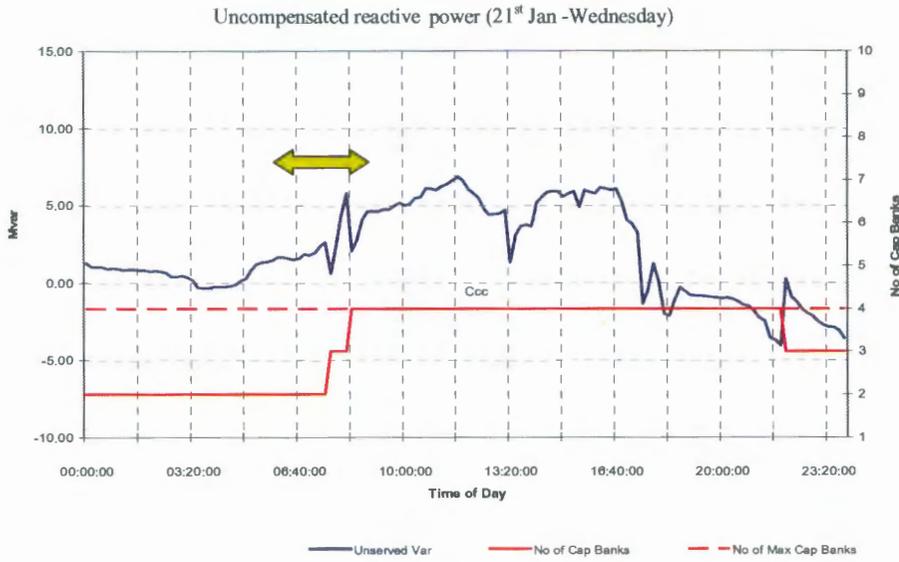


Figure: 4.7 (e to g) Utilization of cap banks under independent mode

The figures 4.8 (a) to (g) shows the reactive power drawn from the system or fed into the system after capacitor banks are connected to the system under present switching criteria. The reactive power drawn from the system at the time, in which capacitors are not fully utilized, is the important matter to be discussed.

In the graphs the negative reactive power means it back feeds reactive power to the network and positive means it draws reactive power from the system.



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 Figure: 4.8 (a) Reactive power flow under present switching criteria in master slave mode 21.01.09
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The yellow arrow shows the area where capacitors are under utilized while the load substantially drawing reactive power from the system.

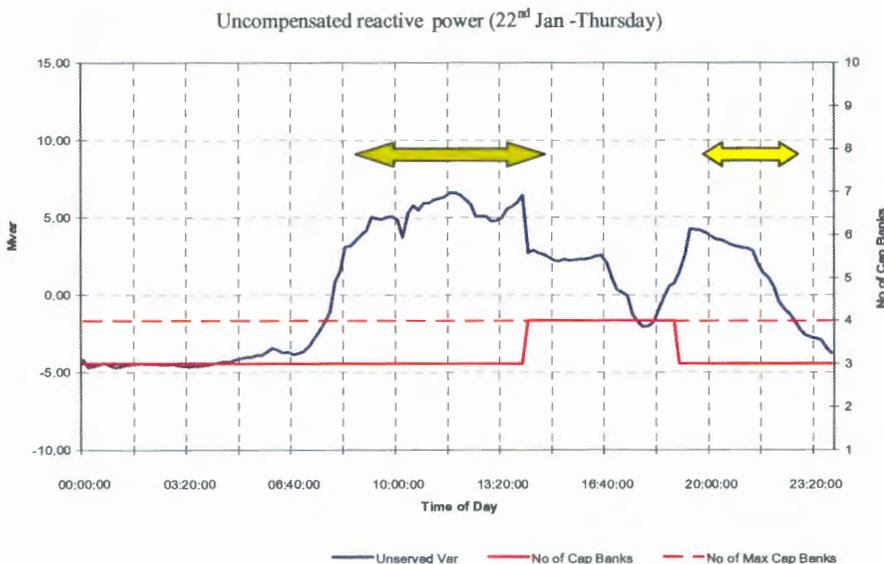


Figure: 4.8 (b) Reactive power flow under present switching criteria in master slave mode 22.01.09

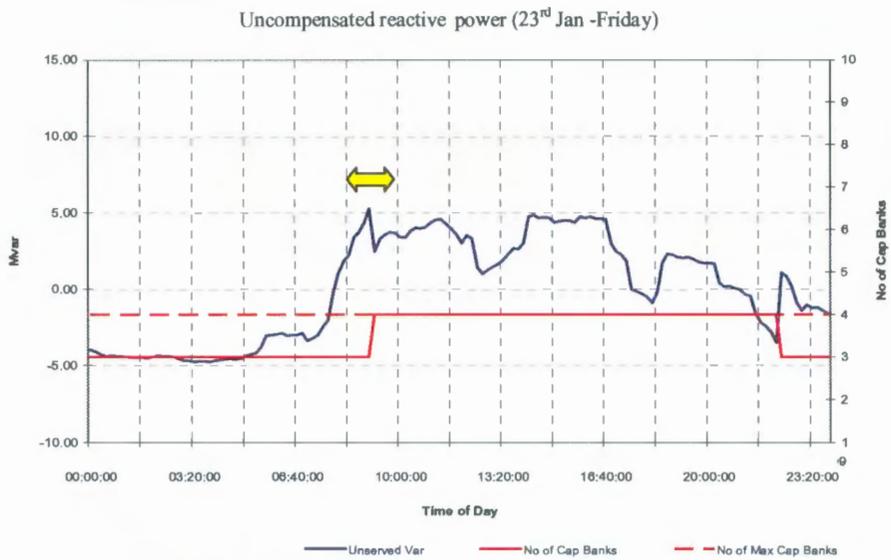


Figure: 4.8 (c) Reactive power flow under present switching criteria in master slave mode 23.01.09

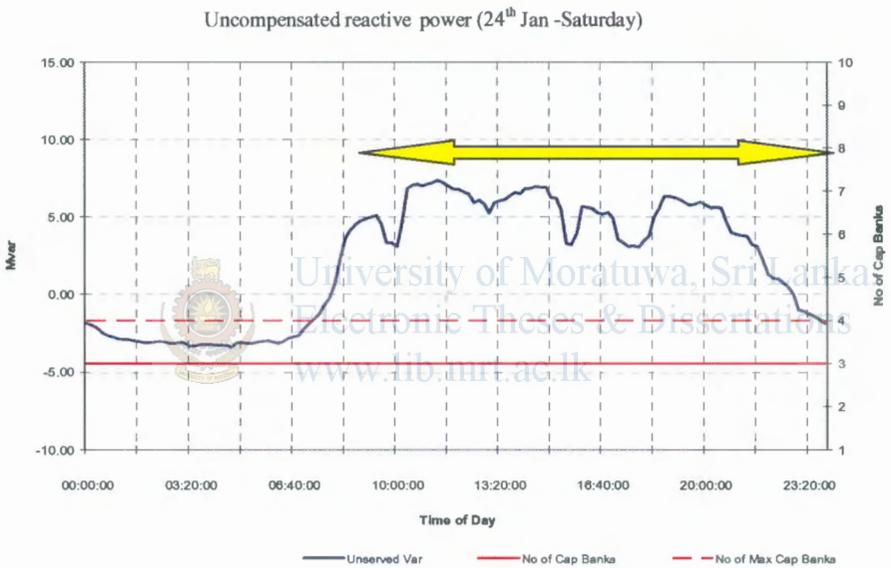


Figure: 4.8 (d) Reactive power flow under present switching criteria in master slave mode 24.01.09

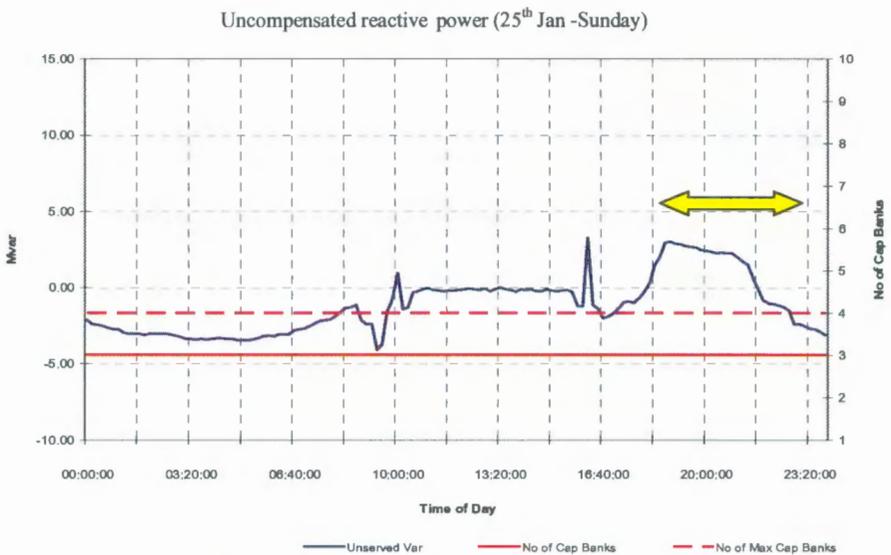


Figure: 4.8 (e) Reactive power flow under present switching criteria in master slave mode 25.01.09

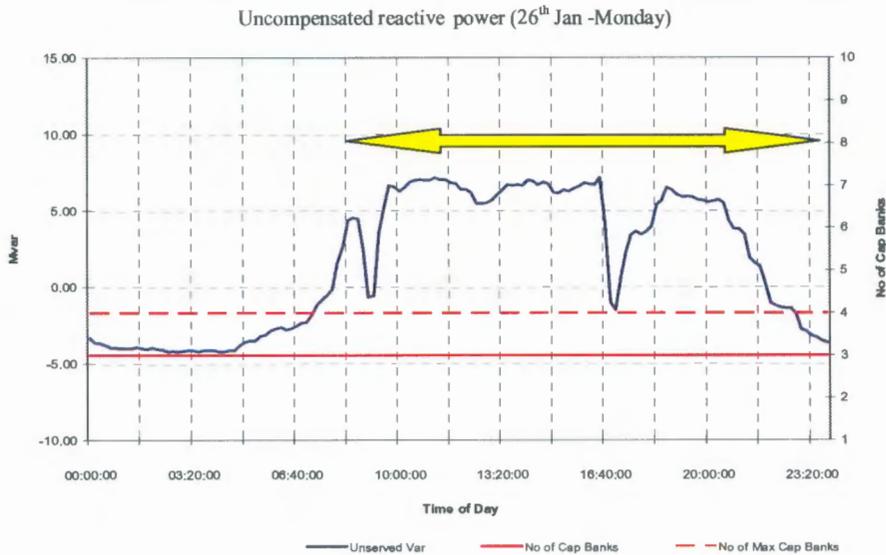


Figure: 4.8 (f) Reactive power flow under present switching criteria in master slave mode 26.01 2009

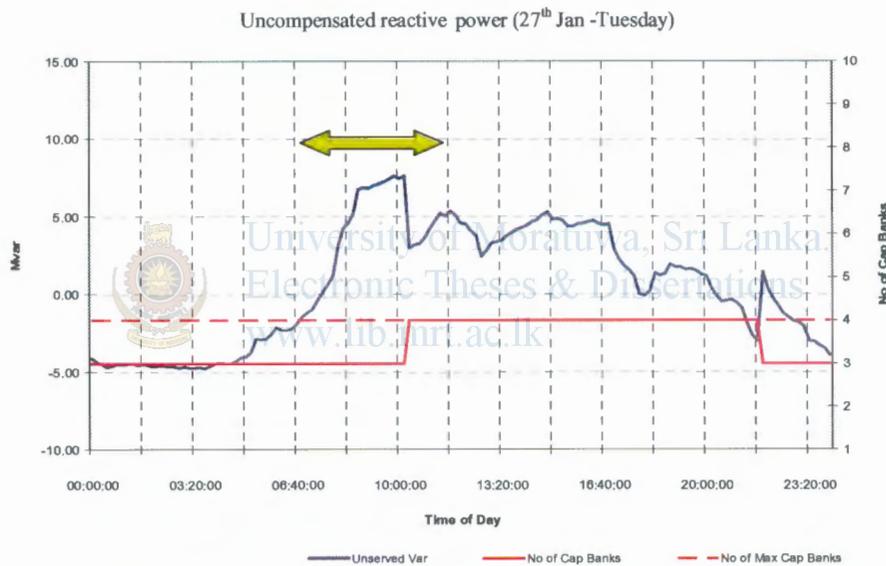


Figure: 4.8 (g) Reactive power flow under present switching criteria in master slave mode 27.01.09

The figure 4.8(b) shows that the 4th bank has not connected even till 14.00 hrs and could have been in ON position more hours during the evening, in the particular day. Figure 4.8 (c) suggests, the fourth capacitor banks could be connected more early in the day.

All other figures 4.8(d), 4.8(e), 4.8(f), and 4.8(g) shows that there are possibilities of switching more capacitor banks early, maintain the already connected ones furthermore, or switch the fourth bank that has not come during the day.

4.7 Behaviour of transformer Tap position

The function of the on load tap changer (OLTC) is to adjust the LV bus voltage to its nominal value. When the load is high, the bus voltage is low due to IZ drop and tap changer raises its tap to high position to adjust the terminal voltage.

The voltage rise obtained by raising one tap position up, is 1.5 % of the voltage at the point of measuring. This is as per the specifications of the OLTC. At 33kV voltage this rise is about 0.495 kV. The approximated percentage voltage rise given by switching one 5Mvar capacitor bank is given as $(kvar / kva) * X_t$ Where kvar = addition of reactive load, kva = transformer rating and X_t = transformer reactance in % [8]. When two transformers are in parallel, this value becomes 0.79% and the voltage rise is 0.260kV at 33kV.

As these figures suggests, the effect of rise in one tap step is same as adding two 5Mvar capacitor banks when two transformers are paralleled or one 5Mvar banks when one transformer is connected. Tap changer adjusts the voltage by adjusting the transformer ratio but capacitor banks by reducing the reactive power through the transformers and transmission lines. Further it reduces the currents and hence the losses and release the equipment capacities. Therefore, the reactions of capacitor controller and AVR has to be optimally utilized.

The Table 4.2 gives an extract from a output file showing simulation results for LV and HV voltage, MW and Mvar at LV side and transformer HV side peak currents under different tap positions with no capacitor banks and with four capacitor banks. (simulation results for conditions at 20.30 hrs on 24.01.09) If the condition starts from point (A) in tablee 4.2, It stabilizes at point (B) under tap changer control and ends at point (C) when capacitors are switched on by a voltage control scheme.

From (A) to (B), the results shows 3% voltage rise, 6% real power increase, 6 % reactive power increase and 5% current increase. From (A) to (C), there is a voltage increase of 3%, real power increase of 6% but the current and reactive power reduces by 7% and 74% respectively. This shows the effectiveness of both control loops when operates independently.



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Multiple Run Output File No cap banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV_MW	LV_MVar	TF_HV_Current
1	.9250000000	74.78120511	33.50227824	-23.51500332	47.12524546	20.50556929	.1714195128
2	.9400000000	74.79557315	32.98062952	-23.51500149	45.66920959	19.87200561	.1660712497----- (B)
3	.9550000000	74.80926471	32.47476970	-23.51500464	44.27907683	19.26711830	.1609690167
4	.9700000000	74.82232163	31.98400256	-23.51500323	42.95096263	18.68921707	.1560980129 ----- (A)
5	.9850000000	74.83478258	31.50767152	-23.51499635	41.68126197	18.13673328	.1514445311

Multiple Run Output File 3 cap banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV_MW	LV_MVar	TF_HV_Current
1	.9250000000	75.17429356	34.45411052	-6.171918169	49.83944434	5.389319552	.1621685425
2	.9400000000	75.17643599	33.91244808	-6.172074955	48.28522601	5.221766022	.1570913437
3	.9550000000	75.17849488	33.38725110	-6.171084037	46.80027560	5.060076609	.1522262048
4	.9700000000	75.18038156	32.87809638	-6.170808773	45.38412407	4.906991965	.1476188801----- (C)
5	.9850000000	75.18219740	32.38406964	-6.171620046	44.03061954	4.760750879	.1431963655

Table: 4.2 Output file from PSCAD simulation showing differences in measurements

Figures 4.9 (a), (b) and (c) shows the observations on the behaviour of the tap position in the absence of the capacitors. The number of capacitor banks that could be connected if the controller was in auto mode, is also indicated in the figures. The figures reveal that there is the possibility of operating the sub station at lower tap positions if the capacitor banks connected under the present criteria. The *Appendix 2(b)* illustrates the effects clearly.

132kV voltage pattern, tap position & no of cap banks (21st & 22nd)

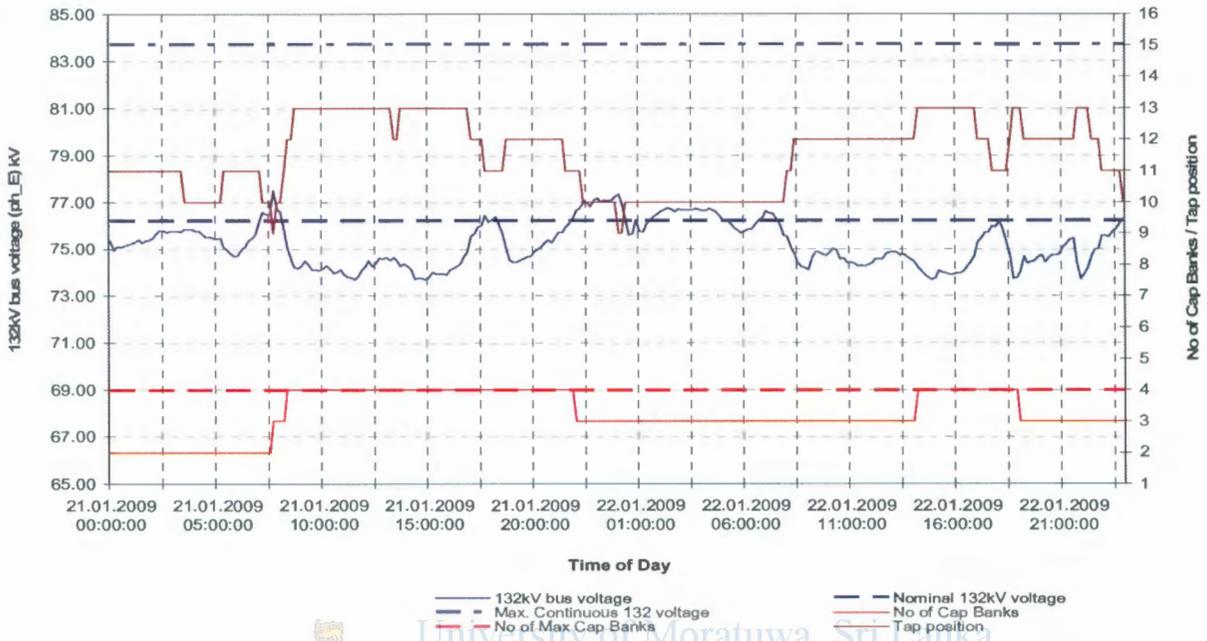


Figure: 4.9 (a) Pattern of tap position with no capacitor banks 21st & 22nd



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132kV voltage pattern, tap position & no of cap banks (23rd & 24th)

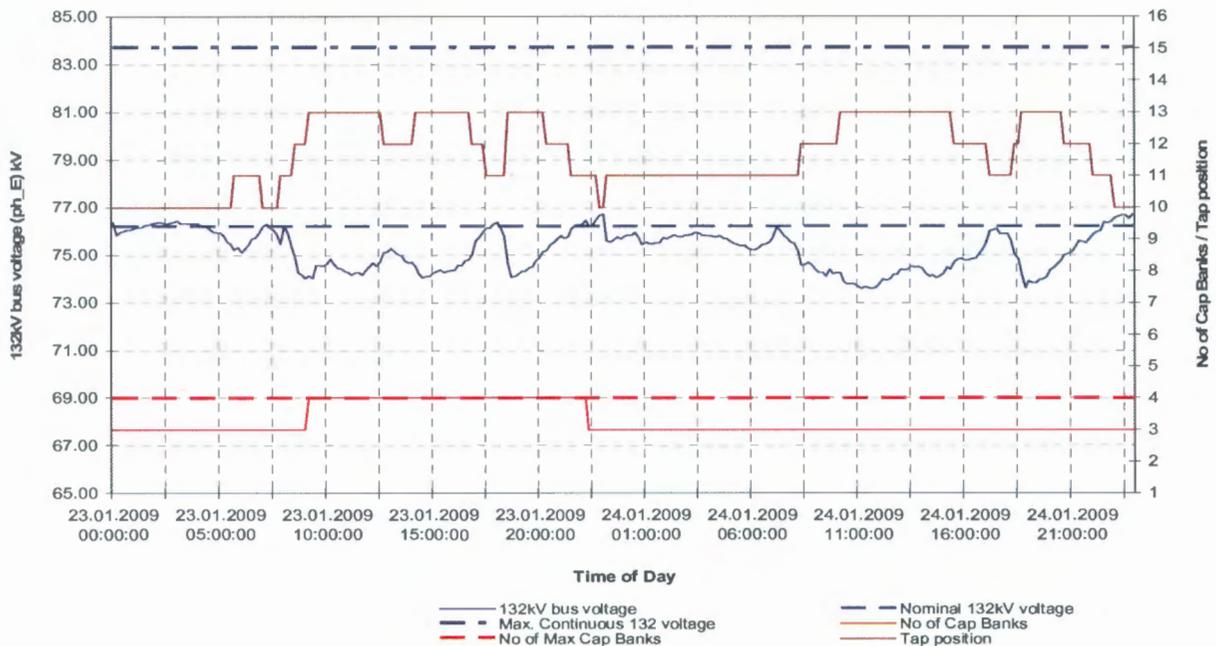


Figure: 4.9 (b) Pattern of tap position with no capacitor banks 23rd & 24th

132kV voltage pattern, tap position & no of cap banks (25th, 26th & 27th)

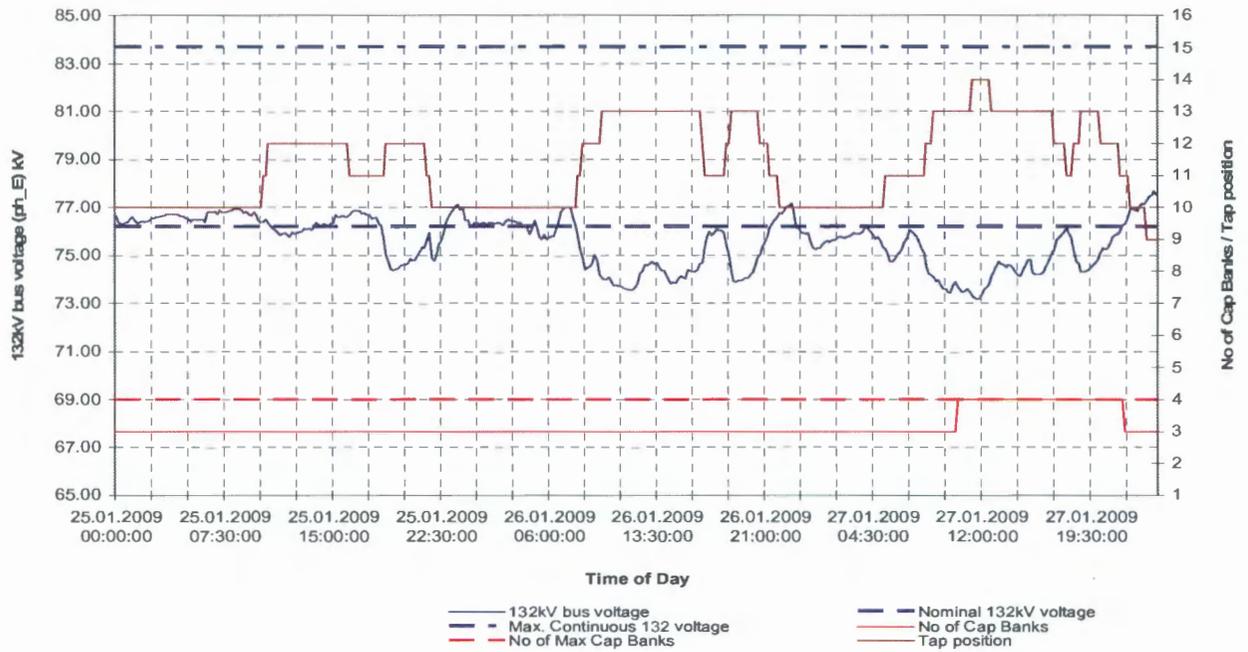
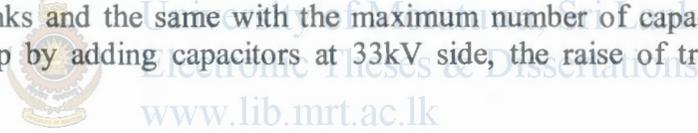


Figure: 4.9 (c) Pattern of tap position with no capacitor banks 25th to 27th

Appendix 2(b) compares the difference between the patterns of the tap position while having no capacitor banks and the same with the maximum number of capacitor banks. Since the voltage boost up by adding capacitors at 33kV side, the raise of transformer tap can be minimized.



4.8 Summary of the system study

By summarizing the results from the case study, it seems that the present switching criteria especially when the bus section breaker is in ON position dose not neither maximize nor optimize the use of installed capacitor banks in the selected substation.

Therefore, it is worth to consider different switching criteria that utilize the capacitor banks, than the existing utilization. However, under such circumstances, the effects to the system voltage, avoiding extreme over compensation that introduces losses due to leading power factor and other technical constraints have to be considered.



5. System modelling, simulations and data analysis

Exploring the possibilities of maximizing the use of capacitor banks in an existing substation has to be done in several steps. As discussed in the previous chapter, the first is to collect and record the system data and analyze them. Then by considering those results, simulation of the system under various operating scenarios and different capacitor bank combinations can be done. This needs a suitably developed computer simulation model. Using such a simulation various effects on the system due to switched capacitor banks can be studied. Followings are the areas that have to be studied as mentioned above.

- Maximum voltage rise due addition of capacitor banks at the bus bar in which they are connected
- The capability of transformer OLTC and AVR to handle those voltage variations by changing tap position, when necessary.
- The capability of OLTC to handle the current through it without exceeding it's current switching capacity during back feeding reactive power into the system
- The effect of resonance when adding more capacitor banks under various load conditions and system harmonic levels
- Effects on voltage distortion caused by load harmonics at 33kV bus, when adding more capacitor banks
- Cost analysis considering the reduction of losses due to power factor improvement, release of system component capacities etc. and many others.

5.1 System modelling and simulation

One of the main aspects of the research is to model the substation for analyzing various system conditions by simulation. Suitable computer aided simulation software with transient as well as steady state analyzing capabilities was needed for this purpose. Therefore, PSCAD which is a tool used by many power system Engineers, was used for modelling and simulations. PSCAD is a graphical user interface working along with an electromagnetic transient analysis program called EMTDC and a widely used software by power system engineers for power system studies [9]. Power system Computer Aided Design abbreviated as PSCAD schematically construct a circuit, run a simulation, analyze the results and manage the data in a completely integrated graphical environment. PSCAD is mainly for transient analysis but also equipped with all modules for the steady state analysis as well.

The difficulty faced in using PSCAD was that it is not free software and needs a license for use. However, a free student version with limited nodes is available. Also a trial version is available for limited time frame. The basic trials were done for simplest blocks with the free student version and later the complete model was developed with the trial version.

5.2 The Basics in Substation model

Main substation components such as power transformers, grounding zigzag transformers, circuit breakers, substation load, capacitor banks, tap changers, etc., are included in the model. Most of the components are available with the master library in the PSCAD. Some has to be approximated to the available modules in the main library.

The transformers are selected as two winding transformers with the tap changer on HV side of the transformers. The real transformer was approximated to the simplest form and percentage impedance was considered as an inductance only. The magnetization circuit was approximated with typical values.

The grounding zigzag transformer is represented with a typical star delta transformer with delta winding unconnected to a load. This representation is sufficiently valid for this kind of analysis [10]. The developed module for the transformer is as given in figure 5.1

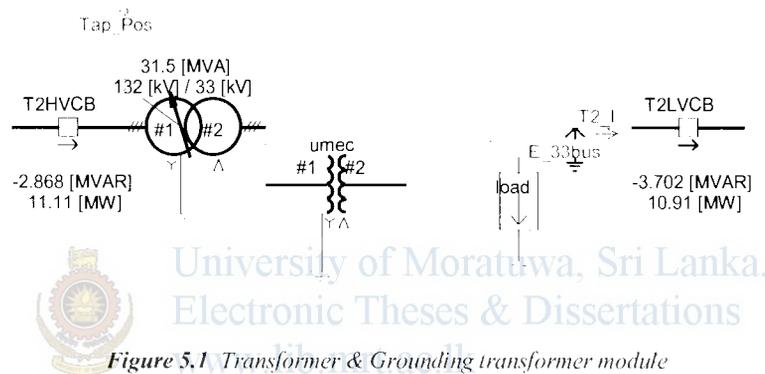
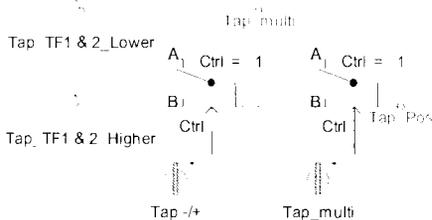
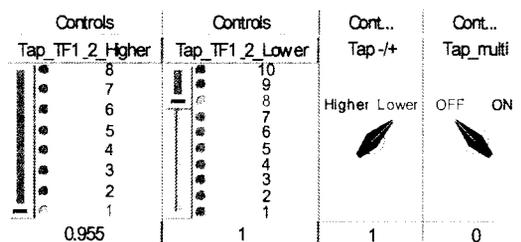


Figure 5.1 Transformer & Grounding transformer module

The tap changer is represented as a HV to LV ratio changer to suit the real tap changer ratios. Nominal ratio of the transformer is 4 and this has to be taken as 1 in the PSCAD model. The tap changer at Panadura is consisting of 18 taps with each 1.5% voltage difference. The tap changer is arranged as to control manually or change step wise in the multiple run mode, as below.



Tap Changer Control

Tap Position	Ratio	Tap Position	Ratio
1	1.105	10	0.970
2	1.090	11	0.955
3	1.075	12	0.940
4	1.060	13	0.925
5	1.045	14	0.910
6	1.030	15	0.895
7	1.015	16	0.880
8	1.000	17	0.865
9	0.985	18	0.850

Figure 5.2 Tap changer control module

Representing the network beyond the substation basically depends on approximations. Typically, in any approximated representation, if frequency response analysis at the bus bar level is not expected then a simple Thevenin's equivalent is sufficient.

The load is represented in two ways in the model and as a lumped load. One is specified with real and reactive power but the input values are real values so that input parameters from outputs of others modules could not be used in this module. Therefore, during multiple run functions, the second module with R and L values was used. R and L values are calculated as per the MW and Mvar values at different time slots.

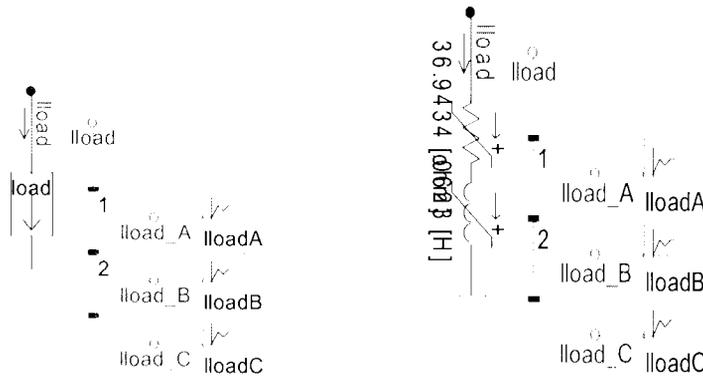


Figure 5.3 Load & load current measuring module

Capacitor banks with inrush and detune reactors are represented with equivalent C and L values as in the diagram. Though the real capacitor bank configuration ungrounded double WYE configuration, it is sufficient to represent it with a lumped star connected load for the analysis.

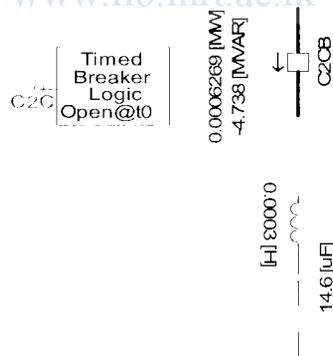


Figure 5.4 Capacitor bank & Inrush/Detuning reactor module

With all these main components and other measuring and recording modules, the complete model developed for the Panadura Grid Substation in CEB system is shown in the Figure 5.4

5.3 Running the simulations

First the simulations were run for measured data, real and reactive loads, tap positions, and voltage at source end and recorded data was compared with the actual measured data. This was done with no capacitor banks connected to the LV bus. Further, the real measurements were done with 10 minute interval but it was time consuming to run the simulations with the same time intervals. Therefore the simulations were run only for 30 minute interval.

The results obtained from the simulation shows that the model gives approximately same results as the actual. Next step of the simulations was to run the model with the capacitor banks connected as per the existing switching criteria. This run was done not only for the same tap position as with no capacitors but also for different tap positions until the LV bus voltage gives a close voltage level to its nominal value.

While running the simulations in this manner, all the data required for the analysis was recorded in the output file of the multiple run function block in PSCAD.

The third step in simulation was to energize one more step of the capacitor banks except for durations where all capacitor banks were energized. For example, when the controller switches only two capacitor banks in the real system, simulations were run with the third and fourth banks as well at different tap positions.

Typical output file obtained for the system with 3 capacitor banks switched on to the bus and under different tap positions is given below.

Multiple Run Output File							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	.9700000000	77.38861778	34.45012437	10.69121078	21.99452264	-4.151149007	.1051607455
2	.9850000000	77.38426922	33.92637354	10.68872963	21.33060859	-4.026310122	.1020034818
3	1.0000000000	77.38008298	33.41842128	10.68597805	20.69732342	-3.905589441	.9899367705E-01
4	1.0150000000	77.37613631	32.92537989	10.68736488	20.09086688	-3.792102475	.9611173627E-01
5	1.0300000000	77.37229255	32.44666079	10.68637580	19.51109591	-3.681892354	.9335744888E-01
6	1.0450000000	77.36867257	31.98162290	10.68801147	18.95561675	-3.577934633	.9071955012E-01
Statistical Summary Based on 6 Runs:							
Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current	
Minimum:	.9700000000	77.36867257	31.98162290	10.68597805	18.95561675	-4.151149007	.9071955012E-01
Maximum:	1.0450000000	77.38861778	34.45012437	10.69121078	21.99452264	-3.577934633	.1051607455
Mean:	1.0075000000	77.37834523	33.19143046	10.68794510	20.43000570	-3.855829672	.9772443994E-01
Std Dev:	.2806243040E-01	.7466101136E-02	.9235881790	.1894898505E-02	1.136968839	.2146131995	.5402956744E-02
2% Level:	.9498668133	77.36301174	31.29461222	10.68405346	18.09495715	-4.296591303	.8662812324E-01
98% Level:	1.065133187	77.39367873	35.08824871	10.69183675	22.76505425	-3.415068041	.1088207566
Probability Density Functions (%) for Variable 1, HV Voltage							
Centre of Range Probability(%) Cumulative Prob.(%) 100-Cumulative Prob.							
Probability Density Functions (%) for Variable 2, LV Voltage							
Centre of Range Probability(%) Cumulative Prob.(%) 100-Cumulative Prob.							
Probability Density Functions (%) for Variable 3, Ph Ang LV							
Centre of Range Probability(%) Cumulative Prob.(%) 100-Cumulative Prob.							
Probability Density Functions (%) for Variable 4, LV MW							
Centre of Range Probability(%) Cumulative Prob.(%) 100-Cumulative Prob.							
Probability Density Functions (%) for Variable 5, LV MVar							
Centre of Range Probability(%) Cumulative Prob.(%) 100-Cumulative Prob.							
Probability Density Functions (%) for Variable 6, TF HV Current							
Centre of Range Probability(%) Cumulative Prob.(%) 100-Cumulative Prob.							

Table: 5.1 Multiple run out put file for six recording inputs for different tap positions

Even with the half hour interval time slots, the simulations takes long time. Therefore, running simulation for more and more days is a time consuming task. Therefore, the simulations were done for three selected days only. The recorded data is as per the format annexed in *Appendix 3-a*. The data summery is given in *Appendix 3-b*.

5.4 Voltage raise due to capacitor banks

The substation model was adjusted to have same measurement condition as measured without capacitor banks and voltage at high and low voltage bus bars, real and reactive power, phase angle measured at 33kV bus bar, current through the transformer etc, were recorded. The changes of above parameters by switching on the capacitor banks as per present criteria, for the optimum condition with maximum var compensation, for maximum capacitor banks were also recorded.

The table 5.2 gives an indication how the high voltage and low voltage bus voltages has been affected when changing from present criteria to maximum capacitor bank state with the AVR function as well.

Time	Changes due to maximum cap banks compared to present criteria 21.01.2009				Changes due to maximum cap banks compared to present criteria 22.01.2009				Changes due to maximum cap banks compared to present criteria 24.01.2009			
	Change of HV bus Voltage (kV)	Change of LV bus Voltage (kV)	Change of No of cap banks	Change of Tap position	Change of HV bus Voltage (kV)	Change of LV bus Voltage (kV)	Change of No of cap banks	Change of Tap position	Change of HV bus Voltage (kV)	Change of LV bus Voltage (kV)	Change of No of cap banks	Change of Tap position
00:00:00	0.22	0.13	2	-1	0.10	-0.17	1	-2	0.12	0.31	1	0
00:30:00	0.25	0.13	2	-1	0.12	-0.18	1	-1	0.12	0.31	1	0
01:00:00	0.25	0.13	2	-1	0.12	-0.18	1	-1	0.12	0.31	1	0
01:30:00	0.24	-0.38	2	-2	0.12	0.32	1	0	0.12	0.31	1	0
02:00:00	0.25	0.13	2	-1	0.12	-0.18	1	-1	0.12	0.31	1	0
02:30:00	0.24	0.13	2	-1	0.12	-0.18	1	-1	0.12	0.31	1	0
03:00:00	0.23	0.13	2	-1	0.12	-0.18	1	-1	0.12	0.32	1	0
03:30:00	0.24	0.13	2	-1	0.12	-0.18	1	-1	0.12	0.32	1	0
04:00:00	0.24	0.13	2	-1	0.12	-0.18	1	-1	0.12	0.32	1	0
04:30:00	0.24	-0.37	2	-2	0.12	-0.18	1	-1	0.12	0.31	1	0
05:00:00	0.24	-0.37	2	-2	0.12	0.32	1	0	0.12	0.31	1	0
05:30:00	0.24	0.12	2	-1	0.12	-0.18	1	-1	0.13	0.32	1	0
06:00:00	0.24	0.13	2	-1	0.12	-0.18	1	-1	0.13	0.32	1	0
06:30:00	0.25	0.13	2	-1	0.12	-0.19	1	-1	0.01	0.32	1	0
07:00:00	0.25	-0.38	2	-2	0.12	-0.18	2	-1	0.12	0.31	1	0
07:30:00	0.24	0.13	2	-1	0.12	-0.19	1	-1	0.12	0.31	1	0
08:00:00	0.13	-0.20	1	-1	0.12	-0.19	1	-1	0.12	0.31	1	0
08:30:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.12	0.31	1	0
09:00:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.12	0.31	1	0
09:30:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.12	0.31	1	0
10:00:00	0.00	0.00	0	0	0.12	0.31	1	0	0.12	0.31	1	0
10:30:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.13	0.31	1	0
11:00:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.13	0.31	1	0
11:30:00	0.00	0.00	0	0	0.13	0.31	1	0	0.13	0.31	1	0
12:00:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.13	0.31	1	0
12:30:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.13	0.31	1	0
13:00:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.13	-0.20	1	0
13:30:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.13	-0.20	1	0
14:00:00	0.00	0.00	0	0	0.13	0.31	1	0	0.13	0.31	1	0
14:30:00	0.00	0.00	0	0	0.00	0.00	0	0	0.13	0.31	1	0
15:00:00	0.00	0.00	0	0	0.00	0.00	0	0	0.13	-0.20	1	0
15:30:00	0.00	0.00	0	0	0.00	0.00	0	0	0.12	0.31	1	0
16:00:00	0.00	0.00	0	0	0.00	0.00	0	0	0.13	-0.20	1	0
16:30:00	0.00	0.00	0	0	0.00	0.00	0	0	0.13	-0.20	1	0
17:00:00	0.00	0.00	0	0	0.00	0.00	0	0	0.13	0.31	1	0
17:30:00	0.00	0.00	0	0	0.00	0.00	0	0	0.12	0.32	1	0

18:00:00	0.00	0.00	0	0	0.00	0.00	0	0	0.12	0.32	1	0
18:30:00	0.00	0.00	0	0	0.00	0.00	0	0	0.13	-0.20	1	0
19:00:00	0.00	0.00	0	0	0.13	0.32	1	0	0.13	0.31	1	0
19:30:00	0.00	0.00	0	0	0.12	-0.20	1	-1	0.13	0.31	1	0
20:00:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.13	0.31	1	0
20:30:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.13	-0.20	1	0
21:00:00	0.00	0.00	0	0	0.13	0.31	1	0	0.13	0.31	1	0
21:30:00	0.00	0.00	0	0	0.13	-0.20	1	-1	0.12	-0.19	1	0
22:00:00	0.12	0.32	1	0	0.12	-0.20	1	-1	0.12	0.31	1	0
22:30:00	0.12	0.32	1	0	0.12	-0.19	1	-1	0.12	-0.19	1	0
23:00:00	0.12	-0.18	1	-1	0.13	0.32	1	0	0.12	0.31	1	0
23:30:00	0.11	-1.17	1	-1	0.12	-0.18	1	-1	0.12	0.31	1	0

Table: 5.2 Increase/Decrease in bus voltages due to maximum capacitor connections

As per the data in the table, the maximum voltage rise in 132kV bus bar is 0.25kV and 0.32kV in 33kV bus bar, under real system conditions (with tap changer). The maximum voltage at the HV bus is 77.80kV. (Refer *Appendix 3-b*)

The maximum effect to the voltage occurs when all capacitors are connected, at maximum source voltage and at minimum load. To check the effect, simulation was run for maximum continuous source voltage of 145kV at minimum substation load of 17.2MW and 9.6Mvar for various tap positions. The results are shown in following table.

Multiple Run Output File		All Caps		17.2 MW		9.6Mvar	
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	1.00000000	83.07677576	36.85594001	31.20543495	21.37469995	-12.92286353	.1051738480
2	1.01500000	83.06745081	36.30956725	31.20486137	20.74670260	-12.54229066	.1020880757
3	1.03000000	83.05852515	35.77920687	31.20529453	20.14495215	-12.17835456	.9915548672E-01
4	1.04500000	83.04997852	35.26412698	31.20548649	19.56857477	-11.83015222	.9635954657E-01
5	1.06000000	83.04179021	34.76367327	31.20524509	19.01855912	-11.49667914	.9365258054E-01
6	1.07500000	83.03394569	34.27726921	31.20527452	18.49070244	-11.17708379	.9107116275E-01
7	1.09000000	83.02642465	33.80428815	31.20504308	17.98304192	-10.87145676	.8860031208E-01
8	1.10500000	83.01920234	33.34420109	31.20536777	17.49747653	-10.57650063	.8622601926E-01

Multiple Run Output File		No Caps		17.2 MW		9.6Mvar	
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	1.00000000	82.55300216	35.48586479	-29.22323451	19.81771014	11.11511467	.1024325584
2	1.01500000	82.55901578	34.96615854	-29.22323752	19.24149323	10.79224345	.9946125022E-01
3	1.03000000	82.56476859	34.46138792	-29.22324054	18.68997372	10.48319655	.9661804273E-01
4	1.04500000	82.57027546	33.97092109	-29.22324356	18.16176419	10.18719813	.9389568724E-01
5	1.06000000	82.57555021	33.49416116	-29.22324657	17.65557300	9.903525888	.9128743988E-01
6	1.07500000	82.58060570	33.03054389	-29.22324957	17.17019652	9.631506662	.8878702009E-01
7	1.09000000	82.58545392	32.57953542	-29.22325255	16.70451202	9.370512516	.8638857295E-01
8	1.10500000	82.59010604	32.14063033	-29.22325551	16.25747125	9.119957137	.8408663513E-01

Table: 5.3 Voltage variation for max continuous HV side voltage & Minimum substation load for different tap positions

Under the selected worst case, with all capacitor banks are switched off, the LV bus voltage will be maintained at 33.494 kV by the AVR adjusting tap position to 4. When all banks are

connected at this stage, AVR & tap changer is capable to maintain the bus voltage at 33.344kV. The tap changes from 4 to 1, under this worst case. Practically this is not a desired condition but such a worst case will not be allowed by the system operator.

According to the theoretical calculations, the voltage rise at LV side of a transformer can be approximated as follows.

$$\text{Percentage voltage rise} = (\text{kvar} / \text{kva}) * X_t$$

Where kvar = addition of reactive load

kva = transformer rating

X_t = transformer reactance in %

With present configuration, the maximum effective reactive power injection when either transformers in parallel, or transformers are independent, is 10Mvar (since each transformer is connected with two banks). Therefore, as per the above approximation, for this substation addition of full reactive load of 20 Mvar give a rise of about 1.04kV at 33kV bus voltage. The simulation results confirm the above approximation as in table 5.3.

As indicated in section 4.7, the change of one tap position change the voltage by 0.015 pu and this is about 0.495kV at 33kV. The effect of rise in voltage over the nominal value due to addition of maximum capacitor banks can be handled with two tap positions.

The table 5.3 very clearly indicates that the high voltage side bus voltage at the present real conditions does not overstep the maximum continuous voltage of 83.715kV. The figure also shows that for maximum allowable HV bus voltage and for minimum load, the maximum LV bus voltage rise for same tap position is (36.8559-35.4859) kV equalling to 1.37kV.

The figures 5.6 (a), (b), (c) show the simulation results of how the voltage at the HV bus behaves with the number of capacitor banks, for 21st, 22nd and 24th of January.

Phase Voltage - 132kV bus 21st January 2009

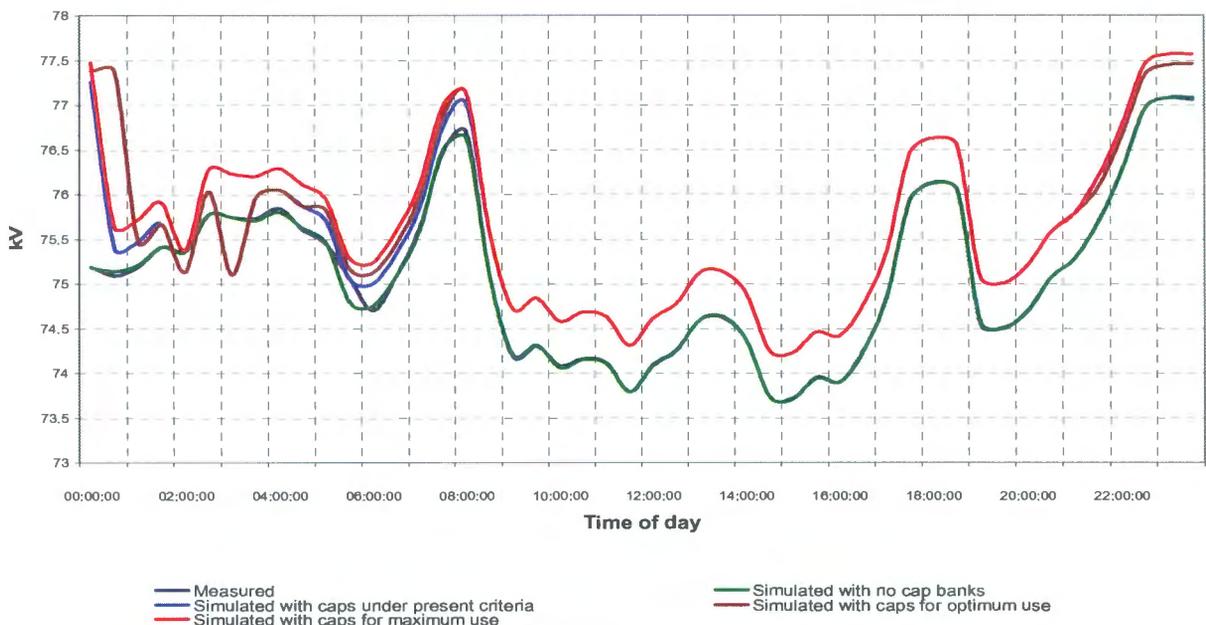


Figure: 5.6 (a)

Phase Voltage - 132kV bus 22nd January 2009

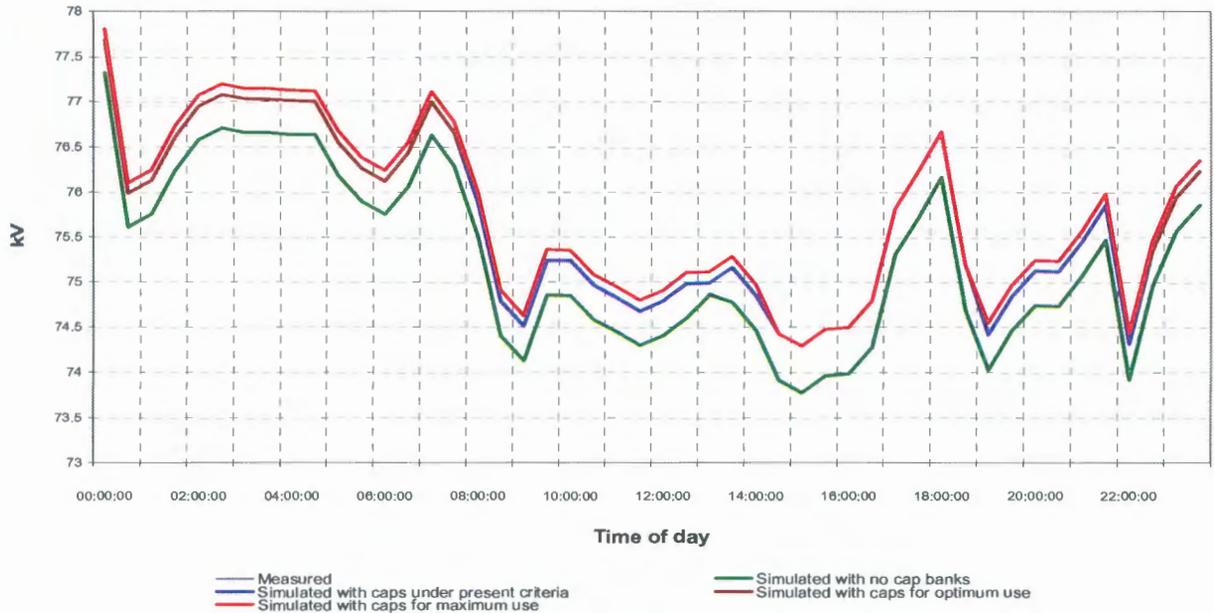


Figure: 5.6 (b)

Phase Voltage - 132kV bus 24th January 2009

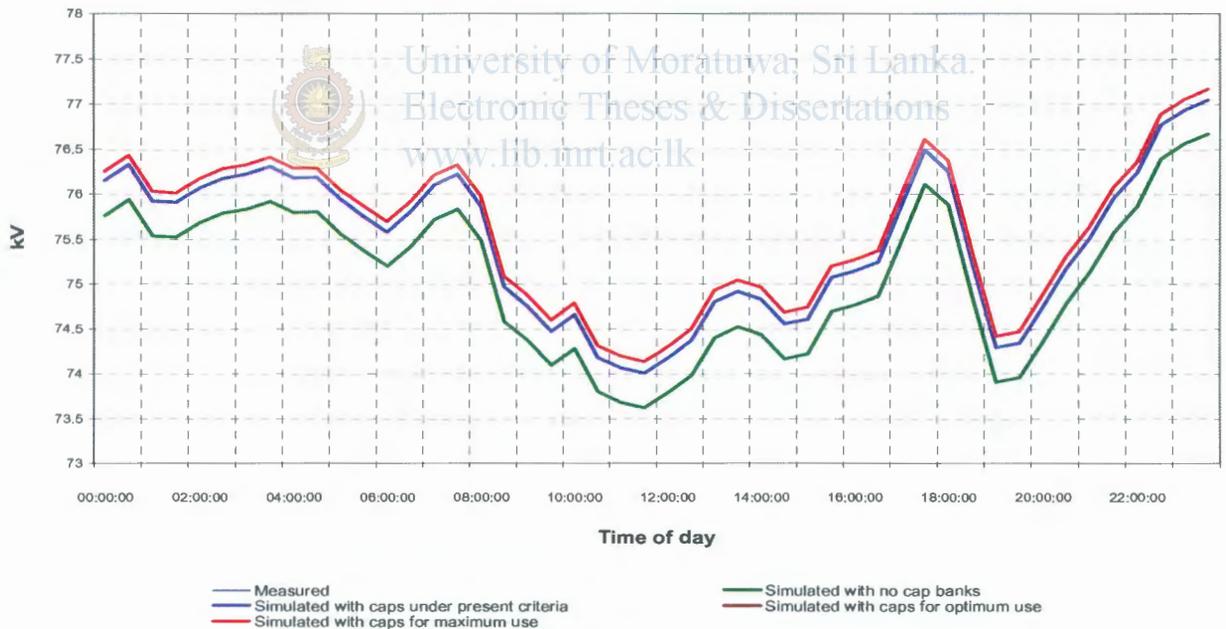


Figure: 5.6 (c)

Figure: 5.6 HV bus voltage variations under different cap bank configurations – Simulated data for 21st, 22nd & 24th Jan 2009

Considering the above factors, switching the maximum capacitor banks under any real time condition, is obviously possible as far as the voltage rise at bus bar is concerned. With the results analyzed, a real time measurement of 132 kV voltages with all 4 capacitor banks in ON condition, was done at Panaduara substation and recorded as in the format *Appendix 4*. The figure 5.7 below shows the variation of high voltage side voltage under above conditions.

Actual voltages with maximum capacitor banks - 18th Feb to 21st Feb 2009

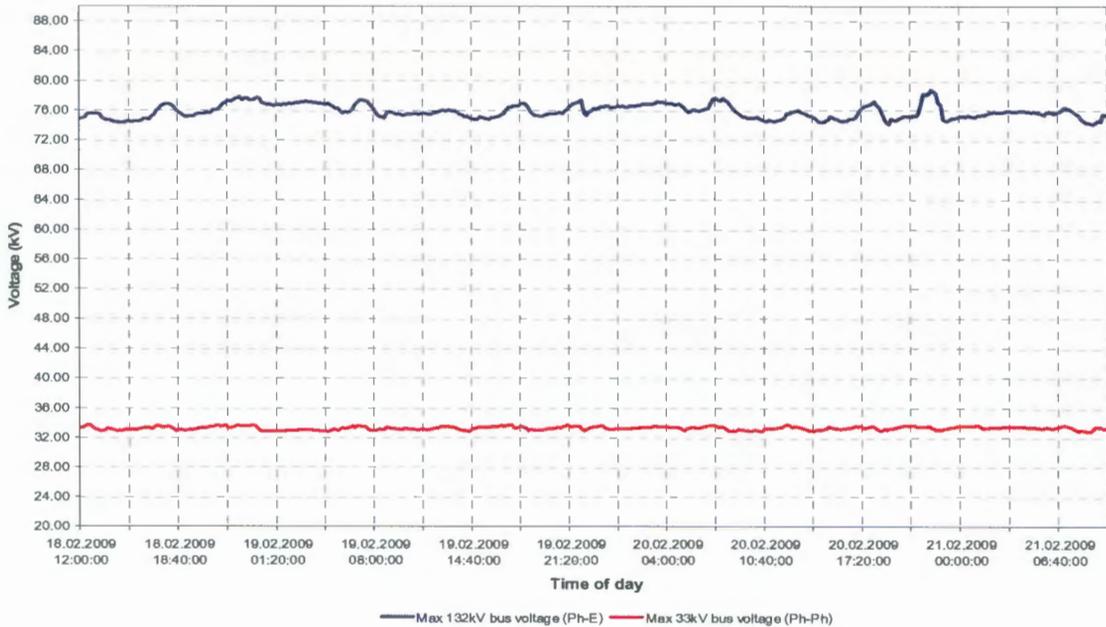


Figure: 5.7 HV bus voltage values with all 4 banks in ON position – Actual measurements

5.5 Voltage control by OLTC & AVR

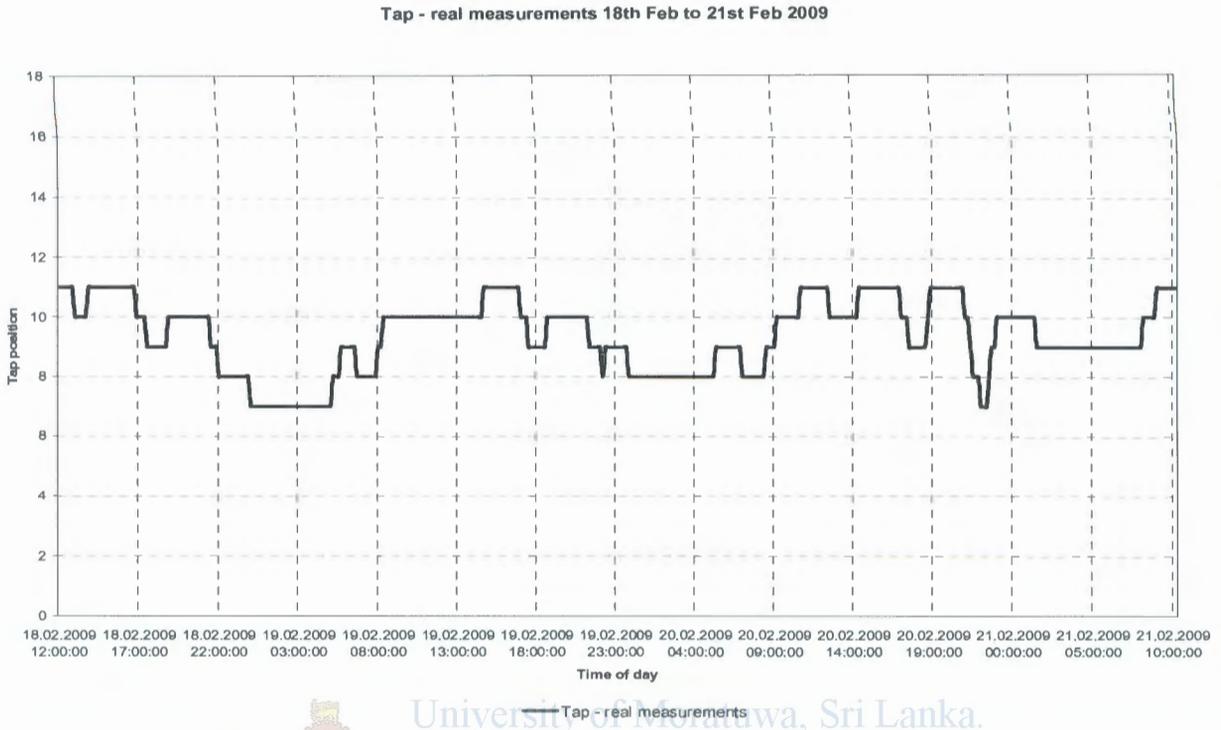
As mentioned in the previous paragraph, while providing the necessary reactive power support to the system at grid substation, if the switching of the capacitor banks is not based on voltage, i.e- voltage controlled switching, and then rise of voltages beyond the nominal values has to be maintained by the AVR and tap changer. For this purpose, the voltage at 33kV bus has to be within the controllable limit of the tap positions. Otherwise, the capacitor banks will be tripped by the over voltage relay.

Variation of tap position - 21st & 22nd January 2009



Figure: 5.8 Tap position variation to give constant LV voltage– Simulation results

The figure 5.8 shows the simulation results of variation of the tap position under maximum var support. It indicates that the tap position remains around the nominal tap and voltage variation has been handled by the taps. A real time measurement also was done to track the varying tap position throughout couple of days with all capacitors connected and the data is shown in figure 5.9.




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5.6 Current through the OLTC

With the present load conditions at the selected substation, the transformers are only partially loaded. Also, the minimum reactive load is around 9 Mvar.

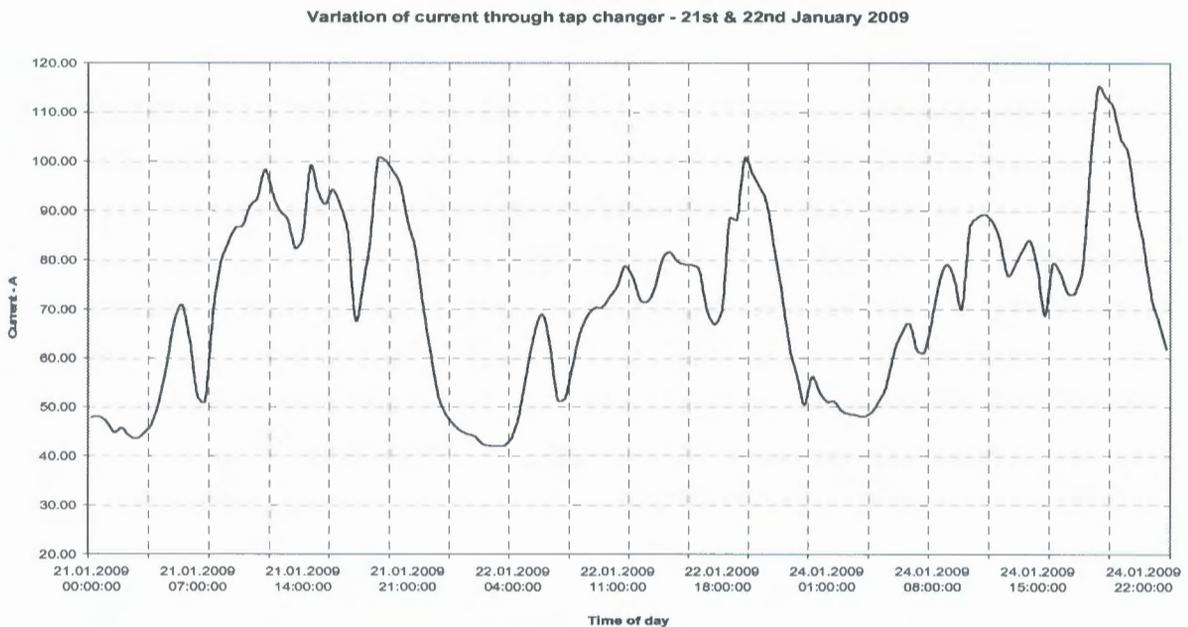


Figure: 5.10 Current variations through OLTC – Simulation results

If all capacitor banks are connected, the maximum leading reactive power flow through a transformer occurs at this stage if the bus section is closed and one transformer is out of service. Even under this condition, it does not exceed the transformer capacities and therefore it is not a factor to be worried about. However simulation was done to check the current variation through the tap changer. The particular tap changer type installed at the transformer high voltage winding can handle 200A current. Figure 5.10 shows it is well within the range of the OLTC switching current capabilities.

5.7 Effect Of resonance due to maximum capacitor banks

The effects of resonance due to adding capacitors were studied using the same PSCAD model. For tracking the frequencies at which the resonance occurs, a module named as “Interface to harmonic impedance solution” available in the master library, was used. The function of the module is to measure the impedance looking from the point of connection at different frequencies and gives an output file.

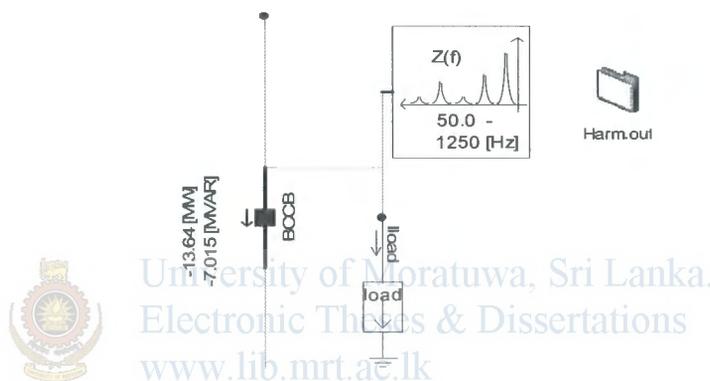


Figure: 5.11 Module for measurement of resonance frequency

A typical output of such a simulation run is shown in the Table 5.4. The simulations were done for different load combinations and for different substation configurations as well. By observing the data recorded from such simulation runs for different loads with all four capacitor banks kept connected, impedance Vs frequency graph was drawn. It shows three distinct frequency points where one with minimum impedance and other two with high impedances, irrespective of the load. Refer *Appendix 5(a) & 5(b)*

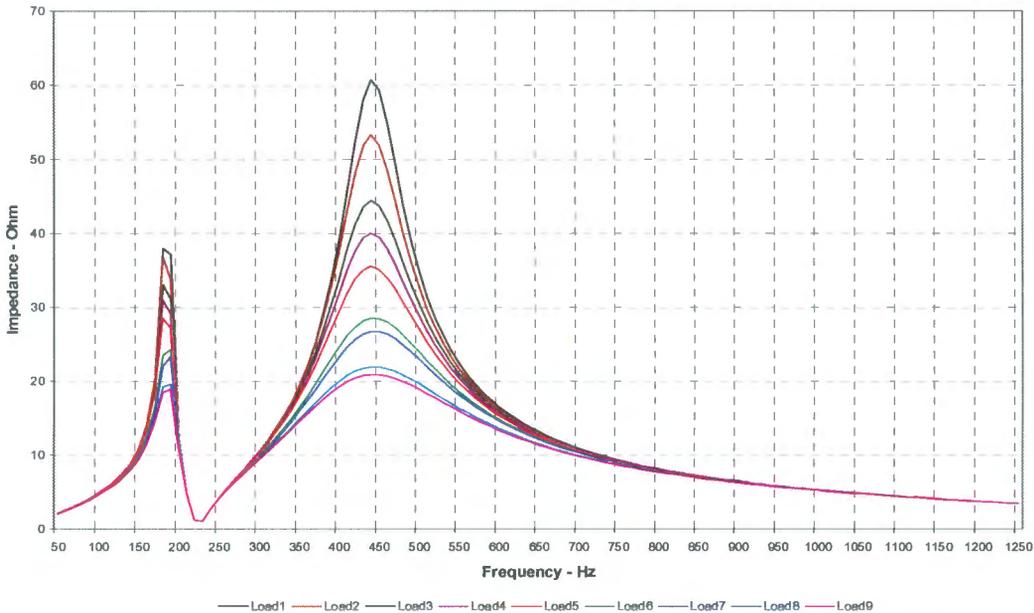
F(Hz)	Z0 (ohms)	PHASE(Z0)(Deg)	Z+ (ohms)	PHASE(Z+)(Deg)	Z- (ohms)	PHASE(Z-)(Deg)
50.00000000	55.09203446	-4.098444255	2.097409124	82.98484493	2.097409124	82.98484493
60.00000000	50.44407441	-24.03703513	2.558087278	83.23138944	2.558087278	83.23138944
70.00000000	43.46072201	-38.10848309	3.047037667	83.22463190	3.047037667	83.22463190
80.00000000	37.04461190	-47.88060994	3.573022622	83.03573202	3.573022622	83.03573202
90.00000000	31.75274158	-54.90963345	4.148001435	82.69569991	4.148001435	82.69569991
100.00000000	27.44952485	-60.20104551	4.788935334	82.21114615	4.788935334	82.21114615
110.00000000	23.90208802	-64.35890785	5.521017001	81.56846432	5.521017001	81.56846432
120.00000000	20.91325779	-67.75173676	6.383784086	80.73025346	6.383784086	80.73025346
130.00000000	18.33472075	-70.61360360	7.443618738	79.62166641	7.443618738	79.62166641
140.00000000	16.05674512	-73.10039898	8.821990580	78.09539609	8.821990580	78.09539609

Table: 5.4 Typical frequency resonance output file

The first high impedance point is in between 3rd and 4th harmonics and closed to 4th harmonic. The presence of such inter-harmonics or 4th harmonics in the system is very low so that contribution to V_{THD} is less. The 2nd high impedance point is in between 8th and 9th harmonics

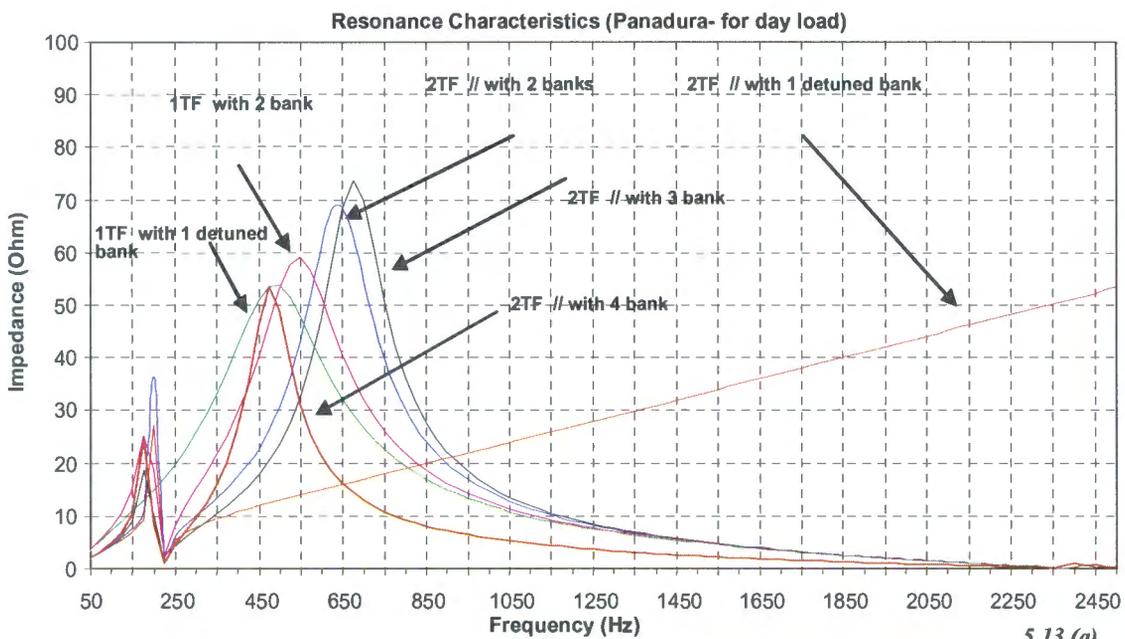
but very close to the 9th harmonic. The content of 9th harmonics in the system loads is higher therefore this will have an impact on the voltage distortion. The minimum impedance is falling between 4th and 5th harmonic levels and close to 5th harmonic level. This is due to the fact that the filter bank is tuned to 5th harmonic. This point does not exactly come to the 5th harmonic level since the model is approximated to a equivalent source beyond the 132kV bus, neglecting the line capacitances.

Resonance Characteristics

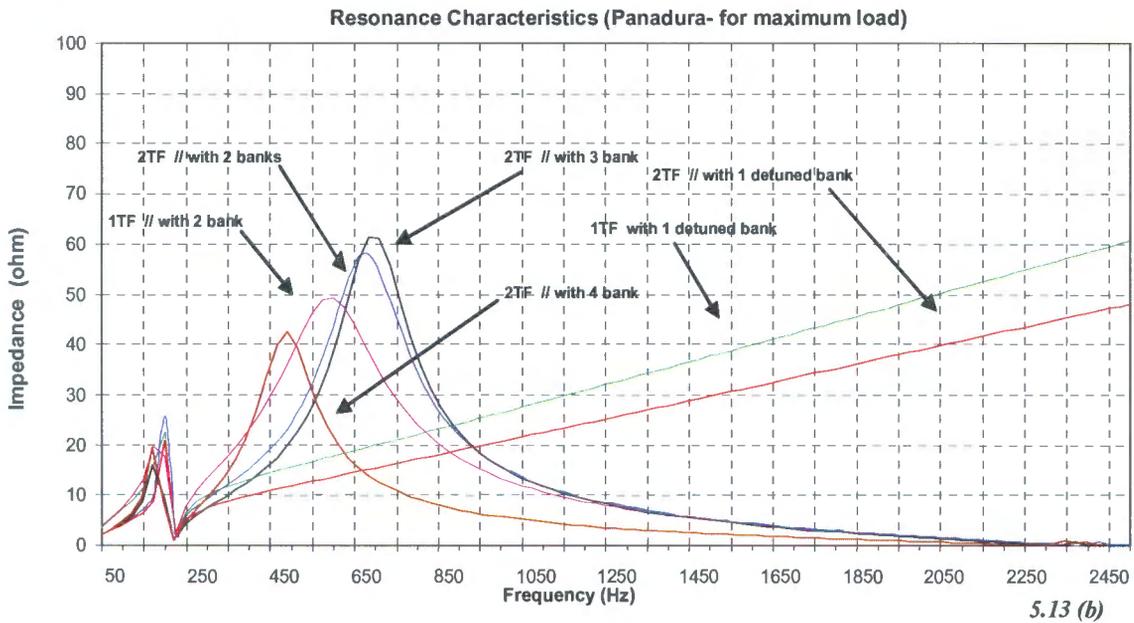


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 Figure: 5.12 Frequency plot for different load conditions with all banks connected

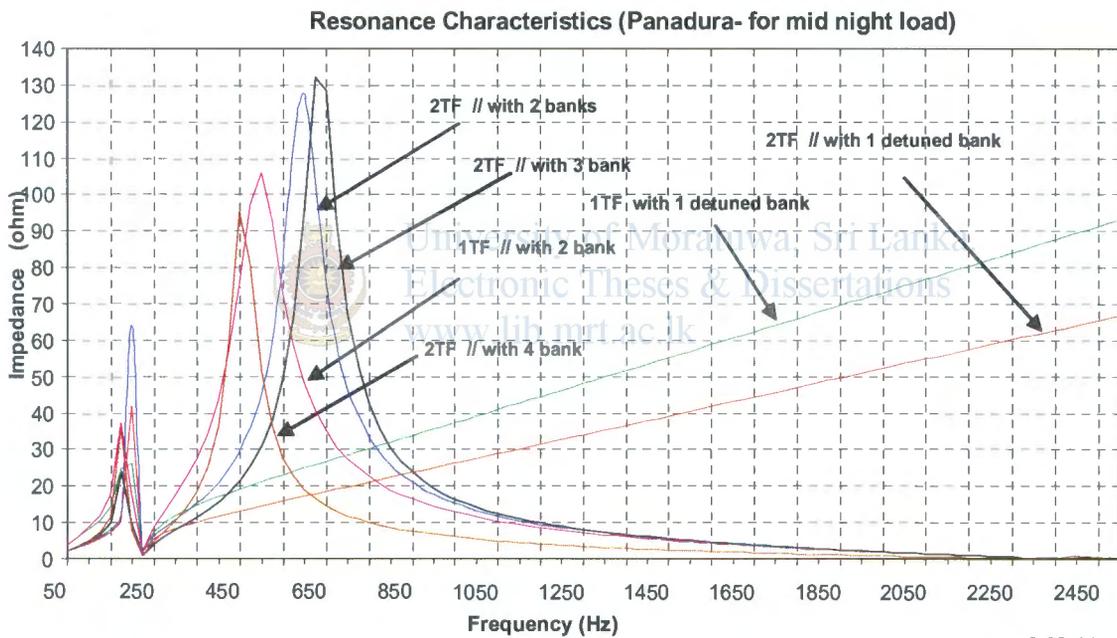
For a typical load condition, simulation was run for different tap positions but there is no significant effect on the resonance frequencies by the tap position. Figures 5.13 (a), (b) and (c) shows the resonance characteristics for different transformer / Cap bank configurations.



5.13 (a)



5.13 (b)



5.13 (c)

Figure: 5.13 Frequency plot for different load conditions under different bank configurations

Finally, taking the factors above into consideration, it seems that the series resonance point does not change with the load or capacitor/transformer arrangement therefore it is not a critical issue. However, the high impedance points close to harmonic levels that are present in the system will cause certain voltage distortion at the 33kV bus. Therefore, these high impedance points are to be evaluated from the voltage distortion point of view.

5.8 Effects on voltage distortion caused by harmonics under maximum capacitor banks

The same PSCAD model used for other simulations were slightly modified and added with required components to investigate the voltage distortion at the 33kV bus due switched capacitor banks in the presence of load side harmonics.

The difficulty faced in modelling for distortion level observations, was due to inability to introduce a harmonic load with specified THD level. However, it was possible to inject individual harmonic currents with amplitudes calculated according to actual measurements.

The accuracy of model was checked by recording the I_{THD} from simulation results for a set of measured values as follows (*Appendix 6*).

3rd %			5th %			7th %			9th %			11th %			13th %		
L1	L2	L3	L1	L2	L3	L1	L2	L3									
6	14	13	3	3	4	2	2	1	1	1	2	1	1	1	1	1	1

For above data, I_{THD} is around 13% and the simulation result also gives the same results as shown in the figure below.

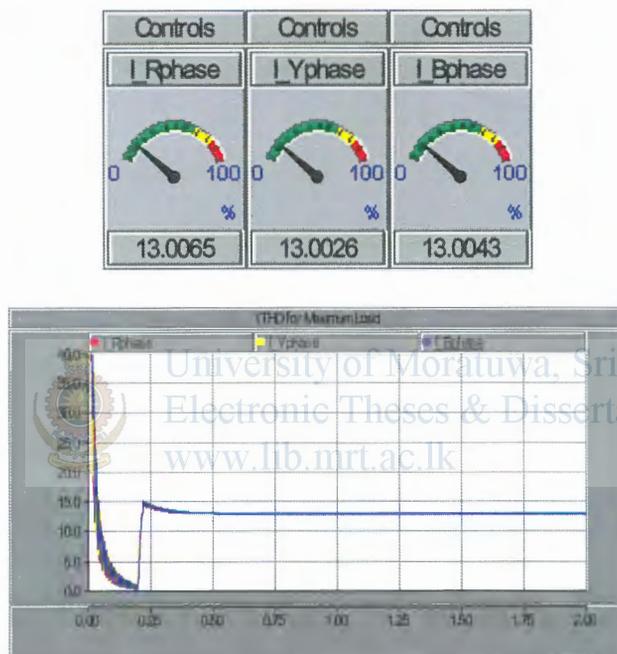


Figure: 5.14 I_{THD} measurement for a known set of data

Considering the observations made in resonance studies, the voltage distortion for a certain load condition was measured for each harmonic order. It shows that 3rd, 7th and 9th order harmonics give maximum contribution to the total harmonic level since the system has a high frequency point close to 3rd and 9th harmonic and also the 3rd harmonic current is dominating in load current. The results in figure 5.15 clearly indicate these results.



Figure: 5.15 Individual distortion levels

The voltage distortion level at the 33kV bus was simulated for maximum load and minimum load conditions for all four capacitor banks switched ON, 3 capacitor banks switched ON, two banks ON, one bank ON and without capacitor banks connected etc.,

Under these configurations, the voltage distortion at 33kV bus level is below 7.2% level (6.5% is the planning value as per IEC 61000-3-6 and 8% tolerance value as per EN 50160) . High distortion is resulted when all capacitor banks are connected. Therefore the impact to allowable voltage distortion levels by maximum use of capacitor banks is under acceptable levels.

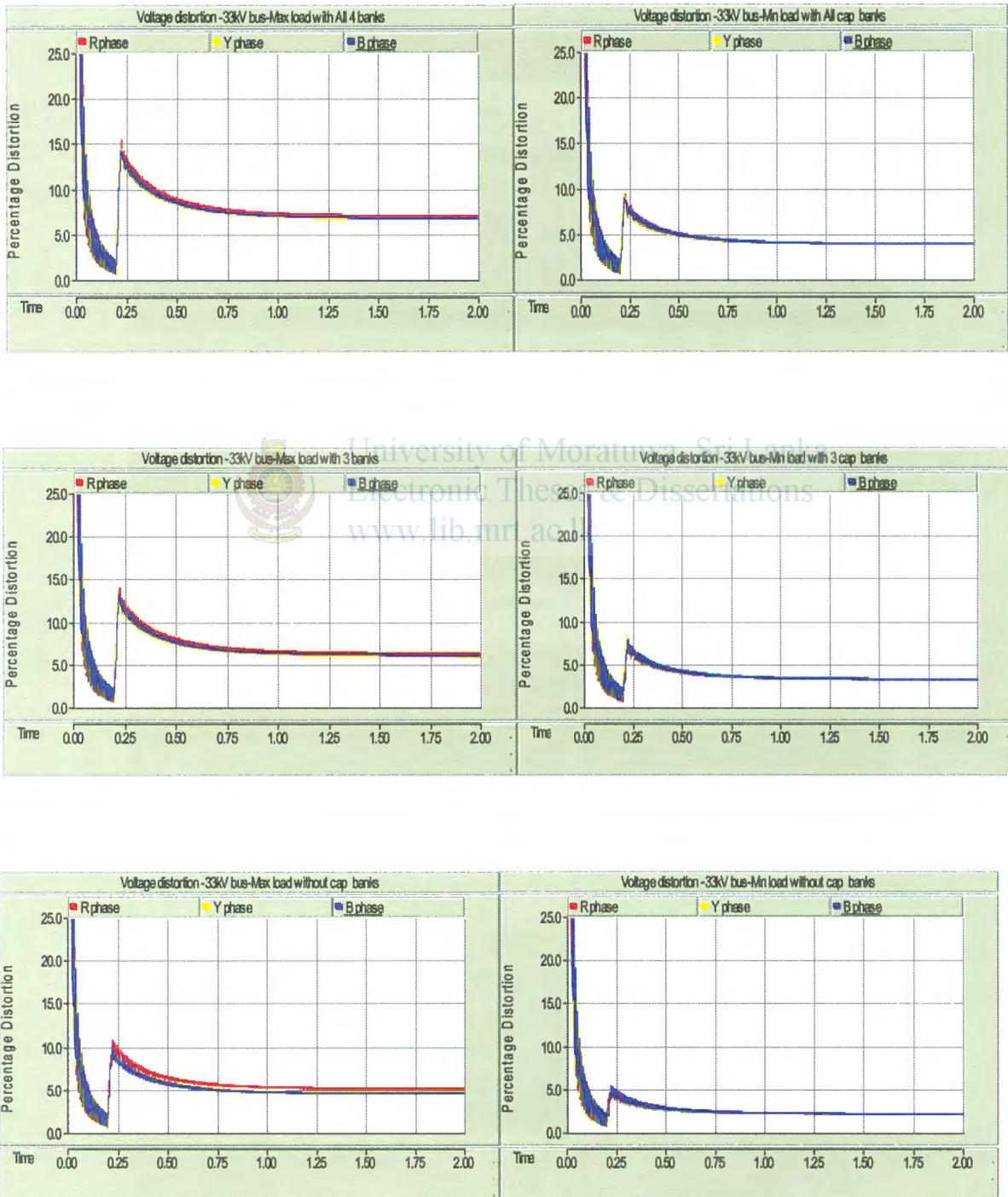
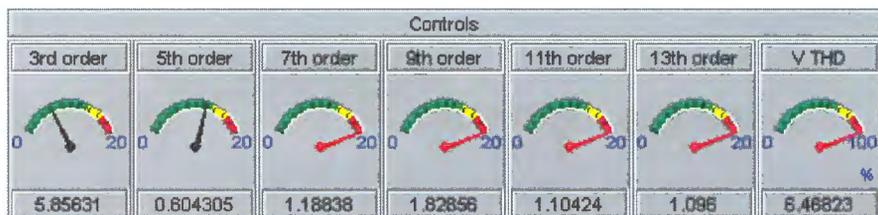


Figure: 5.16 Voltage distortion measurements (Total harmonic distortion levels)



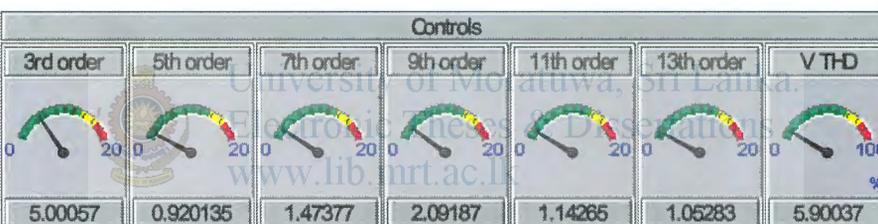
For maximum load with all capacitor banks



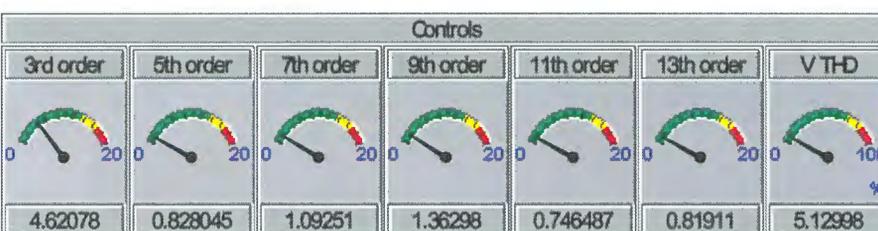
For Maximum load with three capacitor banks



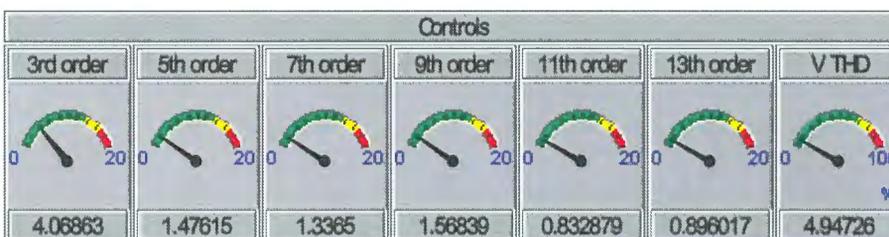
For Maximum load with two filter banks



For Maximum load with one filter and one normal

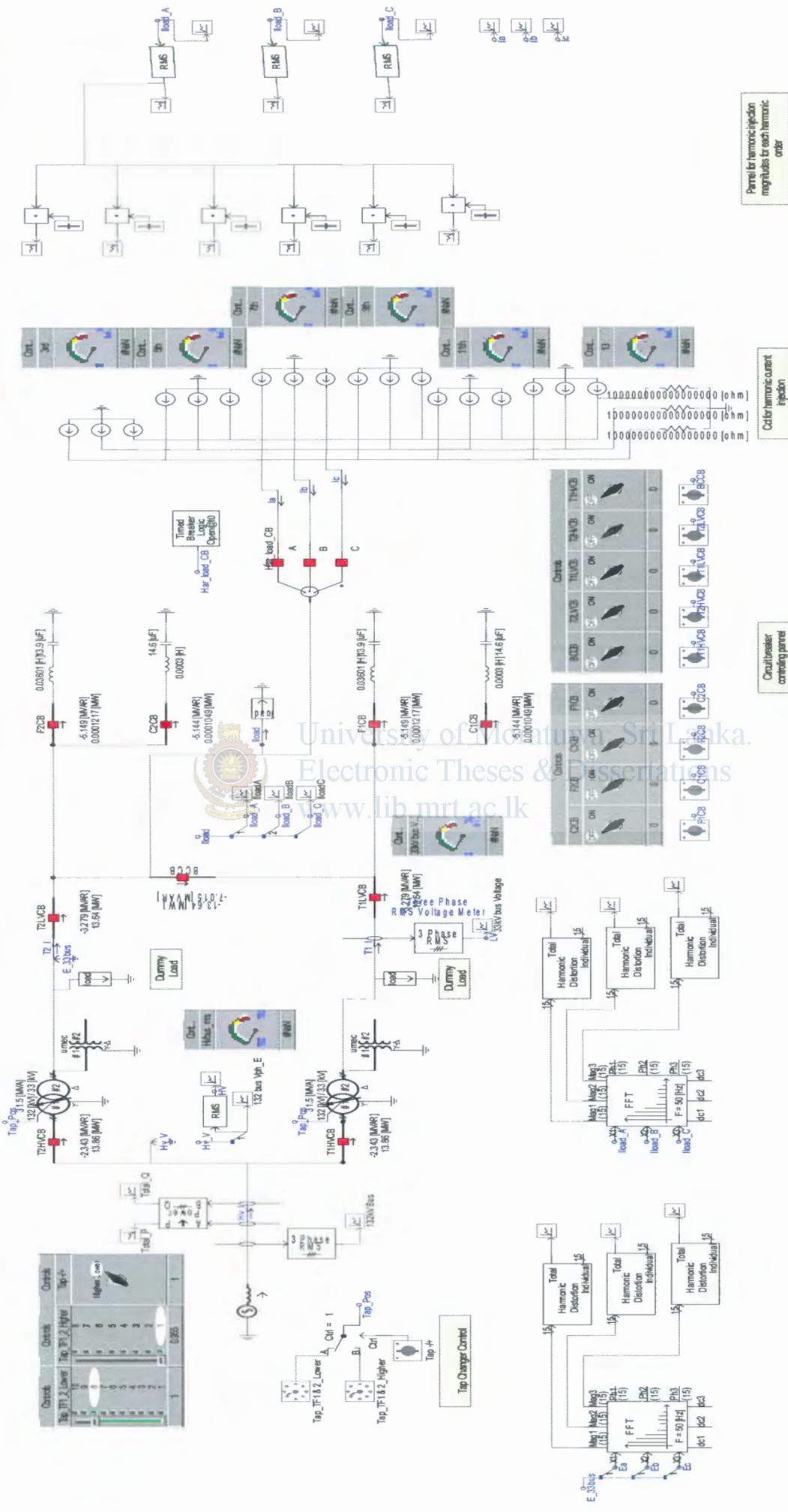


For Maximum load with one filter



For Maximum load with no filter

Figure: 5.17 Voltage distortion measurements
(Individual harmonic distortion levels)



Panel for harmonic injection magnitudes for each harmonic order

Cell for harmonic current injection

Circuit breaker controlling panel

Figure: 5.18 Complete PSCAD model for voltage distortion analysis

6. A Solution for switching

As discussed so far, it is clear that the power factor regulation with present parameters does neither maximize nor optimize the use of the capacitor banks installed in the selected grid substation where the losses and voltage support is concerned. The time periods in which all the capacitor banks are not switched while having an opportunity for that, were observed. Clearly, there is an opportunity to further utilize the already installed banks to reduce the losses by reducing reactive power drawn from sources and to use as an economical voltage stabilizer. The utilization of capacitor banks with the present power factor/var combined scheme is as in the figure 6.1 below. The switching pattern if pure power factor control is used is also included in the figure.

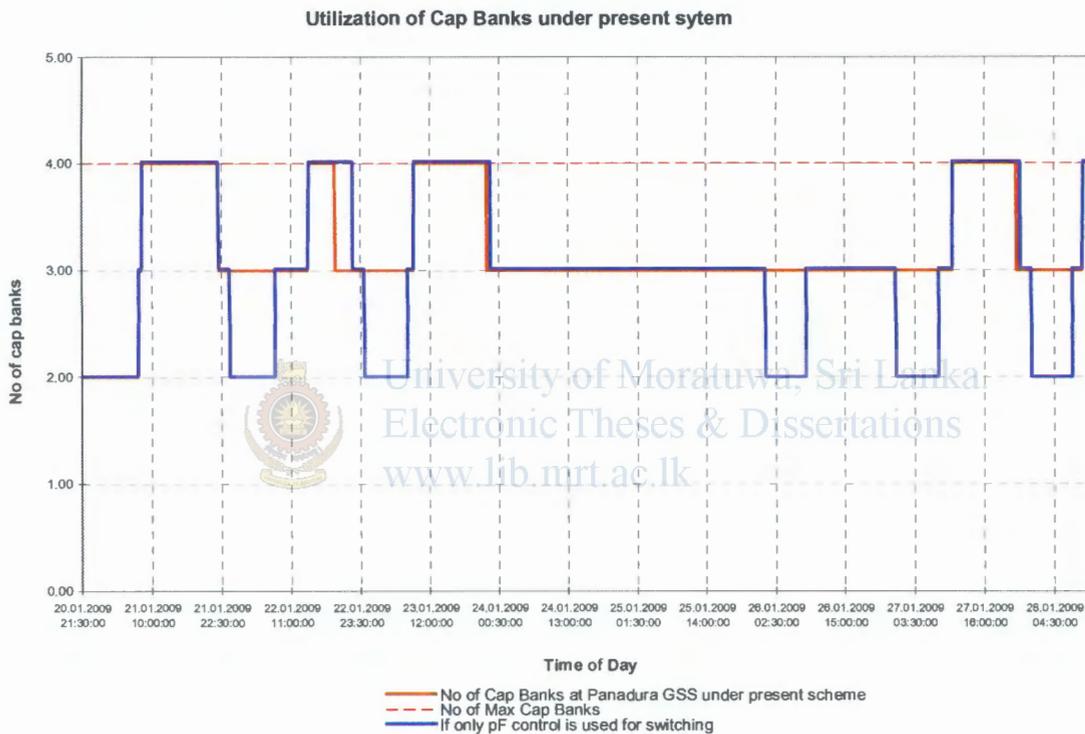


Figure: 6.1 switching pattern under present criteria

Considering the above data, the utilization of the capacitor banks calculated on daily average and with reference to maximum utilization is about 75 %. With the pure power factor control, this becomes to 70.03 %.

Calculation based on; $Utilization = (Mvar_1 * t_1 + Mvar_2 * t_2 + \dots + Mvar_n * t_n) / Maximum\ Mvar * 24$
 $Mvar_n =$ switched capacitor rating at time slot t_n and t_n is taken as 10 min interval)

Although these values are high, it does not indicate the optimality of the use. The present scheme contains unnecessary utilization at certain time periods. Also, it contains periods of partial utilization of capacitor banks even the opportunity is there to fully use them.

In real situation, sometimes the network operators manually switch off the banks to avoid high leading power factor and bus voltage rises or switch on the banks which are already in off position due to improved power factor. Therefore, the high utilization factor is not the

mere deciding factor for the optimal usage. Loss minimization, voltage support, releasing capacity constraints etc., are the factors to be considered.

6.1 Important factors in new switching criteria

As discussed in the previous chapters, the possibility of connecting the maximum number of capacitor banks into the LV bus under any system conditions is obvious. The analysis shows that the harmful effects can be maintained with marginally affecting the regulations and not violating the technical limitations. Therefore, following conclusions can be made.

- For the selected substation, it is possible to connect all four capacitor banks under any system condition.
- Therefore, any other combinational arrangement, to suit the local requirements is also possible.

The first point can be considered in the system point of view. There are situations where the capacitor banks on the distribution substations are kept unused, while having an acute problem of heavy var requirement in transmission system. This happens mostly when power across the company's transmission system does not coincide with load conditions in locations where the capacitor banks are fixed. This situation can be mostly experienced in substations which are heavily interconnected. Under those conditions, keeping a definitely economical reactive energy source underutilized or unutilized depending on local requirements, while generation or some other means producing and transmitting them in the system, is not justifiable.

For substations like Pannipitiya where it has a installed capacity of 100 Mvar and which is still not put into operation due to some technical problems, this kind of approach may be a very economical solution. Power factor regulation with a large reactive power source will not utilize them fully. Other thing is that it is connected to both 220kV system as well as 132kV system sothat the transmission network will be a good tank for reactive power transferred from substation capacitor banks.

When power system economics are considered, CEB has to take advantages of concepts such as "ON Demand Control" to use the already installed capacitor banks in this manner. If the transmission system needs var at a location different to the location where capacitor banks are installed and if a centralized network control center monitor the load flow in its transmission system, then switching of unused capacitor banks at such a time can be used to inject reactive power. This needs a comprehensive load flow study, fully pledged SCADA system and sometimes remote station control fascility etc., to implement the above schemes. Interestingly, those are already in touch with the CEB transmission network. Therefore, if necessary CEB can use its maximum installed capacitor banks without any difficulty.

Secondly, if the first option is not the real requirement of the network, then what is important is to meet local requirements in each substation. As CEB is considered, its main objective is to maintain the bus bar voltage at the desired limit. Reduction of losses, releasing the line and transformer capacities comes as secondary aspects.

In meeting the local requirements, still the voltage and var control may be the best compared to power factor. Power factor is always an indirect measure of reactive power or the system voltage. Power factor does not consider the effects beyond the substation where sometimes it has to consider the bus voltage rise due to line capacitance. During very light loaded

conditions, the line capacitances are predominant and the Ferranti effect comes into effect. In such cases, availability of considerable reactive loads at load centres is a requirement. If the substation reactive power requirement is fully compensated during these periods, the voltage rise at receiving ends will be a problem. In such cases capacitor bank switching based on voltage control may have more benefits. However, the factors like loss minimization, voltage control and the capacity release of the system components can be considered in local station point of view. Providing reactive power from capacitor banks as much as possible to compensate real load requirements while not allowing them to draw from the system is the factor to be considered. This will reduce losses and release the power transformer and transmission line capacity.

6.2 Proposal for switching criteria based on Var control

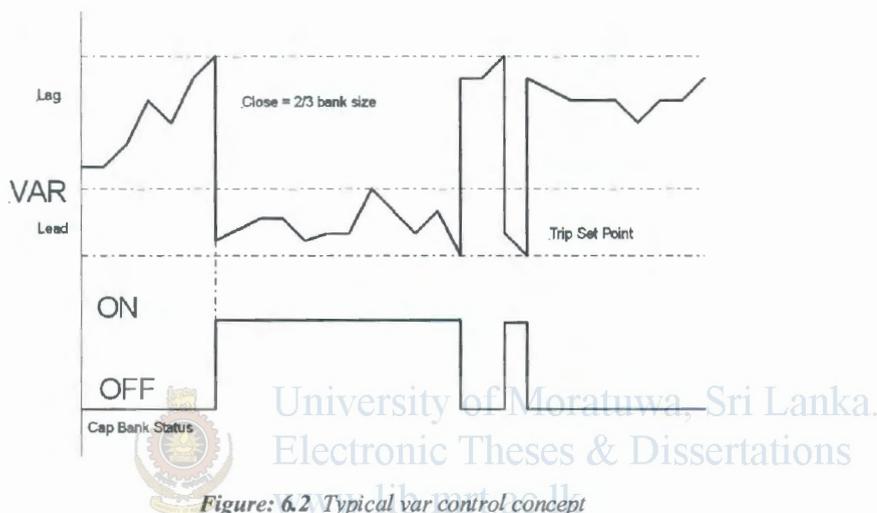


Figure: 6.2 Typical var control concept

Capacitor bank switching based on reactive power requirements is a more flexible and natural means of capacitor control concepts. It adds a fixed amount of lagging reactive power into the system regardless of most other conditions. Since the reduction of losses and the capacity release directly proportional to the reactive current drawn, injecting the reactive power at substation bus level reduces losses beyond bus towards source including the transformer.

In var control based switching, due consideration has to be given to avoid hunting or PUMPING of the banks. Unless the parameters are properly set this purpose cannot be achieved. Hysteresis or restraint control is suggested to avoid such a hunting problem. As shown in figure 6.2, switching "ON" is based on about 2/3 of a step and switching off is based on an amount more than the balance 1/3 of the step in leading direction. These are typically used values decided with experience.

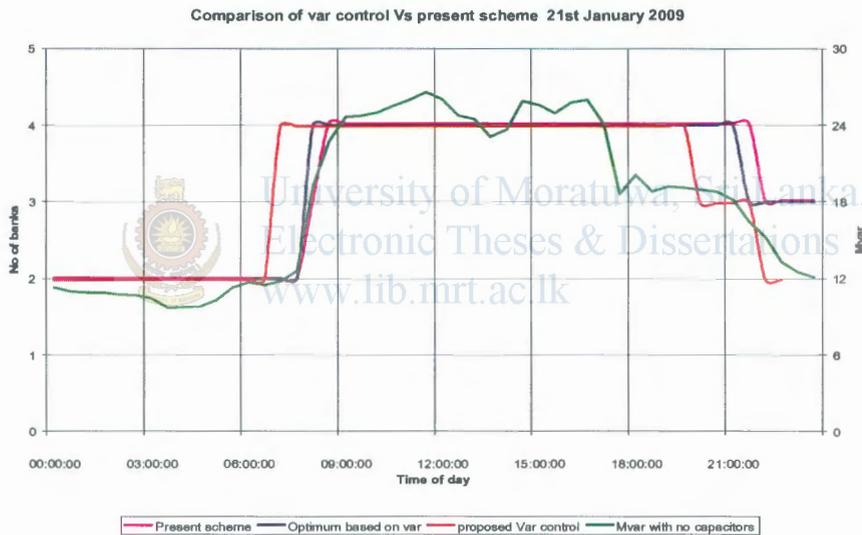
To avoid responding to sudden reactive power changes, restraint control or integration of inputs over certain time period can be used. These are available in most of the capacitor bank controllers.

Considering the above basis, parameters for reactive power control switching for master slave control was suggested as follows. The calculated settings can be used for one setting parameter set, considering master slave control. Second set of parameters is to be defined for the independent mode. Multiple sets of parameters and switching between them depending on

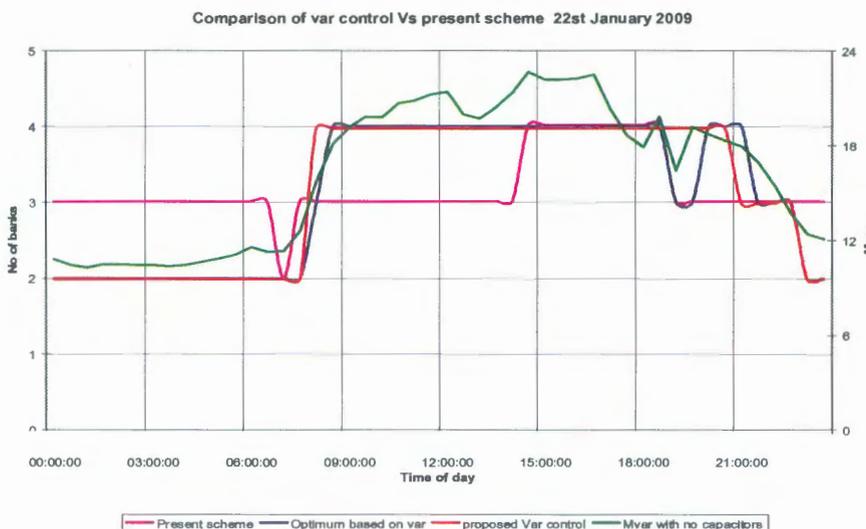
external inputs are regular features in modern controllers. Considering the results obtained by simulations, following points can be considered in a reactive power control based switching criteria for CEB.

- When transformers are paralleled, one controller feels only a half of the capacity of a switched bank.
- Step size of a bank is 5Mvar.
- Switching ON when lagging reactive power exceeds $2.5 * 2/3 = 1.6\text{Mvar}$ (lag)
- Switching OFF when leading reactive power exceeds $(2.5 * 1/3) * 1.4 \approx 1.2\text{Mvar}$ (lead)

Switching points were selected from simulation results with approximated AVR control and shown in figure 6.3. The switching points based on lowest reactive power drawn from system and power factor close to unity (optimum compared to losses) was also show in the diagram.



6.3 (a)



6.3 (b)



6.3 (c)

Figure: 6.3 comparison of switched banks under present, optimum and var control schemes

The three figures show that the proposed switching policy based on reactive power control goes neck to neck with the loss optimized switching pattern than the present switching criteria. No of switching operations were calculated as per the switching points. A typical capacitor bank switch can operate 6times per day considering 50,000 no of operations and 20 years life time. The no of operations of the breakers are within the acceptable limits.

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Date	Number of switching			
	Bank 1	Bank 2	Bank 3	Bank 4
21.01.09	0	0	2	2
22.01.09	0	0	2	2
24.01.09	0	0	1	2

Table: 6.1 No of switching operations under proposed var control scheme

The utilization factor is calculated based on the same criteria described early in the chapter and equals to 80%. The utilization is approximately same as the present system but the new scheme is closer to the loss optimized pattern.

Increase or decrease of energy loss was calculated based on the point that the losses are directly proportional to I^2 . For all three days considered, a decrease of 1.8%, 4.9% and 5.04% was observed and average reduction in energy loss is 3.94%. (Considering only the transformer losses)

From the equation below, it is possible to calculate the capacity release of the substation and hence same capacity must be released from the generation as well [8].

$$\Delta KVA_s = \left[\sqrt{1 - \frac{(KVAR)^2 (\cos\phi)^2}{(KVA_s)^2} + \frac{\sin\phi(KVAR)}{KVA_s}} - 1 \right] KVA_s$$

Where ΔKVA_s - release of substation
 KVA_s - Capacity of substation
 $KVAR$ - Capacity of next step of the banks
 $\cos \Phi$ and $\sin \Phi$ - Cos and sine of power factor before adding next step

For the selected substation, addition of 5Mvar for 2 * 31.5MVA transformers at the conditions as at 8.30hrs on 24th January 2009, the capacity release ΔKVA_s was calculated as,

$$\Delta MVA_s = [\sqrt{\{1-(5 * \cos 7.13/63)^2\}} + \sin 7.13 * (5 / 63) - 1] 63$$

$$= 0.425 \text{ MVA}$$

Date & Time	Under Present criteria								
	MW	Mvar	33 Volt	132 Volt	No of Banks	Ph angle	Utilization	HV A	Tap
24.01.2009 08:30:00	33.01	4.07	32.98	74.97	3	-7.13	7.50	76.08	10

Date & Time	Proposed var control scheme								
	MW	Mvar	33 Volt	132 Volt	No of Banks	Ph angle	Utilization	HV A	Tap
24.01.2009 08:30:00	32.6274	0.89685	32.7843	75.0884	4	1.574976	10.00	73.76	9

 **Table: 6.2** An extract from simulation results to compare capacity release
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With the simulation results it can be calculated as;

$$\Delta MVA_s = \sqrt{(33.01^2 + 4.07^2)} - \sqrt{(32.6274^2 + 0.89685^2)}$$

$$= 0.620 \text{ MVA, But this is with a tap position change as well.}$$

Therefore the simulation results can be justified. Considering the simulation results, total average energy released by switching from present scheme to proposed var control scheme is 15.64 MWh per day (calculated based on 30min sample time hence totalling energy for 30min sample). The scheme maintains the tap close to nominal tap while keeping the 33 kV voltages also within the range. (Refer **Appendix 7**-Data format for reactive power control switching points and summary of results)

6.3 Proposal for switching criteria based on Voltage control

Voltage control based capacitor switching in a utility substation has to follow a complex algorithm. The difficulty in voltage control based switching is due to the voltage regulator of the power transformers. When both functions try to control voltage at the same time without any coordination between them, then there will be severe malfunctioning of the two

controllers. This will cause hunting of capacitor banks and tap changer. Therefore, for such a control scheme, an algorithm to coordinate AVR and capacitor bank controller is required. The factors that has to be considered in such a system are,

- During switching on for decreasing bus bar voltages, the capacitors shall come first if the reactive power load is more than a portion of the minimum step of a bank otherwise the tap changer can increase the voltage. The purpose of this is to minimize the losses and adding excess leading reactive power.
- During switching off for increasing terminal voltages, AVR and the capacitor controller shall follow the same philosophy. The reactive power at the time of decision must be considered in deciding whether to reduce the tap or to switch off a capacitor bank.
- Algorithm for an above control is necessary to optimize the use of capacitor banks. If the only requirement is to control the voltage, then proper dead band selection for two controllers also can serve the purpose.
- Differentiate the integration time, the time period over which the measurement is averaged, also can be used with hysteresis control to make the control philosophy more simple.

One other thing to be considered is that when the network control centre increases the voltage at some other station having no capacitor banks by generator voltage adjustments, the substation having capacitor banks also will feel that and the bus voltage will improve. Then the capacitors will tend to switch off responding to outside voltage adjustments. This is not an economical solution.

A voltage selection scheme based on a hysteresis control as in figure 6.4 is evaluated for comparison with the present and proposed var control schemes. The approximated switching points of capacitor banks based on above voltage control scheme, was selected using the simulation results for 21st, 22nd and 24th January 2009. (*Appendix 8* – Data format for voltage control switching points and summery of results). Figure 6.5 illustrates the comparison of present switching and the switching pattern with the proposed voltage control.

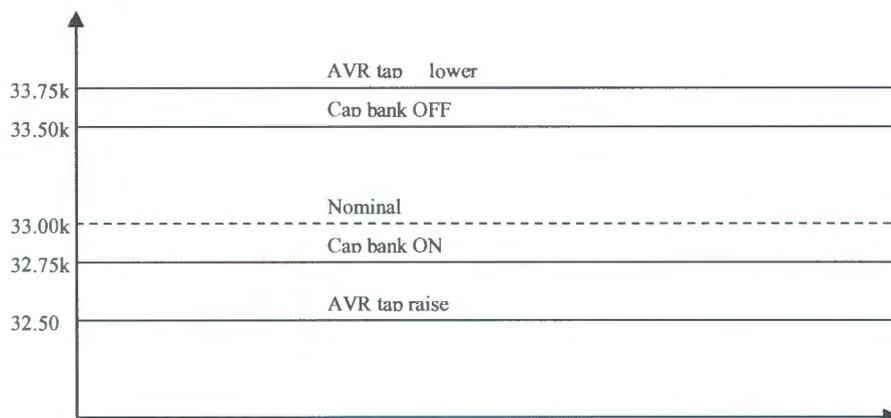
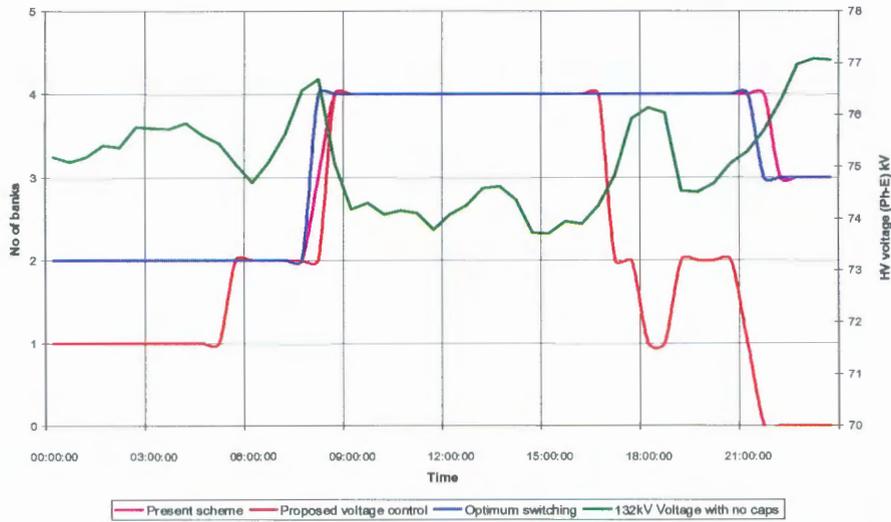


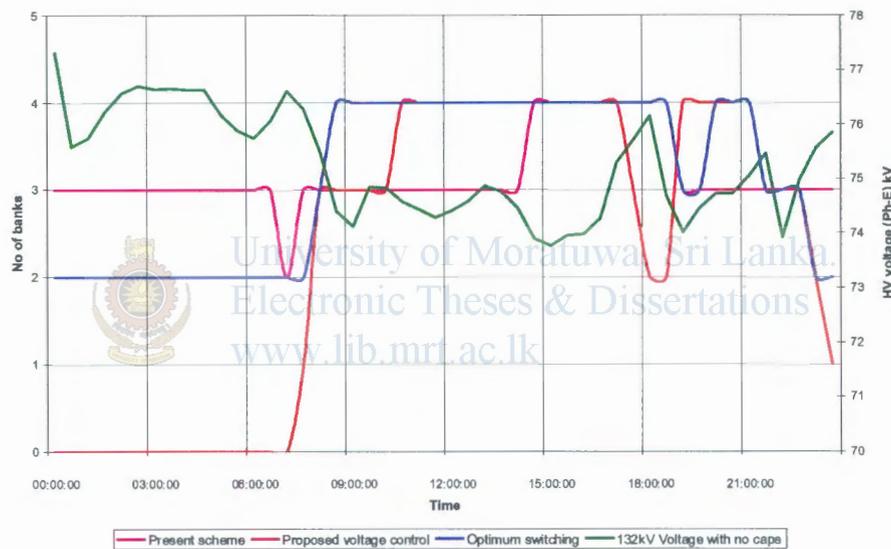
Figure 6.4 Proposal for dead bands for AVR and capacitor controller

Comparioson present scheme Vs voltage control 21st Jan 2009



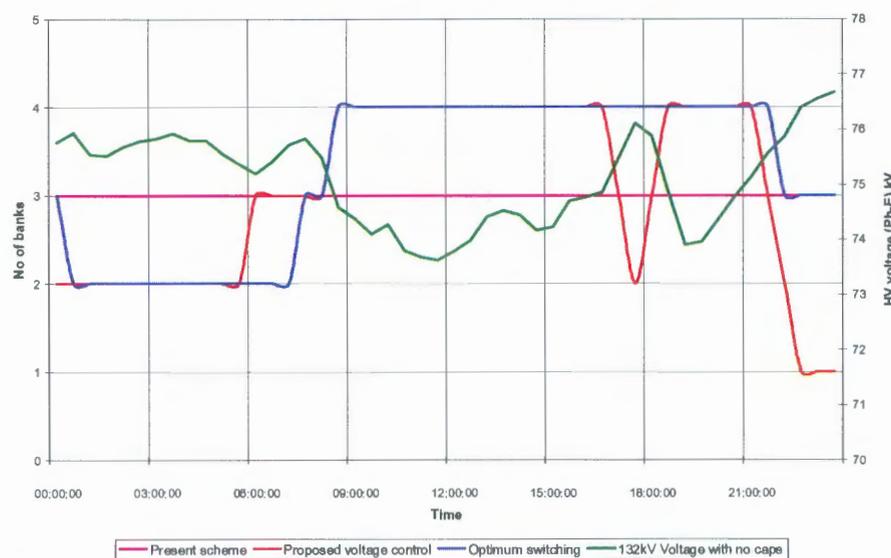
6.5 (a)

Comparioson present scheme Vs voltage control 22st Jan 2009



6.5 (b)

Comparioson present scheme Vs voltage control 24st Jan 2009



6.5 (c)

Figure: 6.5 comparison of switched banks under present and voltage control schemes 21st 22nd & 24th

The figures show that with the voltage control, maximum number of banks is maintained only at day time from around 9.00hrs to 17.00hrs. During mid night, this comes even up to zero banks, due to the voltage rise. The criterion does not maximize the utilization. Gradual switching off of banks around 17.00 to 18.00 hrs also observed due to reduction of loads after office hours. There is a voltage rise during this period and the load rises after that due to lighting. In comparison to the reactive power control, voltage control scheme is not coincides with the optimum curve.

As the data shows, following conclusions can be made.

- Maximum switching operations per 3rd and 4th banks is about 4 so that the switching does not cause any unnecessary impact.
- Utilization when voltage control is used seems to be low compared to loss optimized switching pattern. It is 55%, 58% and 77% on 21st, 22nd and 24th respectively..
- Due to reduced utilization, energy losses and substation capacity release also not be economical. However it matches with the voltage properly. Therefore, if the need is to give voltage support, then this kind of switching policy is very satisfactory.

6.4 Optimum switching solution

In operating a utility network under real conditions, some dominant local requirements that needed to be controlled by capacitor banks are to be decided. As CEB is concerned, due to the factor of concentrating the generation to certain localized areas, maintaining voltage stability is a considerable factor. Although reactive power control capacitor switching gives more benefits, sometimes it may need to switch off the capacitors in low load conditions due to higher receiving end voltages although the station at which capacitors are connected could deal its voltage rise. In that sense, voltage controlled switching can be an optimized solution for CEB although it is not economical in some aspects described earlier.

Reactive power controlled switching aiming to manually switch off the banks at low load conditions to avoid voltage effects at remote ends was evaluated. The results are shown in figures 6.7 & 6.8 (OFF from 22.30hrs to 7.00hrs next day).

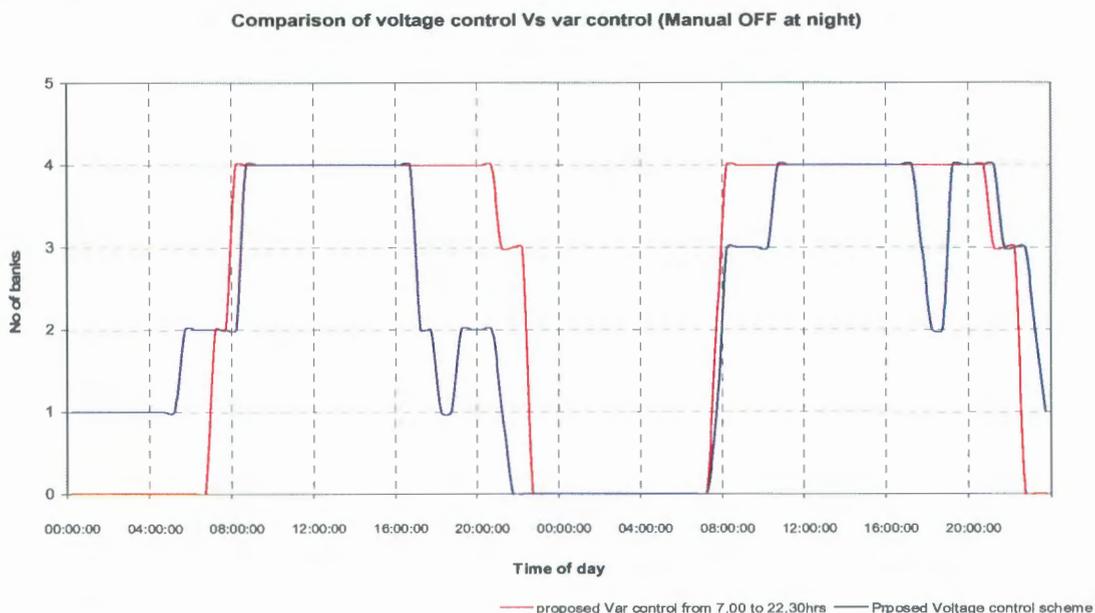


Figure: 6.6 comparison of switched banks under voltage control schemes & var control with manual off - 21st & 22nd

Comparison of voltage control Vs var control (Manual OFF at night) - 24th Jan

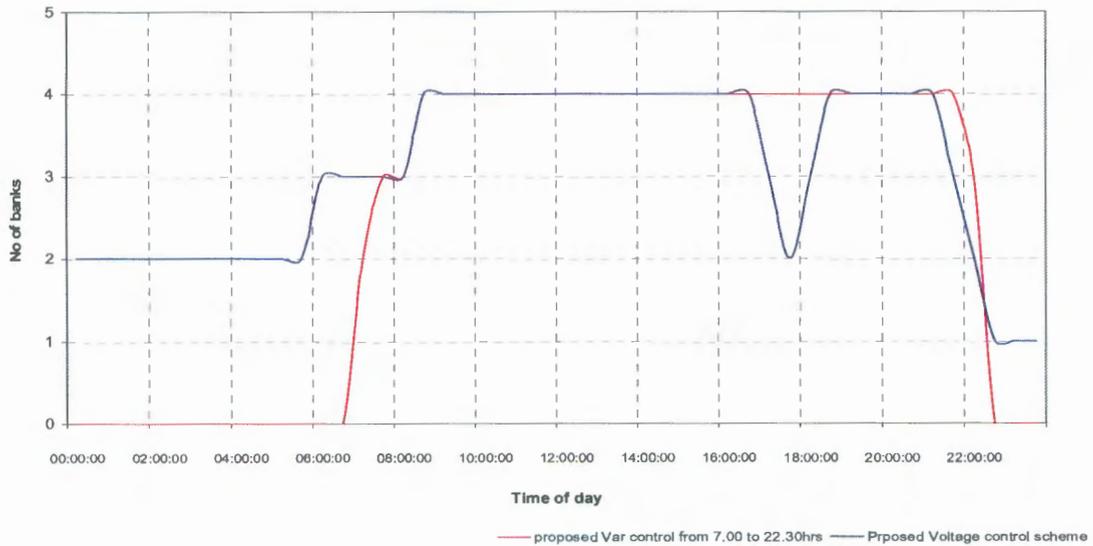


Figure: 6.7 comparison of switched banks under voltage control schemes & var control with manual off - 24th Jan

As we see from the drawings if reactive power controlled switching can be used as above, it can be useful. The disadvantage is the functionality of such a manual auto mixed control. However, if both voltage and reactive power combined controller having multiple variable or Boolean switching controllers can be used to switch the banks considering voltage and var, it could be a good idea.



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7. Conclusion and recommendations

7.1 Analysis and results

- i. Using capacitor banks at 33kV sub distribution level to compensate reactive power requirement and therein, to maintain voltage stability at same level is economical and effective in the CEB system.
- ii. Occasions where the capacitor banks are switched ON and OFF manually by overriding the auto controller was frequently observed. This says that the switching criteria are not fully fit to the requirements in CEB system. The observations also show that present switching criteria at the selected substation neither maximize nor optimize the utilization.
- iii. Simulations with **PSCAD** models prove the technical feasibility of maximum capacitor bank connections to the point at which they are fixed without violating the standards. Voltage rise due to reactive power injection, effects to voltage distortion and resonance due to harmonics with additional capacitor banks, switching capabilities of the on-load tap changer and the capabilities of AVR to handle voltage variations due to reactive power injection are the factors considered in the PSCAD simulations. The results and analysis reveals that it is possible to achieve the purpose without violating otherwise maintaining below the recommended limits of all relevant parameters.
- iv. **PSCAD** simulations indicates that the maximum voltage rise under different capacitor bank combinations (with effective Tap control) for 21st, 22nd & 24th are 77.57kV, 77.8kV & 77.17kV respectively. The maximum percentage rise for high voltage side is .33% and that for low voltage side is 0.95%.
- v. For the worst case of conditions (which will never be allowed by the network operators),
 - Maximum continues voltage at 132 bus bar is 145kV
 - The minimum sub station load 17.2MW+9.6Mvar**PSCAD** simulations indicate that the maximum low voltage rise is 3.8% and that for HV side is 0.56%.
- vi. In the case of effects due to resonance for the selected substation, **PSCAD** simulation results shows that it occurs at an inter-harmonic condition in between 4th and 5th harmonics under any load condition or under any capacitor bank / transformer combination. Normally, the system does not have such inter-harmonics as per the harmonic measurements recorded for the selected sub station. For other sub stations also, such harmonics are not present.
- vii. **PSCAD** simulation results indicate that the highest impedance points seen by the harmonic currents sometimes fall at inter-harmonics and sometimes on harmonic frequencies. The harmonics at which these happens slightly changes with the configuration and load as well. However, the voltage distortion levels remains marginally below 8% which is the accepted level [11].

- viii. Local voltage variation due to added reactive power can be handled by the AVR and tap changer controls so that any combination of banks is feasible to connect.
- ix. The current through the tap changer does not exceed its switching capacity.
- x. Reactive power controlled based switching is a very much economical method of capacitor bank controlling as far as the utilization, loss reduction and capacity release is concerned. Only problem a utility may face is that, some times especially in light load conditions with long transmission lines, there may be a necessity to have some reactive power to reduce the Ferranti effects. In such cases, minimizing reactive power consumption is not desired.
- xi. In real sense, for a utility like CEB where most of the generation is concentrated to certain areas, maintaining voltage stability may be a real challenge than reducing losses using capacitor banks. In such a, voltage control based capacitor switching will be a good solution.

7.2 Conclusion

Considering all these factors discussed so far, followings are the conclusions from this research study.

- i. Present capacitor bank switching philosophy based on power factor regulation does not give maximum benefits to the CEB transmission network. This scheme neither maximizes nor optimises the utilization.
- ii. Considering the installed capacities and step sizes in each substation, it is technically possible to utilize the full installed capacities in all substations without violating the technical standards.
- iii. Therefore, it is technically feasible to back feed the excess capacitor bank capacity for reactive power compensation in the transmission network.
- iv. Use of a switching policy based on reactive power control or voltage control is more useful as far as the CEB system is considered. Reactive power based switching which is simple, is useful for loss minimization and voltage based control is useful when voltage stability is concerned.
- v. Considering the factors discussed in 7.1 viii and ix, for network like CEB, it is useful to consider the controllers with multi-parameter or Boolean switching options. Reactive power and voltage can be the parameters to be considered in the switching decisions.

7.3 Recommendations for future studies

When introducing a switching criterion based on the voltage, the co-relation between AVR loop and capacitor controller loop is an important factor and need to be studied in details. Therefore it is recommended to study an algorithm to correlate these two control loops who tries to control the same parameter at the same time, to avoid unnecessary pumping of capacitor banks and hunting the tap changer.

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Appendix 1(a) – Capacitor bank details in CEB system

GSS	Point of connection	Mvar Rating	Currently Available	Configuration	Bank type 1		Bank type 2	
					C / Phase (μF)	L / Phase (mH)	C / Phase (μF)	L / Phase (mH)
Habarana	33kV BB	10	10	t11-1x5Mvar(type1)	14.6	0.300	13.9	36.01
				t11-1x5Mvar(type2)				
				t11-2x5Mvar(type1 & 2 each)	14.6	0.300	13.9	36.01
				t12-2x5Mvar(type1 & 2 each)				
Panadura	33kV BB	20	20	t11 & 3-2x5Mvar(type1 & 2 each)	14.6	0.300	13.9	36.01
				t12-2x5Mvar(type1 & 2 each)				
				t11-2x5Mvar(type1 & 2 each)	14.6	0.300	13.9	36.01
				t12-2x5Mvar(type1 & 2 each)				
Kiribathkumbura	33kV BB	20	20	t11-1x5Mvar(type1)	14.6	0.300	13.9	36.01
				t11-1x5Mvar(type2)				
				t11-2x5Mvar(type1 & 2 each)	14.6	0.300	13.9	36.01
				t12-2x5Mvar(type1 & 2 each)				
Puttalam	33kV BB	20	20	t11-1x5Mvar(type1)	14.6	0.300	13.9	36.01
				t11-1x5Mvar(type2)				
				t11-2x5Mvar(type1 & 2 each)	14.6	0.300	13.9	36.01
				t12-2x5Mvar(type1 & 2 each)				
Kurunegala	33kV BB	10	10	t11-1x5Mvar(type1)	14.6	0.300	13.9	36.01
				t11-1x5Mvar(type2)				
				t11-2x5Mvar(type1 & 2 each)	14.6	0.300	13.9	36.01
				t12-2x5Mvar(type1 & 2 each)				
Galle	33kV BB	20	20	t11-2x5Mvar(type1)	14.6	0.100		
				t12-2x5Mvar(type1)				
				t11 & 3-2x5Mvar(type1)	14.6	0.100		
				t12-2x5Mvar(type1)				
Old Anuradapura	33kV BB	20	20	t11-2x5Mvar, t12-2x5Mvar	14.6	0.100		
				t11-2x5Mvar, t12-2x5Mvar				
				t11-2x5Mvar, t12-2x5Mvar	14.6	0.100		
				t12-2x5Mvar, t11-2x5Mvar				
Kotugoda (stage 1)	33kV BB	20	20	t11-2x5Mvar, t12-2x5Mvar	14.6	0.100		
				t11-2x5Mvar, t12-2x5Mvar				
Kotugoda (stage 2)	33kV BB	30	30	t11-3x5Mvar, t12-3x5Mvar	14.6	0.255		
				t11-3x5Mvar, t12-3x5Mvar				
Athurugiriya	33kV BB	20	20	Not energized due to technical problems				
				Not energized due to technical problems				
Thulhiriya	33kV BB	10	10	Not energized due to technical problems				
				Not energized due to technical problems				
Pannipitiya	A.TF 33kV winding	100	100	Not energized due to technical problems				
				Not energized due to technical problems				

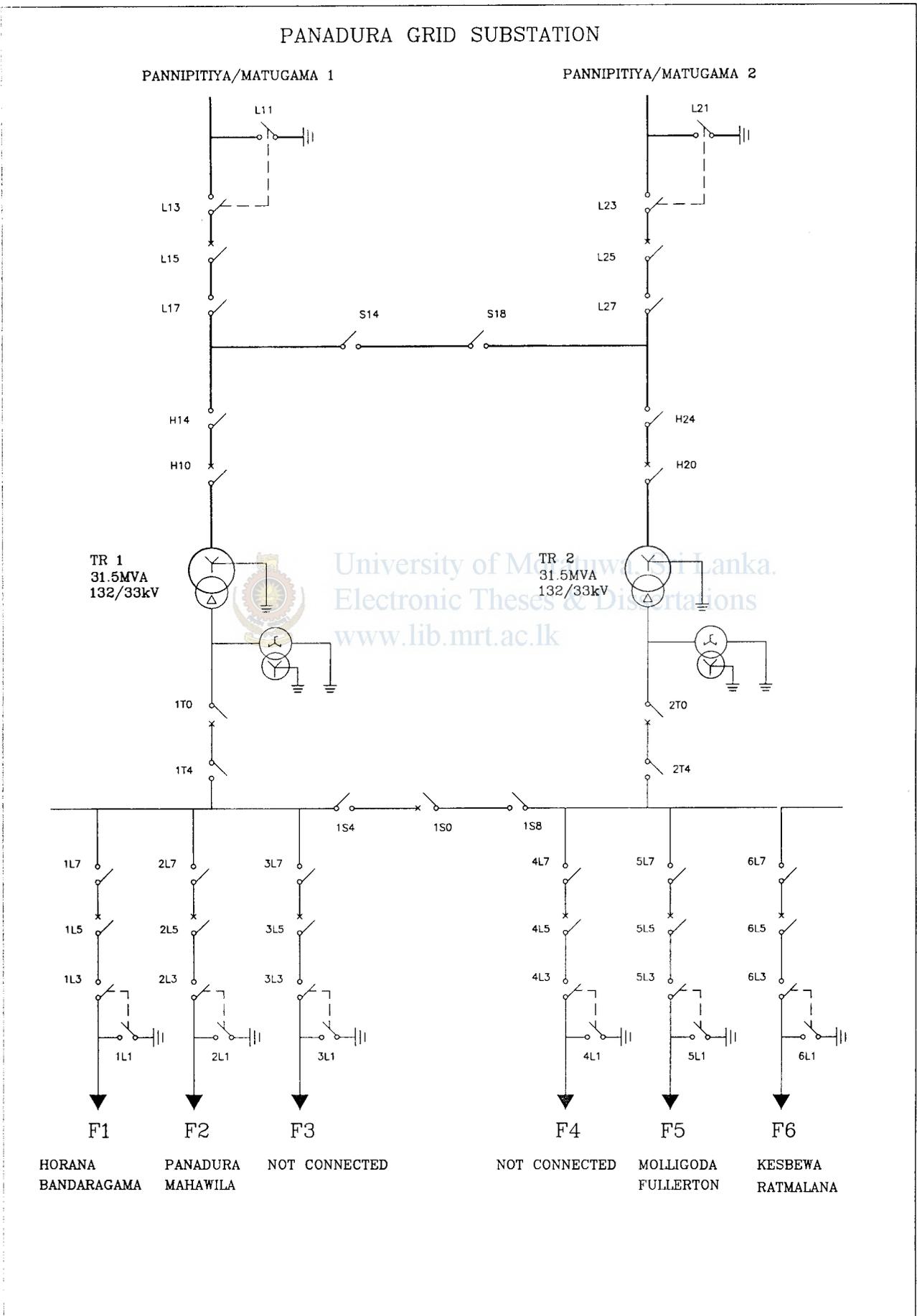
GSS	Fault Level (kA)		Transformers				Earthing TF				
	132kV bus	220kV bus	No	Type	Rating	Vector	Taps	AVR	% Impedance	Rating	Vector
***	5.5		1,2	HYUNDAI TL 666	23 31.5 MVA	YNdI	18	MR VIII 350 Y	.1046 at 31.5 MVA	0.2	ZNyn11
***	12.5		1,2	HYUNDAI TL 288	23 31.5	YNdI	18	MR VIII Y350	.1000 at 31.5 MVA	0.2	ZNyn11
***	9.6		1,2	EGB. LINZ. DOR35500 130E	23 31.5 MVA	YNdI	13	MR VIII 200 Y 60	.1090 at 31.5 MVA	0.2	ZNyn11
***	5.2		3	PAUWELS TRAF0 13	73 - 100% 31.5MVA	YNdI	13	MR MS III 300-72.5-FD	.1090 at 31.5 MVA	0.2	ZNyn11
***	5.1		1,2	HYUNDAI TL 288	23 31.5	YNdI	18	MR VIII Y350	.1000 at 31.5 MVA	0.2	ZNyn11
***	5.1		1,2	PAUWELS TRAF0 19	73 - 100% 31.5MVA	YNdI	19	MR MS III 300-72.5-FD	.1000 at 31.5 MVA	0.2	ZNyn11

*	5		1	ALSTHOM SAVOISIENNE	23.1 30MVA	YNdI	21	ALSTHOM MAC 27	.1040 at 30 MVA	0.2	ZNyn11
			2		31.5				.1029 at 31.5 MVA		
*	8.2		1,2	HYUNDAI TL 288	23 31.5	YNdI	18	MR VIII Y350	.0998 at 31.5 MVA	0.2	ZNyn11
			3	PAUWELS TRAF0 18	73 - 100% 31.5MVA	YNdI	18	MR MS III 300-72.5-FD	.1000 at 31.5 MVA	0.2	ZNyn11
*	6.5		1	ABB ELTA TNARCA 31500 132PT	23 31.5 MVA	YNdI	18	MR V III 200 Y 60 1081G	.1000 at 31.5 MVA	0.2	ZNyn11
			2	ALSTHOM SAVOISIENNE THGE 145 11000	10 MVA	YNdI	21	ALSTHOM K4900	.1000 at 10 MVA	0.2	ZNyn11

*	18	19	1,2	TAKAOKA AUTOTRANSFORMER	200.3 / 250.3 HV 60.3 LV	YNa0dI	13 (MV) / 13 (LV)	MR	II-M 0.138 at 250 MVA	0.2	ZNyn11
**									H-I 0.899 at 250MVA		
									M-L 0.899 at 250MVA		

*	Athurugiriya										
*	Thulhiriya										
*	Pannipitiya										

Appendix 1(b) – Substation arrangement – Panadura Grid sub station



Appendix 2(b) - Comparison of measured tap with no capacitor banks and all capacitor banks

Time of day	With capacitors				Without capacitors											
	Day 1	Day 2	Day 3	Day 4	Day 5	Day 6	Day 7	Day 8	Day 9	Day 10	Day 11	Day 12				
0.00		7	8	10	11	9	10	11	10	10	10	9				
0.30		7	8	10	11	10	10	11	10	10	10	9				
1.00		7	8	10	11	10	10	11	10	10	10	9				
1.30		7	8	9	11	10	10	11	10	10	10	9				
2.00		7	8	9	11	10	10	11	10	10	10	9				
2.30		7	8	9	11	10	10	11	10	10	10	9				
3.00		7	8	9	11	10	10	11	10	10	10	9				
3.30		7	8	9	10	10	10	11	10	10	10	9				
4.00		7	8	9	10	10	10	11	10	10	10	9				
4.30		7	8	9	10	10	10	11	10	10	10	9				
5.00		7	8	9	10	10	10	11	10	10	10	9				
5.30		8	9	9	11	10	10	11	10	10	11	10				
6.00		9	9	9	11	10	11	11	10	10	11	10				
6.30		9	9	9	11	10	11	11	10	10	11	10				
7.00		8	8	9	11	10	10	11	10	10	11	10				
7.30		8	8	9	10	10	10	11	10	10	11	10				
8.00		9	8	9	10	11	11	11	10	11	11	10				
8.30		10	9	10	12	12	12	12	10	12	12	10				
9.00		10	9	11	13	12	12	12	10	12	13	10				
9.30		10	10	11	13	12	13	12	10	12	13	10				
10.00		10	10	11	13	12	13	12	10	13	13	10				
10.30		10	10	11	13	12	13	13	12	13	13	10				
11.00		10	11		13	12	13	13	12	13	13	10				
11.30		10	11		13	12	13	13	12	13	14	10				
12.00	11	10	11		13	12	13	13	12	13	14					
12.30	11	10	10		13	12	13	13	12	13	14					
13.00	10	10	10		13	12	12	13	12	13	13					
13.30	10	10	10		12	12	12	13	12	13	13					
14.00	11	10	10		13	12	12	13	12	13	13					
14.30	11	10	11		13	13	13	13	12	13	13					
15.00	11	11	11		13	13	13	13	12	13	13					
15.30	11	11	11		13	13	13	12	12	13	13					
16.00	11	11	11		13	13	13	12	12	13	13					
16.30	11	11	11		13	13	13	12	11	13	13					
17.00	10	10	10		12	12	12	12	11	11	12					
17.30	9	9	9		12	12	11	11	11	11	12					
18.00	9	9	9		11	11	11	11	11	11	11					
18.30	9	9	9		11	12	13	12	11	12	12					
19.00	10	10	11		12	13	13	13	12	13	13					
19.30	10	10	11		12	12	13	13	12	13	13					
20.00	10	10	11		12	12	13	13	12	13	13					
20.30	10	10	11		12	12	12	13	12	13	12					
21.00	10	10	10		12	12	12	12	12	12	12					
21.30	9	9	8		11	12	11	12	11	11	11					
22.00	8	9	7		11	13	11	11	10	10	11					
22.30	8	9	8		10	12	11	11	10	10	10					
23.00	8	9	10		10	11	10	10	10	10	10					
23.30	8	9	10		10	11	11	10	10	10	9					

**APPENDIX 3(a) - Format for results on network simulation- PSCAD file for
21st January 2009**

Simulation Data for 21.01.2009							
Multiple Run Output File 21_0000_0banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	77.00524558	33.48471654	-28.91023986	20.78008205	11.47616236	1165057576
2	9850000000	77.01242372	32.98050846	-28.91024104	20.15900734	11.13316304	1130230792
3	1.0000000000	77.01928109	32.49117551	-28.91024039	19.56526546	10.80525875	1096948103
4	1.0150000000	77.02583653	32.01607269	-28.91023964	18.99728158	10.49157976	1065119720
1	9550000000	75.18346966	33.18511965	-28.90357332	20.40597652	11.26835132	7713554950E-01
Multiple Run Output File 21_0000_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	77.13063993	33.80064876	-16.97142608	21.17382970	6.461996056	1074130153
2	9850000000	77.13405939	33.29007976	-16.97142261	20.53903229	6.268262417	1042006975
3	1.0000000000	77.13732411	32.79464368	-16.97142103	19.93228385	6.083089504	1011310896
4	1.0150000000	77.14044329	32.31368014	-16.97141958	19.35196030	5.905981935	9819587508E-01
Multiple Run Output File 21_0000_2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	77.25832347	34.12434875	-3.338005934	21.57771560	1.258468439	6794454748E-01
2	9850000000	77.25789239	33.60726631	-3.339223622	20.92905232	1.221310032	6591664180E-01
3	1.0000000000	77.25750360	33.10557628	-3.337917828	20.30879510	1.184935209	6397523240E-01
4	1.0150000000	77.25712171	32.61861369	-3.335508293	19.71528735	1.149592704	6211487648E-01
5	1.0150000000	77.25712171	32.61861369	-3.335508293	19.71528735	1.149592704	6211487648E-01
Multiple Run Output File 21_0000_3banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	77.38861778	34.45230995	10.69121078	21.99452264	-4.151149007	6915652900E-01
2	9850000000	77.38426922	33.92853348	10.68872963	21.33060859	-4.026310122	6708021276E-01
3	1.0000000000	77.38008298	33.42055259	10.68597805	20.69732342	-3.905589441	6510284018E-01
4	1.0150000000	77.37613631	32.92748610	10.68736488	20.09086688	-3.792102475	6320747924E-01
5	1.0300000000	77.37229255	32.44874187	10.68637580	19.51109591	-3.681892354	6140103059E-01
Multiple Run Output File 21_0000_4banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	77.52127304	34.78613246	23.53346296	22.42309594	-9.765548278	7427919654E-01
2	9850000000	77.51293442	34.25562904	23.53508901	21.74406272	-9.470916070	7205608893E-01
3	1.0000000000	77.50495217	33.74110889	23.53504725	21.09589055	-9.188312885	6991474587E-01
4	1.0150000000	77.49730680	33.24172701	23.53789338	20.47591534	-8.918656265	6787006581E-01
5	1.0300000000	77.48998281	32.75699467	23.53466724	19.88322808	-8.660277677	6591994194E-01
6	1.0450000000	77.48296363	32.28621325	23.53510204	19.31578806	-8.413063256	6405660403E-01
Multiple Run Output File 21_0030_0banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	77.00524558	33.48694984	-28.91023986	20.78008205	11.47616236	7661913899E-01
2	9850000000	77.01242372	32.98271334	-28.91024104	20.15900734	11.13316304	7432886575E-01
3	1.0000000000	77.01928109	32.49335258	-28.91024039	19.56526546	10.80525875	7214012965E-01
4	1.0150000000	77.02583653	32.01822255	-28.91023964	18.99728158	10.49157976	7004702474E-01
Multiple Run Output File 21_0030_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	77.13063993	33.80286647	-16.97142608	21.17382970	6.461996056	7063815449E-01
2	9850000000	77.13405939	33.29226967	-16.97142261	20.53903229	6.268262417	6852573129E-01
3	1.0000000000	77.13732411	32.79680637	-16.97142103	19.93228385	6.083089504	6650712117E-01
4	1.0150000000	77.14044329	32.31581619	-16.97141958	19.35196030	5.905981935	6457687843E-01
Multiple Run Output File 21_0030_2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.39278847	33.27852734	-2.670006829	19.63244992	9.154634266	6338808721E-01
2	9850000000	75.39223421	32.77409036	-2.669217346	19.04162845	8.876354555	6149058731E-01
3	1.0000000000	75.39170817	32.28464290	-2.668665422	18.47697173	8.608543472	5967500481E-01
4	1.0150000000	75.39117052	31.80967227	-2.670789836	17.93781681	8.367669027	5794394097E-01
Multiple Run Output File 21_0030_3banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.52006321	33.59854148	11.95011566	20.01172131	-4.235325610	6478155760E-01
2	9850000000	75.51568776	33.08763200	11.95050773	19.40792785	-4.107581368	6284184710E-01
3	1.0000000000	75.51148304	32.59202894	11.955121222	18.83061055	-3.985746708	6098139862E-01
4	1.0150000000	75.50744840	32.11102547	11.95077679	18.27910791	-3.868604512	5921376496E-01
Multiple Run Output File 21_0030_4banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.64971731	33.92456369	25.15776741	20.40169061	-9.581636754	7013364073E-01
2	9850000000	75.64137957	33.40697012	25.15464744	19.78399641	-9.290850358	6803271481E-01
3	1.0000000000	75.63344434	32.90492621	25.15622969	19.19382981	-9.014038178	6601222164E-01
4	1.0150000000	75.62581500	32.41782706	25.15851896	18.62985243	-8.749207225	6408217049E-01
Multiple Run Output File 21_0100_0banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9550000000	75.21320236	33.25382425	-29.86454626	18.94153074	10.87636156	7224249489E-01
2	9700000000	75.14556977	32.65648976	-29.37426187	18.90572067	10.64172559	7188783677E-01

APPENDIX 4- Data format for measured data in Panadura Grid substation with all capacitor banks connected

Date & Time	Transformer 1 - 33 kv Side Data												Transformer 2 - 132 Side Data												Transformer 1 - 132 Side Data																			
	Vvar				Power factor				Ph Volt/ 33kv Bus				Vvnt				Power Factor				Ph Volt/ 132 kv Bus				VW				Power Factor				Vvnr				Power Factor							
	L1	L2	L3	L4	L1	L2	L3	L4	L1	L2	L3	L4	L1	L2	L3	L4	L1	L2	L3	L4	L1	L2	L3	L4	L1	L2	L3	L4	L1	L2	L3	L4	L1	L2	L3	L4	L1	L2	L3	L4				
18.02.2009-12:00:00	5.15	6.23	6.25	6.60	0.65	0.72	0.30	0.33	33.25	33.30	33.35	33.40	0.99425	0.99437	0.99449	0.99461	74.26	74.32	74.38	74.44	5.33	6.42	6.43	6.71	0.15	0.21	0.25	0.28	0.9900	0.9900	0.9900	0.9900	74.22	74.41	74.86	74.96	6.36	6.38	6.05	6.48	0.18	0.18	0.00	0.00
18.02.2009-12:10:00	6.05	6.14	6.16	6.59	0.62	0.70	0.33	0.33	33.28	33.32	33.36	33.40	0.99437	0.99449	0.99461	0.99473	74.39	74.45	74.50	74.56	6.22	6.30	6.32	6.60	0.10	0.10	0.18	0.18	0.9900	0.9900	0.9900	0.9900	74.35	74.51	74.96	75.02	6.27	6.28	6.01	6.42	0.14	0.14	0.00	0.00
18.02.2009-12:20:00	6.00	6.09	6.11	6.49	0.59	0.66	0.33	0.33	33.33	33.37	33.41	33.45	0.99449	0.99461	0.99473	0.99485	74.46	74.52	74.57	74.63	6.15	6.24	6.23	6.52	0.08	0.08	0.16	0.16	0.9900	0.9900	0.9900	0.9900	74.38	74.57	75.02	75.03	6.20	6.20	6.01	6.43	0.13	0.13	0.00	0.00
18.02.2009-12:30:00	5.85	5.95	5.96	6.35	0.37	0.45	0.33	0.33	33.62	33.66	33.70	33.74	0.99473	0.99485	0.99497	0.99509	74.80	74.90	74.95	75.00	6.09	6.22	6.18	6.47	0.05	0.04	0.04	0.04	0.9900	0.9900	0.9900	0.9900	74.83	74.99	75.43	75.43	6.20	6.18	6.15	6.26	0.01	0.01	0.00	0.00
18.02.2009-12:40:00	5.87	5.97	5.97	6.33	0.37	0.44	0.33	0.33	33.59	33.64	33.68	33.72	0.99485	0.99497	0.99509	0.99521	74.99	75.10	75.15	75.20	5.91	6.03	6.05	6.34	0.26	0.26	0.12	0.12	0.9900	0.9900	0.9900	0.9900	74.92	75.11	75.53	75.53	6.24	6.10	6.09	6.12	0.15	0.15	0.00	0.00
18.02.2009-12:50:00	5.86	5.95	5.95	6.28	0.33	0.38	0.33	0.33	33.50	33.54	33.58	33.62	0.99497	0.99509	0.99521	0.99533	74.98	74.98	74.98	74.98	5.92	6.05	6.03	6.32	0.20	0.20	0.11	0.11	0.9900	0.9900	0.9900	0.9900	74.95	75.05	75.53	75.53	6.25	6.00	6.00	6.12	0.15	0.15	0.00	0.00
18.02.2009-13:00:00	5.84	5.90	5.92	6.26	0.31	0.36	0.33	0.33	33.59	33.63	33.67	33.71	0.99509	0.99521	0.99533	0.99545	74.99	75.05	75.10	75.15	5.86	5.97	5.96	6.25	0.21	0.19	0.19	0.19	0.9900	0.9900	0.9900	0.9900	74.96	75.11	75.59	75.59	5.95	5.96	6.32	6.05	0.22	0.22	0.00	0.00
18.02.2009-13:10:00	5.91	5.99	5.98	6.28	0.34	0.36	0.33	0.33	33.64	33.68	33.72	33.76	0.99521	0.99533	0.99545	0.99557	74.95	74.95	74.95	74.95	5.90	5.98	6.00	6.29	0.22	0.19	0.19	0.19	0.9900	0.9900	0.9900	0.9900	74.86	75.08	75.53	75.53	5.98	6.00	6.32	6.05	0.22	0.22	0.00	0.00
18.02.2009-13:20:00	5.89	5.97	5.96	6.29	0.36	0.40	0.33	0.33	32.84	32.88	32.92	32.96	0.99533	0.99545	0.99557	0.99569	74.90	74.96	75.01	75.06	5.88	6.06	6.04	6.34	0.22	0.25	0.17	0.17	0.9900	0.9900	0.9900	0.9900	74.56	74.99	75.43	75.43	6.05	6.00	6.31	6.10	0.20	0.20	0.00	0.00
18.02.2009-13:30:00	5.94	6.00	6.01	6.34	0.39	0.43	0.33	0.33	32.84	32.88	32.92	32.96	0.99545	0.99557	0.99569	0.99581	74.96	74.96	74.96	74.96	5.97	6.06	6.03	6.33	0.19	0.27	0.15	0.15	0.9900	0.9900	0.9900	0.9900	74.48	74.92	75.37	75.37	6.02	6.02	6.27	6.10	0.17	0.17	0.00	0.00
18.02.2009-13:40:00	6.00	6.08	6.08	6.43	0.49	0.54	0.33	0.33	32.67	32.71	32.75	32.79	0.99557	0.99569	0.99581	0.99593	74.36	74.36	74.36	74.36	6.03	6.11	6.11	6.41	0.15	0.13	0.13	0.13	0.9900	0.9900	0.9900	0.9900	74.19	74.35	74.80	74.80	6.07	6.08	6.23	6.14	0.13	0.13	0.00	0.00
18.02.2009-13:50:00	6.14	6.21	6.21	6.49	0.56	0.61	0.33	0.33	32.90	32.94	32.98	33.02	0.99569	0.99581	0.99593	0.99605	74.30	74.20	74.20	74.20	6.14	6.22	6.22	6.52	0.03	0.05	0.05	0.05	0.9900	0.9900	0.9900	0.9900	74.22	74.67	75.12	75.12	6.17	6.16	6.29	6.00	0.00	0.00	0.00	0.00
18.02.2009-14:00:00	6.23	6.29	6.29	6.56	0.63	0.67	0.33	0.33	32.82	32.86	32.90	32.94	0.99581	0.99593	0.99605	0.99617	74.13	74.23	74.23	74.23	6.25	6.33	6.34	6.63	0.03	0.04	0.04	0.04	0.9900	0.9900	0.9900	0.9900	74.03	74.19	74.77	74.77	6.25	6.27	6.30	6.00	0.00	0.00	0.00	0.00
18.02.2009-14:10:00	6.34	6.41	6.41	6.63	0.71	0.76	0.33	0.33	32.89	32.93	32.97	33.01	0.99593	0.99605	0.99617	0.99629	73.97	74.07	74.07	74.07	6.27	6.35	6.36	6.65	0.06	0.07	0.12	0.12	0.9900	0.9900	0.9900	0.9900	73.87	74.06	74.51	74.51	6.36	6.34	6.41	6.08	0.00	0.00	0.00	0.00
18.02.2009-14:20:00	6.47	6.54	6.54	6.72	0.80	0.85	0.33	0.33	32.67	32.71	32.75	32.79	0.99605	0.99617	0.99629	0.99641	73.94	74.04	74.04	74.04	6.27	6.34	6.34	6.63	0.12	0.08	0.17	0.17	0.9900	0.9900	0.9900	0.9900	73.97	74.45	74.86	74.86	6.47	6.42	6.54	6.00	0.00	0.00	0.00	0.00
18.02.2009-14:30:00	6.62	6.71	6.69	6.78	0.88	0.93	0.33	0.33	32.61	32.65	32.69	32.73	0.99617	0.99629	0.99641	0.99653	73.91	74.01	74.01	74.01	6.29	6.37	6.37	6.66	0.07	0.08	0.13	0.13	0.9900	0.9900	0.9900	0.9900	73.65	73.91	74.38	74.38	6.72	6.72	6.77	6.00	0.00	0.00	0.00	0.00
18.02.2009-14:40:00	6.57	6.64	6.65	6.75	0.83	0.89	0.33	0.33	32.63	32.67	32.71	32.75	0.99629	0.99641	0.99653	0.99665	73.85	73.95	73.95	73.95	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.68	73.84	74.32	74.32	6.75	6.76	6.77	6.00	0.00	0.00	0.00	0.00
18.02.2009-14:50:00	6.61	6.68	6.70	6.77	0.83	0.89	0.33	0.33	32.62	32.66	32.70	32.74	0.99641	0.99653	0.99665	0.99677	73.81	73.91	73.91	73.91	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.65	73.81	74.29	74.29	6.73	6.74	6.75	6.00	0.00	0.00	0.00	0.00
18.02.2009-15:00:00	6.43	6.52	6.52	6.68	0.75	0.81	0.33	0.33	32.71	32.75	32.79	32.83	0.99653	0.99665	0.99677	0.99689	73.75	73.85	73.85	73.85	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.61	73.77	74.25	74.25	6.73	6.74	6.75	6.00	0.00	0.00	0.00	0.00
18.02.2009-15:10:00	6.44	6.53	6.53	6.71	0.78	0.85	0.33	0.33	32.71	32.75	32.79	32.83	0.99665	0.99677	0.99689	0.99701	73.79	73.89	73.89	73.89	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.65	73.81	74.29	74.29	6.73	6.74	6.75	6.00	0.00	0.00	0.00	0.00
18.02.2009-15:20:00	6.37	6.45	6.44	6.67	0.75	0.80	0.33	0.33	32.74	32.78	32.82	32.86	0.99677	0.99689	0.99701	0.99713	73.94	74.04	74.04	74.04	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.81	73.97	74.45	74.45	6.63	6.62	6.65	6.00	0.00	0.00	0.00	0.00
18.02.2009-15:30:00	6.34	6.42	6.41	6.67	0.74	0.81	0.33	0.33	32.76	32.80	32.84	32.88	0.99689	0.99701	0.99713	0.99725	73.90	74.00	74.00	74.00	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.84	74.03	74.48	74.48	6.62	6.63	6.66	6.00	0.00	0.00	0.00	0.00
18.02.2009-15:40:00	6.37	6.45	6.44	6.67	0.75	0.82	0.33	0.33	32.76	32.80	32.84	32.88	0.99701	0.99713	0.99725	0.99737	73.94	74.04	74.04	74.04	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.84	74.03	74.48	74.48	6.62	6.63	6.66	6.00	0.00	0.00	0.00	0.00
18.02.2009-15:50:00	6.37	6.45	6.44	6.67	0.75	0.82	0.33	0.33	32.76	32.80	32.84	32.88	0.99713	0.99725	0.99737	0.99749	73.94	74.04	74.04	74.04	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.84	74.03	74.48	74.48	6.62	6.63	6.66	6.00	0.00	0.00	0.00	0.00
18.02.2009-16:00:00	6.29	6.38	6.37	6.68	0.75	0.82	0.33	0.33	33.04	33.08	33.12	33.16	0.99725	0.99737	0.99749	0.99761	73.97	74.07	74.07	74.07	6.23	6.31	6.31	6.60	0.06	0.06	0.11	0.11	0.9900	0.9900	0.9900	0.9900	73.94	74.11	74.57	74.57	6.51	6.55	6.57	6.00	0.00	0.00	0.00	0.00
18.02.2009-16:10:00	6.2																																											

Appendix 5(a) - Format for Frequency-Impedance simulation data – PSCAD
files for different loads

F(Hz)	Z+ (ohms) Load1	Z+ (ohms) Load2	Z+ (ohms) Load3	Z+ (ohms) Load4	Z+ (ohms) Load5	Z+ (ohms) Load6	Z+ (ohms) Load7	Z+ (ohms) Load8	Z+ (ohms) Load9
50	2.0765887	2.0974091	2.0938632	2.0924098	2.0904169	2.0380374	2.0166673	2.0358159	2.0277524
60	2.5324908	2.5580873	2.5532651	2.5511371	2.5482138	2.4822916	2.4554765	2.4774542	2.4670118
70	3.0162483	3.0470377	3.0405371	3.037462	3.0332319	2.9516432	2.9185768	2.9426786	2.9293315
80	3.5364887	3.5730226	3.5642736	3.5598639	3.5537925	3.4536223	3.4132049	3.4384057	3.4214321
90	4.1049853	4.1480014	4.1361797	4.129873	4.1211864	3.9983713	3.9490821	3.9738656	3.9522605
100	4.7384353	4.7889353	4.7728116	4.7637661	4.7513098	4.6000412	4.5397269	4.5617876	4.5341188
110	5.4616401	5.521017	5.4986775	5.485579	5.4675564	5.2792297	5.204738	5.2204323	5.1845905
120	6.313534	6.3837841	6.3520728	6.3327479	6.3062025	6.0674403	5.9739428	5.9772097	5.9299477
130	7.3595296	7.4436187	7.3969111	7.367479	7.3271645	7.0157807	6.8954229	6.8754771	6.8114973
140	8.7194927	8.8219906	8.7491315	8.7018956	8.6374988	8.2133364	8.0522694	7.9881293	7.8981076
150	10.640346	10.768344	10.643549	10.560785	10.448883	9.8299192	9.60091	9.4465598	9.3123762
160	13.724152	13.88629	13.634243	13.464871	13.239596	12.226899	11.867781	11.504469	11.28621
170	19.881867	20.046926	19.34241	18.875272	18.278965	16.260304	15.599972	14.650473	14.248471
180	37.950898	36.802963	33.072782	30.915345	28.517119	23.455769	22.043799	19.21962	18.428301
190	37.148986	33.897713	31.054411	29.267838	27.233357	24.25234	23.319389	19.584215	18.86131
200	12.372599	12.10434	11.969455	11.860278	11.714053	11.70184	11.706542	11.016954	10.91408
210	4.8556057	4.8222601	4.8148178	4.8076943	4.7979541	4.8344668	4.8511058	4.7788975	4.7755018
220	1.2363445	1.2344949	1.2344579	1.2343385	1.23418	1.2377061	1.2392401	1.2364586	1.2368056
230	1.0498503	1.0510206	1.0508435	1.0507704	1.0506633	1.0476728	1.0464051	1.0474522	1.0469511
240	2.7516257	2.758756	2.7565104	2.7551997	2.7533405	2.7308829	2.7215471	2.7238488	2.7192654
250	4.1658817	4.1804173	4.1733415	4.168822	4.1624651	4.1078978	4.0855856	4.0819208	4.0695309
260	5.4367975	5.4587562	5.4438002	5.4338296	5.4198797	5.3228248	5.2837336	5.2646531	5.241087
270	6.6464259	6.6753073	6.648887	6.6308245	6.6056691	6.4554109	6.3957629	6.3504241	6.3122625
280	7.8487364	7.8837037	7.8412999	7.8118352	7.7709887	7.5549175	7.4703841	7.3860644	7.3295131
290	9.0846843	9.1244881	9.0601282	9.0149227	8.9525669	8.6549274	8.5402268	8.4015203	8.3221643
300	10.389967	10.43271	10.338255	10.271457	10.179836	9.7804254	9.6289569	9.4168265	9.3094313
310	11.799755	11.842477	11.706593	11.610148	11.478737	10.951355	10.75481	10.445516	10.303863
320	13.352232	13.390217	13.19683	13.059484	12.87382	12.18433	11.932264	11.496195	11.312963
330	15.091855	15.117475	14.843414	14.64924	14.389271	13.493189	13.172557	12.573051	12.339779
340	17.072884	17.073643	16.684977	16.4112	16.048968	14.888592	14.483316	13.675672	13.382861
350	19.363542	19.318682	18.765009	18.378816	17.875299	16.37663	15.867334	14.798405	14.435844
360	22.050869	21.925415	21.130988	20.585	19.886073	17.956283	17.320441	15.929453	15.486874
370	25.245594	24.97982	23.830134	23.056415	22.088367	19.615551	18.828497	17.05001	16.518157
380	29.084116	28.57512	26.89811	25.801556	24.467869	21.326394	20.363775	18.133858	17.505988
390	33.718426	32.78955	30.33428	28.788945	26.972701	23.039317	21.881592	19.147976	18.421663
400	39.269064	37.626983	34.05546	31.913326	29.493543	24.67978	23.318908	20.054701	19.233615
410	45.684875	42.889757	37.82742	34.956922	31.849006	26.150176	24.597344	20.815705	19.910797
420	52.433237	47.984648	41.207167	37.575353	33.795311	27.3414	25.63279	21.39739	20.426915
430	58.116626	51.83139	43.591001	39.360318	35.080352	28.154837	26.351306	21.776488	20.764525
440	60.734608	53.269008	44.459271	39.999918	35.534858	28.528612	26.706835	21.944206	20.917864
450	59.26789	51.93395	43.694875	39.444014	35.146967	28.455773	26.693117	21.907481	20.893508
460	54.765617	48.568935	41.649979	37.923158	34.061192	27.984302	26.343902	21.686997	20.708729
470	49.120345	44.320928	38.889969	35.806758	32.505447	27.199644	25.721813	21.312829	20.388199
480	43.625783	40.035601	35.907196	33.439908	30.705922	26.200215	24.901883	20.819258	19.960102
490	38.797738	36.122359	33.009969	31.063924	28.837307	25.077173	23.956855	20.240211	19.452659
500	34.721277	32.705208	30.346518	28.814936	27.01248	23.903527	22.948088	19.606103	18.891628
510	31.319711	29.774232	27.965365	26.753892	25.293569	22.731609	21.922217	18.942231	18.298912
520	28.478543	27.271568	25.863967	24.897188	23.708165	21.595455	20.911746	18.268378	17.6921
530	26.089803	25.130025	24.017909	23.238206	22.263064	20.514995	19.937501	17.599207	17.084634
540	24.06372	23.287678	22.395973	21.760142	20.953748	19.500216	19.011535	16.945033	16.48632
550	22.32902	21.692017	20.967102	20.442902	19.77027	18.554563	18.139739	16.312695	15.903973
560	20.830007	20.300046	19.703195	19.266503	18.700623	17.677432	17.323909	15.706387	15.342051
570	19.523275	19.07705	18.579924	18.212567	17.732559	16.865876	16.563261	15.128356	14.803232
580	18.374857	17.995108	17.576682	17.264848	16.854492	16.115724	15.855478	14.579467	14.288878
590	17.35799	17.031727	16.676184	16.409271	16.055889	15.422288	15.19742	14.059634	13.799417
600	16.451409	16.168709	15.863993	15.633778	15.327384	14.780784	14.585571	13.568141	13.334626
610	15.638068	15.391242	15.128046	14.928091	14.660757	14.18658	14.016335	13.103874	12.893844
620	14.904178	14.687194	14.45825	14.28346	14.04884	13.635323	13.486196	12.665485	12.476137
630	14.238489	14.046559	13.846124	13.692431	13.485409	13.123006	12.991823	12.251512	12.080404
640	13.631746	13.461025	13.28452	13.148645	12.965061	12.645977	12.530113	11.86045	11.70546
650	13.076275	12.923648	12.767379	12.64666	12.483107	12.200942	12.098216	11.49081	11.350092

Appendix 5(b) - Format for Frequency-Impedance(ohm) simulation data - PSCAD files for different transformer/capacitor bank configurations

F (Hz)	For average day load						For maximum day load						For minimum night load											
	1TF1/Cap bank	2TF1/Cap bank	1TF2/Cap bank	2TF2/Cap bank	1TF1/Cap bank	2TF1/Cap bank	1TF1/Cap bank	2TF1/Cap bank	1TF2/Cap bank	2TF2/Cap bank	1TF1/Cap bank	2TF1/Cap bank	1TF1/Cap bank	2TF1/Cap bank	1TF2/Cap bank	2TF2/Cap bank	1TF1/Cap bank	2TF1/Cap bank	1TF2/Cap bank	2TF2/Cap bank	1TF1/Cap bank	2TF1/Cap bank	1TF2/Cap bank	2TF2/Cap bank
50	3.530260	2.016445	2.035243	2.053395	2.074929	3.581832	2.016361	2.035122	2.05281	2.074724	3.779017	3.845555	2.092375	2.112594	2.134383	2.155489								
75	5.313060	3.042179	3.107011	3.183534	3.254573	5.255470	3.024827	3.088072	3.163514	3.233115	5.58072	5.994559	3.167892	3.238324	3.311984	3.393629								
100	7.120951	4.05991	4.265768	4.484188	4.715357	7.003480	4.049372	4.203509	4.414909	4.597521	7.911532	8.525269	4.298236	4.473812	4.657358	4.847397								
125	8.968542	5.065659	5.360674	5.677524	6.011357	8.895943	5.140100	5.454989	5.947822	6.421034	10.490114	11.300216	5.53742	5.928717	6.327846	6.732927								
150	10.875430	6.145338	6.592222	7.059336	7.546822	11.300262	6.343683	6.82753	7.32768	7.82453	13.24683	14.200778	6.011357	6.507938	6.912783	7.32927								
175	12.867165	7.250407	7.816330	8.399336	8.997224	13.246822	7.594992	8.17752	8.76788	9.36788	15.190798	16.245742	7.141791	7.737102	8.142783	8.558334								
200	14.962128	8.411817	9.097119	9.797119	10.507119	15.190798	9.48392	10.18392	10.89392	11.60392	17.180798	18.335742	8.292119	8.997102	9.30788	9.618334								
225	17.243000	9.644931	1.036888	1.065888	1.094888	17.180798	11.47832	12.17832	12.87832	13.57832	19.170798	20.325742	9.402119	10.107102	10.41788	10.728334								
250	19.716856	1.0931489	1.122477	1.151477	1.180477	19.170798	13.66282	14.36282	15.06282	15.76282	21.16282	22.317742	10.516119	11.221102	11.53188	11.842334								
275	22.456639	1.21131	1.24031	1.26931	1.29831	21.16282	15.84682	16.54682	17.24682	17.94682	23.16282	24.317742	11.625119	12.330102	12.64088	12.951334								
300	25.499633	1.330659	1.359659	1.388659	1.417659	23.16282	18.03082	18.73082	19.43082	20.13082	25.16282	26.317742	12.734119	13.439102	13.74988	14.060334								
325	28.743177	1.451008	1.480008	1.509008	1.538008	25.16282	20.21482	20.91482	21.61482	22.31482	27.16282	28.317742	13.837119	14.542102	14.85288	15.163334								
350	32.186722	1.571357	1.600357	1.629357	1.658357	27.16282	22.40082	23.10082	23.80082	24.50082	29.16282	30.317742	14.934119	15.639102	15.94988	16.260334								
375	35.830260	1.691706	1.720706	1.749706	1.778706	29.16282	24.58682	25.28682	25.98682	26.68682	31.16282	32.317742	16.031119	16.736102	17.04688	17.357334								
400	39.573712	1.812055	1.841055	1.870055	1.899055	31.16282	26.77282	27.47282	28.17282	28.87282	33.16282	34.317742	17.125119	17.830102	18.14088	18.451334								
425	43.417167	1.932404	1.961404	1.990404	2.019404	33.16282	28.96082	29.66082	30.36082	31.06082	35.16282	36.317742	18.214119	18.919102	19.22988	19.540334								
450	47.260612	2.052753	2.081753	2.110753	2.139753	35.16282	31.14682	31.84682	32.54682	33.24682	37.16282	38.317742	19.307119	20.012102	20.32288	20.633334								
475	51.104057	2.173102	2.202102	2.231102	2.260102	37.16282	33.33282	34.03282	34.73282	35.43282	39.16282	40.317742	20.397119	21.102102	21.41288	21.723334								
500	55.047502	2.293451	2.322451	2.351451	2.380451	39.16282	35.51882	36.21882	36.91882	37.61882	41.16282	42.317742	21.481119	22.186102	22.49688	22.807334								
525	59.090947	2.413800	2.442800	2.471800	2.500800	41.16282	37.70482	38.40482	39.10482	39.80482	43.16282	44.317742	22.565119	23.270102	23.58088	23.891334								
550	63.134392	2.534149	2.563149	2.592149	2.621149	43.16282	39.89082	40.59082	41.29082	41.99082	45.16282	46.317742	23.649119	24.354102	24.66488	24.975334								
575	67.177837	2.654498	2.683498	2.712498	2.741498	45.16282	42.07682	42.77682	43.47682	44.17682	47.16282	48.317742	24.737119	25.442102	25.75288	26.063334								
600	71.221282	2.774847	2.803847	2.832847	2.861847	47.16282	44.26282	44.96282	45.66282	46.36282	49.16282	50.317742	25.825119	26.530102	26.84088	27.151334								
625	75.264727	2.895196	2.924196	2.953196	2.982196	49.16282	46.44882	47.14882	47.84882	48.54882	51.16282	52.317742	26.907119	27.612102	27.92288	28.233334								
650	79.308172	3.015545	3.044545	3.073545	3.102545	51.16282	48.63482	49.33482	50.03482	50.73482	53.16282	54.317742	27.991119	28.696102	29.00688	29.317334								
675	83.351617	3.135894	3.164894	3.193894	3.222894	53.16282	50.82082	51.52082	52.22082	52.92082	55.16282	56.317742	29.075119	29.780102	30.09088	30.401334								
700	87.395062	3.256243	3.285243	3.314243	3.343243	55.16282	53.00682	53.70682	54.40682	55.10682	57.16282	58.317742	30.165119	30.870102	31.18088	31.491334								
725	91.438507	3.376592	3.405592	3.434592	3.463592	57.16282	55.19282	55.89282	56.59282	57.29282	59.16282	60.317742	31.255119	31.960102	32.27088	32.581334								
750	95.481952	3.496941	3.525941	3.554941	3.583941	59.16282	57.37882	58.07882	58.77882	59.47882	61.16282	62.317742	32.345119	33.050102	33.36088	33.671334								
775	99.525397	3.617290	3.646290	3.675290	3.704290	61.16282	59.56482	60.26482	60.96482	61.66482	63.16282	64.317742	33.435119	34.140102	34.45088	34.761334								
800	103.568842	3.737639	3.766639	3.795639	3.824639	63.16282	61.75082	62.45082	63.15082	63.85082	65.16282	66.317742	34.525119	35.230102	35.54088	35.851334								
825	107.612287	3.857988	3.886988	3.915988	3.944988	65.16282	63.93682	64.63682	65.33682	66.03682	67.16282	68.317742	35.615119	36.320102	36.63088	36.941334								
850	111.655732	3.978337	4.007337	4.036337	4.065337	67.16282	66.12282	66.82282	67.52282	68.22282	69.16282	70.317742	36.705119	37.400102	37.71088	38.021334								
875	115.699177	4.098686	4.127686	4.156686	4.185686	69.16282	68.30882	69.00882	69.70882	70.40882	71.16282	72.317742	37.795119	38.490102	38.80088	39.111334								
900	119.742622	4.219035	4.248035	4.277035	4.306035	71.16282	70.49482	71.19482	71.89482	72.59482	73.16282	74.317742	38.885119	39.580102	39.89088	40.201334								
925	123.786067	4.339384	4.368384	4.397384	4.426384	73.16282	72.68082	73.38082	74.08082	74.78082	75.16282	76.317742	39.975119	40.675102	40.98588	41.296334								
950	127.829512	4.459733	4.488733	4.517733	4.546733	75.16282	74.86682	75.56682	76.26682	76.96682	77.16282	78.317742	41.075119	41.770102	42.08088	42.391334								
975	131.872957	4.580082	4.609082	4.638082	4.667082	77.16282	77.05282	77.75282	78.45282	79.15282	79.16282	80.317742	42.175119	42.870102	43.18088	43.491334								
1000	135.916402	4.700431	4.729431	4.758431	4.787431	79.16282	79.23882	79.93882	80.63882	81.33882	81.16282	82.317742	43.275119	43.970102	44.28088	44.591334								
1025	139.959847	4.820780	4.849780	4.878780	4.907780	81.16282	81.42482	82.12482	82.82482	83.52482	83.16282	84.317742	44.375119	45.070102	45.38088	45.691334								
1050	143.993292	4.941129	4.970129	4.999129	5.028129	83.16282	83.61082	84.31082	85.01082	85.71082	85.16282	86.317742	45.475119	46.170102	46.48088	46.791334								
1075	148.036737	5.061478	5.090478	5.119478	5.148478	85.16282	85.80682	86.50682	87.20682	87.90682	87.16282	88.317742	46.575119	47.270102	47.58088	47.891334								
1100	152.080182	5.181827	5.210827	5.239827	5.268827	87.16282	87.99282	88.69282	89.39282	90.09282	89.16282	90.317742	47.675119	48.370102	48.68088	48.991334								
1125	156.123627	5.302176	5.331176	5.360176	5.389176	89.16282	90.17882	90.87882	91.57882	92.27882	91.16282	92.317742	48.775119	49.470102	49.78088	50.091334								
1150	160.167072	5.422525	5.451525	5.480525	5.509525	91.16282	92.36482	93.06482	93.76482	94.46482	93.16282	94.317742	49.875119	50.570102	50.88088	51.191334								
1175	164.210517	5.542874	5.571874	5.600874	5.629874	93.16282	94.55082	95.25082	95.95082	96.65082	95.16282	96.317742	50.975119	51.670102	51.98088	52.291334								
1200	168.253962	5.663223	5.692223	5.7212																				

APPENDIX 6 - Data format for harmonic measurement – Panaduara grid sub station

Date	Time	THD %			3rd %			5th %			7th %			9th %			11th %			13th %		
		L1	L2	L3	L1	L2	L3	L1	L2	L3												
01 02 2009	17 00 00	1.20	1.40	1.30	0.50	0.60	0.50	0.70	0.60	0.80	0.20	0.20	0.30	0.10	0.20	0.20	0.30	0.30	0.20	0.20	0.10	0.20
01 02 2009	17 10 00	1.00	1.30	1.40	0.40	0.40	0.40	0.50	0.60	0.80	0.40	0.40	0.10	0.20	0.20	0.40	0.40	0.40	0.40	0.30	0.60	0.30
01 02 2009	17 20 00	3.50	2.00	2.00	0.70	0.80	0.50	0.90	0.80	1.10	0.40	0.50	0.30	0.50	0.30	0.40	0.60	0.50	0.40	0.50	0.70	0.70
01 02 2009	17 30 00	1.30	1.20	1.50	0.30	0.40	0.30	0.90	0.80	1.10	0.50	0.60	0.40	0.30	0.20	0.20	0.40	0.40	0.40	0.30	0.30	0.30
01 02 2009	17 40 00	1.30	1.10	1.40	0.30	0.40	0.30	0.90	0.80	1.10	0.30	0.30	0.30	0.20	0.10	0.10	0.40	0.40	0.40	0.30	0.30	0.30
01 02 2009	17 50 00	1.20	1.10	1.40	0.30	0.40	0.30	0.90	0.80	1.20	0.20	0.30	0.20	0.20	0.10	0.10	0.40	0.30	0.50	0.30	0.20	0.30
01 02 2009	18 00 00	1.30	1.20	1.40	0.30	0.40	0.40	0.40	0.40	1.20	0.30	0.30	0.30	0.20	0.10	0.10	0.40	0.40	0.50	0.40	0.20	0.30
01 02 2009	18 10 00	1.30	1.30	1.50	0.40	0.50	0.40	0.90	0.90	1.20	0.30	0.40	0.30	0.20	0.10	0.10	0.40	0.40	0.40	0.30	0.20	0.30
01 02 2009	18 20 00	1.30	1.30	1.40	0.40	0.50	0.40	0.90	0.90	1.10	0.30	0.40	0.30	0.20	0.10	0.10	0.40	0.40	0.40	0.20	0.20	0.30
01 02 2009	18 30 00	1.30	1.30	1.40	0.40	0.50	0.30	1.10	1.00	1.20	0.30	0.30	0.30	0.10	0.10	0.20	0.40	0.40	0.40	0.30	0.20	0.20
01 02 2009	18 40 00	1.40	1.40	1.50	0.50	0.50	0.50	0.90	0.80	0.90	0.30	0.30	0.30	0.10	0.10	0.10	0.40	0.50	0.40	0.20	0.20	0.10
01 02 2009	18 50 00	1.10	1.10	1.20	0.50	0.40	0.40	0.80	0.80	0.90	0.20	0.30	0.30	0.20	0.10	0.10	0.30	0.40	0.40	0.20	0.10	0.20
01 02 2009	19 00 00	1.70	1.60	1.70	0.50	0.50	0.50	0.70	0.60	0.70	0.20	0.30	0.30	0.20	0.20	0.20	0.30	0.30	0.30	0.20	0.10	0.20
01 02 2009	19 10 00	1.20	1.10	1.30	0.40	0.50	0.40	0.70	0.60	0.70	0.20	0.30	0.30	0.20	0.10	0.10	0.30	0.30	0.20	0.20	0.20	0.20
01 02 2009	19 20 00	0.80	0.80	0.90	0.30	0.30	0.20	0.70	0.60	0.70	0.20	0.30	0.30	0.10	0.10	0.10	0.20	0.30	0.20	0.20	0.20	0.20
01 02 2009	19 30 00	1.20	1.20	1.20	0.40	0.50	0.40	0.70	0.60	0.80	0.20	0.30	0.30	0.10	0.10	0.10	0.20	0.20	0.20	0.20	0.20	0.20
01 02 2009	19 40 00	1.60	0.90	1.00	0.80	0.40	0.30	0.80	0.70	0.80	0.30	0.30	0.30	0.10	0.10	0.10	0.30	0.30	0.20	0.20	0.20	0.20
01 02 2009	19 50 00	1.20	1.10	1.20	0.50	0.40	0.50	0.80	0.70	0.90	0.20	0.30	0.30	0.10	0.10	0.10	0.20	0.20	0.20	0.20	0.20	0.20
01 02 2009	20 00 00	1.10	1.10	1.20	0.50	0.40	0.50	0.80	0.70	0.90	0.20	0.30	0.30	0.10	0.10	0.10	0.20	0.20	0.20	0.30	0.20	0.30
01 02 2009	20 10 00	1.10	0.90	1.10	0.40	0.40	0.30	0.90	0.80	0.90	0.20	0.20	0.20	0.10	0.10	0.10	0.20	0.20	0.10	0.30	0.30	0.30
01 02 2009	20 20 00	1.10	1.30	1.40	0.50	0.40	0.40	1.00	0.90	1.10	0.20	0.20	0.20	0.10	0.10	0.10	0.20	0.20	0.20	0.30	0.30	0.30
01 02 2009	20 30 00	1.30	1.20	1.30	0.60	0.40	0.40	0.90	0.90	1.00	0.20	0.30	0.30	0.10	0.10	0.10	0.20	0.30	0.20	0.30	0.30	0.30
01 02 2009	20 40 00	1.30	1.20	1.30	0.60	0.40	0.40	0.90	0.90	1.00	0.20	0.20	0.20	0.10	0.10	0.10	0.30	0.30	0.20	0.30	0.30	0.30
01 02 2009	20 50 00	1.40	1.30	1.40	0.60	0.40	0.40	1.00	0.90	1.00	0.20	0.20	0.20	0.10	0.10	0.10	0.30	0.20	0.20	0.30	0.30	0.30
01 02 2009	21 00 00	1.30	1.40	1.40	0.60	0.60	0.60	0.90	0.90	1.10	0.20	0.20	0.20	0.10	0.10	0.10	0.20	0.20	0.20	0.30	0.30	0.30
01 02 2009	21 10 00	1.30	1.10	1.30	0.50	0.40	0.50	0.90	0.80	1.00	0.20	0.20	0.30	0.10	0.10	0.10	0.30	0.20	0.20	0.30	0.30	0.30
01 02 2009	21 20 00	1.40	1.40	1.40	0.60	0.60	0.40	1.00	0.90	1.10	0.20	0.30	0.20	0.10	0.10	0.10	0.20	0.30	0.20	0.30	0.30	0.30
01 02 2009	21 30 00	1.80	1.80	1.80	0.70	0.70	0.60	1.10	1.00	1.20	0.30	0.30	0.30	0.20	0.20	0.20	0.30	0.20	0.30	0.30	0.30	0.30
01 02 2009	21 40 00	1.40	1.40	1.50	0.60	0.40	0.50	1.10	1.10	1.20	0.20	0.30	0.30	0.10	0.10	0.10	0.30	0.30	0.20	0.30	0.30	0.30
01 02 2009	21 50 00	1.60	1.40	1.70	0.70	0.60	0.60	1.10	1.10	1.10	0.20	0.20	0.30	0.30	0.20	0.20	0.30	0.30	0.20	0.30	0.30	0.30
01 02 2009	22 00 00	1.60	1.40	1.60	0.60	0.50	0.50	1.40	1.20	1.40	0.20	0.30	0.30	0.20	0.10	0.20	0.20	0.30	0.30	0.20	0.30	0.30
01 02 2009	22 10 00	1.50	1.30	1.60	0.50	0.50	0.50	1.30	1.20	1.50	0.20	0.30	0.30	0.10	0.10	0.10	0.20	0.30	0.30	0.20	0.30	0.30
01 02 2009	22 20 00	1.50	1.50	1.50	0.50	0.50	0.50	1.40	1.40	1.40	0.30	0.30	0.30	0.10	0.10	0.10	0.30	0.40	0.30	0.10	0.20	0.20
01 02 2009	22 30 00	6.50	4.50	5.20	0.50	0.50	0.50	1.40	1.40	1.40	0.30	0.30	0.30	0.10	0.10	0.10	0.20	0.40	0.40	0.50	0.20	0.20
01 02 2009	22 40 00	1.50	1.50	1.50	0.50	0.50	0.50	1.40	1.40	1.40	0.30	0.30	0.30	0.10	0.10	0.10	0.20	0.40	0.40	0.50	0.20	0.20
01 02 2009	22 50 00	1.50	1.40	1.70	0.40	0.50	0.50	1.30	1.10	1.50	0.30	0.30	0.30	0.10	0.10	0.10	0.30	0.50	0.40	0.20	0.20	0.10
01 02 2009	23 00 00	1.60	1.50	1.90	0.50	0.60	0.70	1.40	1.30	1.60	0.40	0.30	0.30	0.20	0.10	0.20	0.50	0.40	0.60	0.30	0.20	0.30
01 02 2009	23 10 00	1.50	1.40	1.70	0.40	0.50	0.50	1.30	1.20	1.50	0.30	0.40	0.30	0.20	0.10	0.20	0.50	0.50	0.60	0.30	0.10	0.30
01 02 2009	23 20 00	1.40	1.40	1.80	0.40	0.40	0.60	1.10	1.10	1.50	0.30	0.40	0.40	0.20	0.20	0.20	0.50	0.60	0.60	0.30	0.10	0.20
01 02 2009	23 30 00	1.40	1.30	1.70	0.30	0.40	0.50	1.10	0.80	1.30	0.40	0.40	0.50	0.30	0.20	0.20	0.60	0.70	0.70	0.20	0.30	0.20
01 02 2009	23 40 00	1.60	1.40	1.60	0.40	0.60	0.50	1.20	0.80	1.50	0.30	0.40	0.40	0.30	0.10	0.20	0.60	0.60	0.60	0.20	0.30	0.20
01 02 2009	23 50 00	1.50	1.10	1.70	0.20	0.50	0.50	1.10	0.70	1.40	0.30	0.30	0.30	0.40	0.30	0.10	0.70	0.70	0.70	0.30	0.20	0.20
02 02 2009	00 00 00	1.40	1.20	1.70	0.40	0.40	0.50	1.10	0.90	1.30	0.30	0.30	0.30	0.40	0.40	0.30	0.10	0.10	0.70	0.60	0.70	0.30
02 02 2009	00 10 00	1.50	1.40	1.80	0.40	0.50	0.60	1.10	0.90	1.40	0.20	0.60	0.30	0.20	0.20	0.20	0.70	0.70	0.70	0.30	0.20	0.20
02 02 2009	00 20 00	1.50	1.40	1.80	0.40	0.40	0.40	1.10	0.90	1.40	0.30	0.30	0.30	0.40	0.10	0.20	0.30	0.60	0.50	0.20	0.10	0.20
02 02 2009	00 30 00	1.50	1.40	1.80	0.40	0.40	0.40	1.10	0.90	1.40	0.30	0.30	0.30	0.40	0.10	0.20	0.30	0.60	0.50	0.20	0.20	0.20
02 02 2009	00 40 00	1.80	1.40	2.00	0.40	0.50	0.50	1.10	0.90	1.50	0.30	0.30	0.30	0.40	0.10	0.20	0.30	0.60	0.50	0.10	0.20	0.20
02 02 2009	00 50 00	2.20	1.80	2.10	0.40	0.50	0.60	1.10	0.90	1.60	0.40	0.60	0.60	0.20	0.20	0.20	0.60	0.70	0.60	0.30	0.20	0.40
02 02 2009	01 00 00	1.70	1.70	2.10	0.50	0.50	0.50	1.10	1.10	1.70	0.30	0.60	0.30	0.20	0.20	0.20	0.40	0.60	0.40	0.20	0.20	0.30
02 02 2009	01 10 00	1.80	1.70	2.00	0.40	0.50	0.50	1.10	0.90	1.70	0.40	0.50	0.60	0.30	0.20	0.10	0.40	0.50	0.50	0.20	0.30	0.30
02 02 2009	01 20 00	2.20	2.20	2.20	0.40	0.40	0.40	1.10	0.90	1.60	0.40	0.50	0.60	0.30	0.20	0.20	0.50	0.50	0.60	0.20	0.20	0.20
02 02 2009	01 30 00	2.10	1.70	2.30	0.20	0.60	0.60	1.10	0.80	1.70	0.40	0.40	0.50	0.30	0.20	0.20	0.60	0.50	0.70	0.30	0.30	0.20
02 02 2009	01 40 00	1.90	1.40	2.10	0.50	0.50	0.50	1.10	1.00	1.70	0.40	0.40	0.60	0.30	0.10	0.10	0.50	0.50	0.60	0.20	0.30	0.20
02 02 2009	01 50 00	2.00	1.40	2.10	0.40	0.30	0.60	1.10	1.00	1.80	0.40	0.50	0.60	0.30	0.10	0.20	0.60	0.50	0.70	0.30	0.40	0.30
02 02 2009	02 00 00																					

APPENDIX 7(a) - Format for results - Reactive power control switching points

for 21st January 2009

Multiple Run Output File 21_0000 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
2	9850000000	77.25789739	33.60726631	3.339793622	20.92905232	1.221310032	6591664180E-01
3	1.000000000	77.25750360	33.10557628	3.337917828	20.30879510	1.184935209	6397523240E-01
Multiple Run Output File 21_0030 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.39278847	33.27852734	2.670006829	19.63244992	9154634266	6338808721E-01
2	9850000000	75.39223421	32.77409038	2.869217346	19.04162845	8876354555	6149058731E-01
3	1.000000000	75.39176317	32.28464290	2.666665422	18.47691175	8608543472	5567500481E-01
Multiple Run Output File 21_0100 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
2	9700000000	75.46793668	33.08939903	2.784357870	15.97324084	7608387043	6141779392E-01
3	9850000000	75.46736237	32.66344795	2.220185510	18.49691342	7371398615	5957242630E-01
4	1.000000000	75.46687528	32.17261095	2.494954335	17.06149107	714824927	5782700675E-01
Multiple Run Output File 21_0130 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
2	9700000000	75.67106877	33.1774635	2.89537769	18.60165145	7117664066	5992480987E-01
3	9850000000	75.67042957	32.87167540	2.191311045	18.04624186	6914587221	5813194118E-01
4	1.000000000	75.66987506	32.37062138	2.18196177	17.11062765	6699229319	5640926343E-01
Multiple Run Output File 21_0200 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
2	9700000000	75.13278093	33.02240301	1.481547255	18.24471439	4750881129	5883156739E-01
3	9850000000	75.13175018	32.61697058	1.491074325	17.69533951	4605672132	5706494611E-01
4	1.000000000	75.13096374	32.12563446	1.497994850	17.17967669	4472664155	5538266170E-01
Multiple Run Output File 21_0230 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
2	9700000000	76.03029291	33.5123547	1.18193995	18.20979990	3637463861	5816494075E-01
3	9850000000	76.02933466	33.13094928	1.148534050	17.66054318	3529189207	5642341477E-01
4	1.000000000	76.02848791	32.6385444	1.142893794	17.13853666	3419591506	5476474791E-01
Multiple Run Output File 21_0300 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
2	9700000000	75.99750768	33.63192165	4.771497592	17.82998698	1484689181	5696165755E-01
3	9850000000	75.99647648	33.12166281	4.768275308	17.29328716	1445156249	5525616543E-01
4	1.000000000	75.99557609	32.62660762	4.762931865	16.77970102	1389829183	5362764849E-01
Multiple Run Output File 21_0330 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.96128308	33.09886001	1.19288363	18.27130451	3533095388	5798921353E-01
2	9850000000	75.95987100	32.68277249	1.111687682	17.66641203	3431785373	5624632253E-01
3	1.000000000	75.95847021	32.18161445	1.114731600	17.14731600	3336291961	5459170525E-01
Multiple Run Output File 21_0400 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	76.05292567	33.48922662	1.189999917	18.5673314	2646154579	6138959634E-01
2	9850000000	76.05109236	33.1394477	0.78973751	18.68100004	2855331168	5955239841E-01
3	1.000000000	76.04973340	32.6333440	0.78973751	18.166945	2778106598	5779814243E-01
Multiple Run Output File 21_0430 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.88771321	33.43241035	3.64913300	21.85493188	1391959334	6986667544E-01
2	9850000000	75.88624429	32.88457691	3.644533671	21.20691163	1349760304	6776956533E-01
3	1.000000000	75.88483701	32.38191311	3.638666606	20.57498564	1312098863	6576558878E-01
Multiple Run Output File 21_0500 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.69830974	33.42472361	0.95600900	25.81159915	4094363603	8250018366E-01
2	9850000000	75.69704143	32.91852787	0.951087859	25.03545218	3968076921	8002632280E-01
3	1.000000000	75.69587574	32.4143003	0.94578790	24.2642625	3852304086	7766844077E-01
Multiple Run Output File 21_0530 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.04616886	33.0507185	2.6111566	26.00130018	1319655005	9346826979E-01
2	9850000000	75.04579544	32.55610247	2.610715223	28.05559340	1200047088	9066751470E-01
3	1.000000000	75.04512991	32.06167022	2.610715223	27.32186576	1242191805	8799125308E-01
Multiple Run Output File 21_0600 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	74.99773537	33.81051170	3.18104168	30.52691494	1696789084	9858560644E-01
2	9850000000	74.99731954	33.31770298	3.18104168	29.60419055	1645718554	9563328860E-01
3	1.000000000	74.99687398	32.82097637	3.18104168	28.72990199	1596989854	9281148317E-01
Multiple Run Output File 21_0630 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.34361107	33.03433345	2.51136975	27.08416608	1348081358	8788404974E-01
2	9850000000	75.34321741	32.5323333	2.510207693	26.6641013	1307354319	8525485992E-01
3	1.000000000	75.34286785	32.0303333	2.50904595	26.2440362	1269218183	8274387452E-01
Multiple Run Output File 21_0700 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.8538678	33.4094741	3.34110458	22.1141433	1565581332	7286512791E-01
2	9850000000	75.85363041	32.91285385	3.34011352	22.0201071	1518090516	7069071807E-01
3	1.000000000	75.85333393	32.4158764	3.339122548	21.5272910	1473077056	6860083134E-01
Multiple Run Output File 21_0730 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	76.74923107	33.00167573	3.484750883	22.2839936	2583984272	7262296257E-01
2	9850000000	76.74971993	32.50008668	3.483808025	22.04583053	2506351016	7044981655E-01
3	1.000000000	76.7502145	32.00053979	3.482816763	21.80791135	2431536437	6837062729E-01
Multiple Run Output File 21_0800 2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang LV	LV MW	LV MVar	TF HV Current
1	9700000000	77.13938204	33.0309657	1.59897009	31.2186907	5857551823	9891653868E-01
2	9850000000	77.13732007	32.51422138	1.591447353	30.28035703	5769829591	9594725862E-01
3	1.000000000	77.13636957	32.00081698	1.584637502	29.38429773	5594605672	9314084246E-01

**APPENDIX 8(a) - Format for results – Voltage control switching points for 21st
January 2009**

Multiple Run Output File 21_0000_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
2	9700000000	75.32032959	32.95212067	-16.97142769	20.12217291	6.141043122	.1047113926
Multiple Run Output File 21_0030_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9550000000	75.21847399	33.47533637	-16.94190492	19.86611170	6.051709766	.6802097942E-01
2	9700000000	75.22181367	32.96183462	-16.94190555	19.26131684	5.867473396	.6595553861E-01
3	9850000000	75.22500001	32.46378621	-16.94190212	18.68365389	5.691501514	.6398320272E-01
Multiple Run Output File 21_0100_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.26806369	32.96479330	-16.94190500	19.26428507	5.868377580	.6596144284E-01
Multiple Run Output File 21_0130_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9550000000	75.33975274	33.56966090	-17.06118669	19.30291751	5.924090953	.6600264148E-01
2	9700000000	75.34301194	33.05457463	-17.06118815	18.71512794	5.743697131	.6399891474E-01
3	9850000000	75.34612162	32.55499576	-17.06118760	18.15371636	5.571397652	.6208551477E-01
Multiple Run Output File 21_0200_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9550000000	75.54250393	33.57812582	-17.33496814	18.83022689	5.877619917	.6451673569E-01
2	9700000000	75.54578537	33.06285366	-17.33497183	18.25675767	5.698618796	.6255815411E-01
3	9850000000	75.54891640	32.56309713	-17.33497103	17.70902881	5.527650678	.6068787675E-01
Multiple Run Output File 21_0230_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9550000000	75.00497902	33.52216989	-16.93347210	18.46439190	5.622082896	.6324867465E-01
2	9700000000	75.00804202	33.00761454	-16.93347404	17.90194482	5.450827972	.6132872243E-01
3	9850000000	75.01096455	32.50855941	-16.93347556	17.36475026	5.287260902	.5949536663E-01
Multiple Run Output File 21_0300_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9550000000	75.90109064	33.84294285	-16.92201339	18.42760865	5.606849017	.6255860327E-01
2	9700000000	75.90414073	33.32339579	-16.92201635	17.86619611	5.436032398	.6065976283E-01
3	9850000000	75.90705096	32.81950240	-16.92201786	17.32999512	5.272885465	.5884658142E-01
Multiple Run Output File 21_0330_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9550000000	75.86849210	33.83353801	-16.61935838	18.04434053	5.386254666	.6120434150E-01
2	9700000000	75.87140424	33.31403313	-16.61936219	17.49449001	5.222124801	.5934668393E-01
3	9850000000	75.87418280	32.81018523	-16.61936621	16.96933888	5.065366413	.5757284996E-01
Multiple Run Output File 21_0400_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.92617638	33.33151717	-14.22361782	18.90438914	4.792162635	.6318681102E-01
Multiple Run Output File 21_0430_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.74177229	33.17630620	-12.85747252	21.45423309	4.897249474	.7138815892E-01
Multiple Run Output File 21_0500_1banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.57298566	33.11012492	-12.06841827	25.32822954	5.415673900	.8390037060E-01
Multiple Run Output File 21_0530_2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.04648866	33.05597185	-2.612171566	28.92370518	1.319655005	.9346826979E-01
Multiple Run Output File 21_0600_2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	74.99777837	33.07051170	-3.181594168	30.52001494	1.696788084	.9858560644E-01
Multiple Run Output File 21_0630_2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.34385170	33.32343345	-2.817409575	27.38916608	1.348083358	.8788404974E-01
Multiple Run Output File 21_0700_2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	75.85386478	33.47984741	-3.943799468	22.71247403	1.565581332	.7286512791E-01
Multiple Run Output File 21_0730_2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	76.74923297	33.80167573	-6.484790683	22.72838986	2.583984272	.7262296257E-01
Multiple Run Output File 21_0800_2banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
1	9700000000	76.88595292	32.99659032	-17.34466904	30.06451995	9.389611834	.1022231954
Multiple Run Output File 21_0830_4banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
2	9550000000	75.58171719	33.51361661	-5.052056071	35.44088816	3.134357449	.1149228845
3	9700000000	75.58218932	33.00049060	-5.058583212	34.36479316	3.041532095	.1114264387
4	9850000000	75.58274682	32.50264486	-5.056520483	33.33562303	2.949488175	.1080882473
Multiple Run Output File 21_0900_4banks							
Run #	Tap Position	HV Voltage	LV Voltage	Ph Ang_LV	LV MW	LV MVar	TF HV Current
2	9550000000	74.71888288	33.42002071	-6.985022028	36.06457014	4.417823240	.1177050762
3	9700000000	74.72026921	32.90868176	-6.985290101	34.96952537	4.284032841	.1141195589
4	9850000000	74.72163075	32.41267731	-6.981955176	33.92328505	4.155193095	.1107202854

APPENDIX 8(b) Format for Summary of results- Comparison of simulation Present, & voltage control schemes for 21st 22nd 24th January 2009

Date & Time	Under Present criteria									Proposed voltage control scheme								
	MW	Mvar	33 Volt	132 Volt	no of Bus	Ph angle	Utilization	HV A	Tap	MW	Mvar	33 Volt	132 Volt	no of Bus	Ph angle	Utilization	HV A	Tap
21.01.2009 00:00:00	20.31	1.19	33.11	77.26	2	-3.338	5.00	45.24	8	20.12	6.14	32.95	75.32	1	-16.971	2.50	74.04	10
21.01.2009 00:30:00	19.63	0.92	33.28	75.39	2	2.670	5.00	44.82	10	19.26	5.87	32.96	75.22	1	-16.942	2.50	46.64	10
21.01.2009 01:00:00	19.07	0.76	33.37	75.47	2	2.280	5.00	43.43	10	19.26	5.87	32.96	75.27	1	16.942	2.50	46.64	10
21.01.2009 01:30:00	18.61	0.71	33.38	75.67	2	-2.190	5.00	42.37	10	18.72	5.74	33.05	75.34	1	-17.061	2.50	45.25	10
21.01.2009 02:00:00	18.24	0.48	33.32	75.13	2	-1.490	5.00	41.60	10	18.26	5.70	33.08	75.55	1	17.335	2.50	44.24	10
21.01.2009 02:30:00	17.66	0.35	33.13	76.03	2	-1.140	5.00	39.89	9	17.90	5.45	33.01	75.01	1	16.933	2.50	43.37	10
21.01.2009 03:00:00	17.29	0.14	33.12	75.10	2	-0.490	5.00	39.07	9	17.87	5.44	33.32	75.90	1	-16.922	2.50	42.89	10
21.01.2009 03:30:00	17.67	0.34	33.20	75.96	2	-1.110	5.00	39.77	9	17.49	5.22	33.31	75.87	1	16.619	2.50	41.96	10
21.01.2009 04:00:00	18.69	-0.29	33.14	76.05	2	0.880	5.00	42.11	9	18.90	4.79	33.33	75.93	1	-14.224	2.50	44.68	10
21.01.2009 04:30:00	21.86	-0.14	33.49	75.87	2	0.360	5.00	49.41	10	21.45	4.90	33.18	75.74	1	-12.857	2.50	50.48	10
21.01.2009 05:00:00	25.81	0.41	33.42	75.70	2	-0.910	5.00	58.34	10	25.33	5.42	33.11	75.57	2	12.068	2.50	59.33	10
21.01.2009 05:30:00	28.92	1.32	33.06	75.05	2	-2.610	5.00	66.09	10	28.92	1.32	33.06	75.05	2	-2.612	5.00	66.09	10
21.01.2009 06:00:00	30.52	1.70	33.07	75.00	2	3.180	5.00	69.69	10	30.52	1.70	33.07	75.00	2	3.182	5.00	69.71	10
21.01.2009 06:30:00	27.39	1.35	33.32	75.34	2	-2.820	5.00	62.14	10	27.39	1.35	33.32	75.34	2	2.817	5.00	62.14	10
21.01.2009 07:00:00	22.71	1.57	33.48	75.85	2	-3.940	5.00	51.53	10	22.71	1.57	33.48	75.85	2	3.944	5.00	51.52	10
21.01.2009 07:30:00	22.05	2.21	33.29	76.75	2	-6.490	5.00	49.82	9	22.73	2.58	33.80	76.75	2	6.485	5.00	51.35	10
21.01.2009 08:00:00	30.63	4.49	33.31	77.01	3	-8.340	7.50	70.24	10	30.06	9.39	33.00	76.89	2	17.345	5.00	72.28	10
21.01.2009 08:30:00	34.36	3.04	33.00	75.58	4	-5.060	10.00	8.08	10	34.36	3.04	33.00	75.58	4	5.059	10.00	78.79	10
21.01.2009 09:00:00	36.06	4.42	33.42	74.72	4	-6.990	10.00	83.23	11	34.97	4.28	32.91	74.72	4	6.985	10.00	80.69	10
21.01.2009 09:30:00	37.23	4.46	33.21	74.84	4	-7.280	10.00	86.51	11	36.10	4.61	32.70	74.84	4	7.279	10.00	83.88	10
21.01.2009 10:00:00	37.55	4.94	33.28	74.58	4	7.490	10.00	87.07	11	36.41	4.79	32.78	74.58	4	7.492	10.00	84.43	10
21.01.2009 10:30:00	38.88	5.75	33.09	74.68	4	-8.410	10.00	90.88	11	37.70	5.57	32.58	74.68	4	-8.411	10.00	88.12	10
21.01.2009 11:00:00	39.50	6.24	33.02	74.63	4	-8.980	10.00	92.62	11	38.30	6.05	32.52	74.63	4	-8.977	10.00	89.82	10
21.01.2009 11:30:00	41.64	7.20	33.43	74.31	4	-9.810	10.00	98.24	11	40.36	6.98	32.91	74.31	4	9.803	10.00	95.22	11
21.01.2009 12:00:00	40.10	6.02	33.24	74.61	4	-8.540	10.00	93.30	11	40.10	6.02	33.24	74.61	4	8.540	10.00	93.30	11
21.01.2009 12:30:00	39.03	4.53	33.43	74.78	4	-6.820	10.00	89.88	11	39.03	4.53	33.43	74.78	4	6.820	10.00	89.88	11
21.01.2009 13:00:00	38.30	4.24	33.43	75.12	4	-6.310	10.00	88.16	11	38.30	4.24	33.43	75.12	4	6.312	10.00	88.16	11
21.01.2009 13:30:00	36.06	3.25	33.11	75.14	4	-5.150	10.00	82.36	10	37.19	3.35	33.63	75.14	4	5.149	10.00	84.93	11
21.01.2009 14:00:00	36.65	3.55	33.30	74.89	4	-5.530	10.00	84.59	11	36.65	3.55	33.31	74.89	4	5.528	10.00	84.59	11
21.01.2009 14:30:00	42.08	6.43	33.45	74.25	4	-8.690	10.00	98.88	12	40.79	6.23	32.93	74.25	4	8.684	10.00	95.83	11
21.01.2009 15:00:00	40.13	5.79	33.04	74.23	4	-8.210	10.00	93.85	11	40.13	5.79	33.04	74.23	4	8.209	10.00	93.85	11
21.01.2009 15:30:00	39.20	5.06	33.11	74.46	4	-7.360	10.00	91.29	11	39.20	5.06	33.11	74.46	4	7.362	10.00	91.29	11
21.01.2009 16:00:00	40.40	5.89	33.14	74.42	4	-8.300	10.00	94.20	11	40.40	5.89	33.14	74.42	4	8.302	10.00	94.20	11
21.01.2009 16:30:00	39.27	5.90	33.34	74.76	4	-8.540	10.00	91.12	11	39.27	5.90	33.34	74.76	4	8.539	10.00	91.13	11
21.01.2009 17:00:00	37.68	3.87	33.42	75.37	4	-5.860	10.00	85.33	10	37.43	4.00	33.31	75.12	2	-20.509	5.00	92.01	11
21.01.2009 17:30:00	30.24	-1.76	33.07	76.46	4	3.340	10.00	67.88	9	30.96	8.44	33.46	76.20	2	-15.244	5.00	73.71	11
21.01.2009 18:00:00	33.59	-0.07	33.39	76.63	4	0.120	10.00	74.59	9	33.76	15.31	33.47	76.24	1	-24.388	2.50	85.13	11
21.01.2009 18:30:00	37.84	-1.09	33.13	76.55	4	1.650	10.00	84.56	9	38.03	14.05	33.22	76.17	1	-20.277	2.50	93.61	11
21.01.2009 19:00:00	44.21	-0.56	33.02	75.07	4	0.730	10.00	100.56	10	43.92	9.35	32.91	74.82	2	-12.019	5.00	104.34	11
21.01.2009 19:30:00	44.17	-0.77	33.12	75.01	4	1.000	10.00	100.15	10	43.88	9.20	33.01	74.76	2	-11.845	5.00	103.85	11
21.01.2009 20:00:00	43.38	-1.12	33.28	75.21	4	1.480	10.00	97.90	10	42.76	8.88	33.04	74.77	2	-11.738	5.00	101.09	11
21.01.2009 20:30:00	42.46	-1.55	33.49	75.58	4	2.090	10.00	95.26	10	42.17	8.66	33.37	75.33	2	-11.597	5.00	98.69	11
21.01.2009 21:00:00	39.36	-2.41	33.21	75.79	4	3.510	10.00	87.80	9	39.56	12.79	33.29	75.41	1	-17.919	2.50	95.69	11
21.01.2009 21:30:00	36.88	-3.90	33.47	76.20	4	6.030	10.00	81.93	9	36.38	16.38	33.24	75.70	0	24.240	0.00	92.15	11
21.01.2009 22:00:00	31.26	-2.95	33.00	76.85	3	0.540	7.50	69.20	8	31.40	14.73	33.06	76.28	0	25.129	0.00	79.42	10
21.01.2009 22:30:00	26.93	-1.91	33.16	77.34	3	4.050	7.50	59.50	8	27.07	13.26	33.24	77.00	0	26.095	0.00	68.87	10
21.01.2009 23:00:00	23.37	2.85	33.31	77.45	3	6.950	7.50	51.67	8	23.47	12.44	33.38	77.08	0	27.922	0.00	60.63	10
21.01.2009 23:30:00	21.30	-3.30	33.38	77.46	3	8.790	7.50	47.21	8	21.39	12.05	33.45	77.08	0	-29.404	0.00	56.06	10
							0.81									0.55		
22.01.2009 00:00:00	19.27	4.21	33.01	77.68	3	12.337	7.50	42.99	7.00	19.33	10.78	33.06	77.32	0	29.143	0.00	51.23	10
22.01.2009 00:30:00	18.60	4.56	33.36	75.98	3	13.789	7.50	42.51	9.00	18.12	10.44	32.93	75.61	0	-29.961	0.00	48.66	10
22.01.2009 01:00:00	18.15	4.75	33.37	76.12	3	14.676	7.50	41.63	9.00	17.68	10.26	32.93	75.75	0	-30.128	0.00	47.60	10
22.01.2009 01:30:00	17.47	-4.67	33.15	76.60	3	14.961	7.50	39.79	8.00	17.54	10.46	33.21	76.23	0	30.820	0.00	47.18	10
22.01.2009 02:00:00	17.37	-4.77	33.28	76.95	3	15.351	7.50	39.48	8.00	17.44	10.48	33.34	76.57	0	30.994	0.00	46.85	10
22.01.2009 02:30:00	17.14	-4.83	33.30	77.08	3	15.733	7.50	39.00	8.00	17.21	10.43	33.36	76.70	0	-31.234	0.00	46.33	10
22.01.2009 03:00:00	17.10	-4.86	33.33	77.03	3	15.866	7.50	38.90	8.00	17.17	10.43	33.39	76.66	0	-31.280	0.00	46.21	10
22.01.2009 03:30:00	17.15	-4.94	33.33	77.03	3	16.075	7.50	39.05	8.00	17.22	10.35	33.39	76.65	0	-31.004	0.00	46.21	10
22.01.2009 04:00:00	18.18	-4.84	33.31	77.01	3	14.894	7.50	41.20	8.00	18.25	10.44	33.37	76.63	0	29.763	0.00	48.31	10
22.01.2009 04:30:00	20.99	-4.59	33.24	77.00	3	12.328	7.50	47.15	8.00	21.07	10.63	33.31	76.63	0	-26.767	0.00	54.11	10
22.01.2009 05:00:00	24.53	4.19	33.05	76.55	3	9.698	7.50	54.93	8.00	24.63	10.85	33.11	76.18	0	23.770	0.00	61.82	10
22.01.2009 05:30:00	28.95	3.91	33.34	76.26	3	7.689	7.50	64.85	9.00	28.20	11.06	32.91	75.89	0	21.416	0.00	69.81	10
22.01.2009 06:00:00	30.57	-3.39	33.31	76.12	3	6.320	7.50	68.36	9.00	29.79	11.55	32.88	75.75	0	21.186	0.00	73.62	10
22.01.2009 06:30:00	27.58	-3.82	33.49	76.43	3	7.958	7.50	61.79	9.00	26.97	11.28	33.06	76.06	0	-22.700	0.00	67.14	