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UNIFIED POWER FLOW CONTROLLER AS POWER SYSTEM STABILIZER

A dissertation submitted to the Department of Electrical Engineering, University of Moratuwa in partial fulfilment of the requirements for the degree of Master of Science

by

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DECLARATION

The work submitted in this dissertation is the result of my own investigation, except where otherwise stated.

It has not already been accepted for any degree, and is also not being concurrently submitted for any other degree.

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Abstract

The FACTS device - Unified Power Flow Controller (UPFC) and its performance is studied under transient condition in the usage as a power system stabilizer. This device creates an impact on power system stability with its unique capability to control real and reactive power flows simultaneously on a transmission line and regulate voltage at the bus connected. These features become significant as the UPFC can allow loading of the transmission lines up to their thermal limits by regulating the power flow through desired paths. This gives the power system operators the desired flexibility in satisfying the deregulated power system imposed requirements.

The new technology associated with the UPFC and its structure is studied. The theoretical analysis is done in verifying its capability for stability enhancement. A practical system is modelled to verify the theoretical analysis in MATLAB/SIMULINK platform.

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Theoretical and practical verification reveals the function of UPFC in power system stabilisation. The parameters associated with the UPFC are studied for optimum stability criterion.

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