IN-RUSH CURRENT MITIGATION ON TOROIDAL TRANSFORMERS WITH SLOTTED CORE

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Dissertation submitted in partial fulfillment of the requirements for the degree of Master of Science in Electrical Installation

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DECLARATION OF THE CANDIDATE AND SUPERVISORS

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Date

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ABSTRACT

When it comes to transformer industry, toroidal transformers plays a major role, especially in high-tech applications, as they outperform traditional laminated transformers. However, toroidal transformers have a much higher inrush current, especially compared to laminate transformers, which will be a major drawback at the high power applications.

Currently there are many options available outside the toroidal transformer to avoid this inrush problem, but reliability issues will still there when using external inrush controlling mechanisms. Traditional inrush current mitigation methods on transformers are not sufficient for toroidal transformers. These methods tend to reduce good performance as well as inrush current.

The proposed inrush current mitigation method using a transformer-based slotted core, significantly reduces the inrush current while protecting the excellent performance characteristics which is typical for toroidal transformers. In addition, it offers better control of the inrush current than traditional methods.

The proposed method is a slotted core which has a slot in the outer periphery.

That controls the saturation inductance and hence the inrush current.

At the end, the slotted core maintains high performance without compromising normal operation.

This document includes a practical development of slotted cores and as well as experimental tests of inrush current, and finally a new design tool for the optimized deigns.

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LIST OF ABBREVIATIONS

Abbreviation	Description
AC	Alternative Current
AISI	American Iron and Steel Institute
DC	Direct Current
EMI	Electro Magnetic Interference
GOSS	Grain Oriented Silicon Steel
Н	Height
ID	Inner Diameter
IEC	International Electrotechnical Commission
MMF	Magneto Motive Force
MPL	Magnetic Path Length
NC	Nano Crystalline
NGOSS	Non Grain Oriented Silicon Steel
NTC	Negative Temperature Coefficient
OD	Outer Diameter
RMS	Root Mean Square

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INTRODUCTION

Inrush current (sometimes called input surge current) is defined as maximum peak current drawn by electrical equipment due to driving its core into deep saturation at the time of energization. Inrush current is an undesirable phenomenon to occur and the equipment manufacturers/designers have to take this in to consideration where it is applicable. Elimination of inrush current could be very costly and impossible but mitigation of inrush current is possible [1].

Generally for all cases inrush current does not last for a long time. For example it lasts only for few cycles for alternating current (AC), for transformers and motors. Magnitude of inrush current to its rated current could be several times, or even closer to 30 times in extreme cases, especially with toroidal transformers [2]. The magnitude of the inrush current is based on several parameters like; switching angle, source impedance, magnitude of input voltage, residual flux on the core, saturation inductance, etc. As a result, more often overcurrent protection reacts for these high currents and trips the device from the source resulting inability to energize the equipment. Also the inrush current will result in significant voltage drops, and thus affect the power quality, reliability and stability [2].

Inrush current most of the time is harmless to the device but unwanted tripping could cause undue problems to the electrical system. But in special cases, mostly with toroidal transformers which normally connected at high end applications, it needs to protect the expensive power electronic equipment from the high currents [2].

To understand this phenomenon in transformers and motors it requires sound knowledge of mathematics and magnetism. Inrush current occurrences on transformers are explained in chapter 2 to the extent of the topic being discussed. Typical inrush current transient waveform when a transformer is energized is illustrated in Figure 1.1, which is captured on a single phase 1000VA toroidal transformer.



Figure 1.1: Inrush current transient waveform

1.1 Toroidal Transformer Construction

Toroidal winding is considered to be challenging with respect to winding in laminated transformer, as it required rotating the coil during winding through the inner diameter of the core. Typical toroidal core does not hold any gaps in its magnetic path, which cause the toroidal transformer to be high performing with respect to the laminated transformers. The performance of an ideal transformer can be closely approximated with this most expensive toroidal construction [1].

In the manufacturing process, sufficient winding wire must be loaded into the winding shuttle of the machine, and then wind onto the toroidal transformer's core as per the required turns in the particular design. This will be done for the both primary and secondary windings of the transformer. Also it can wind multiple parallel windings at once, hence saving cost.

For primary - secondary isolation transformer, insulation is required in between the primary winding and the secondary winding. Generally the exposed enamel copper wire is protected by outer wrapping insulation tape for safety purpose. Normally all the insulations are done based on the creep and clearance distance requirements coming under IEC 61558 standard.

Toroidal transformer will not require a winding bobbin like with the laminated construction, but core insulation will act as a bobbin, which is creating better coupling of flux in the core together with the windings.

Toroidal construction is not much common and not popular in the industry due to its manufacturing complexity and high cost. However toroidal components can be seen in high end applications regardless of its high costs due to their high performance requirements.

Figure 1.2 shows a single-phase toroidal transformer which goes to a power supply unit for high-end audio amplifier.



Figure 1.2: Toroidal transformer

Widely used transformer construction is laminated type transformers due to its simplicity in construction. A typical laminated type transformer is illustrated in Figure 1.3. When comes to low power transformers, most of the laminated type transformers are made with EI shaped core laminations. These are stamped as English letters 'E' and 'I' and these E's and I's are then stacked to form the core. Then the copper wire winding is done on a bobbin, and the wound bobbin is then inserted into the stacked E sections and then the I section will fixed on top of it.



Figure 1.3: EI laminated transformer

1.2 Motivation to the Research

Toroidal transformer has its advantages over laminated transformers of high efficiency, low weight, low leakage and low Electro Magnetic Interference (EMI), low volume, etc. The core loss in a toroidal transformer is very low since its gapless round shape, and which supports and allows the magnetic flux to travel ideally in a less reluctance magnetic path with minimum stray field. As a result, when designing toroidal transformers, the designers can go for high design flux densities and utilise material effectively than in laminated type [1].

However due to its low reluctance to flux, toroidal transformer exhibits severe inrush currents than standard laminated type transformers. This is one major drawback in toroids and it becomes worst when it comes to high power transformers. The situation gets worsen when higher quality grade steel is used, due to even low reluctance in the core to the flux.

High quality grain-oriented silicon steel (GOSS) has steep induction curve against excitation current and also they do have higher residual flux (remanence flux) which cause high inrush of the transformer.

Presently there are many methods to limit inrush current on toroidal transformers, both using external equipment based and transformer-based solutions. But most of the existing transformer-based mitigation methods are weakening the performance indicators of the toroidal transformer design; even it is more reliable than the external equipment based inrush current mitigation methods [1].

Therefore still there is an industry requirement to search for a more developed and optimized inrush mitigation method to boost the market share on toroidal transformers.

1.3 Objective of the Research

The main objective of this research is to develop a reliable and economical transformer-based inrush current mitigation method for toroidal transformers, which is compatible with the srilankan manufacturing facility. The conceptual proposal would be to use a slotted core which has a slot at the outer periphery of the steel core.

Using the proposed slotted core method, it is supposed to limit inrush current while maintaining its performance. With this method, it is expecting to save material costs and labour on the product and overall being competitive in the market.

The ultimate goal of the project is to promote toroidal transformers in the industry over other types of transformers, even in the high power levels.

INRUSH CURRENT IN TOROIDAL TRANSFORMERS

As mentioned in chapter 1 above, inrush current is a main problem for toroidal transformers than laminated transformers, especially considering high power levels. A brief introduction to toroidal transformers is provided in chapter 1 ,Here we will discuss the following key topics to better understand inrush current scenarios using toroidal transformers.

- 1) Theoretical background of inrush current
- 2) Toroidal core
- 3) Saturation inductance and inrush current

2.1 Theoretical Background of Inrush Current

This is a transient scenario, where high saturation of the transformer core originates high inrush current at the point of energization. There are several explanations on this scenario in several sources, but the below will illustrate the basics of the inrush current occurrence in a much clearer way.

Basically the input voltage applied to the transformer will be the driving force to the inrush current and that will force the flux to build up double the steady state flux plus the remanence flux. Hence the transformer gets in to deep saturation and that result with creating a high energization current [3].

Inrush current occurrence of a transformer is a transient effect which could be explained with electromagnetism as described follows.

The inrush current phenomenon is governed by Faraday's law [3].

$$\mathbf{v}(\mathbf{t}) = \frac{d}{dt} \mathbf{\phi}'(\mathbf{t}) \tag{2.1}$$

Where v(t) is the instantaneous voltage applied and $\varphi'(t)$ is the instantaneous flux linkage.

Then,
$$\phi'(t) = \int_0^t v(t) dt \qquad (2.2)$$

Neglecting the leakage flux component, the total instantaneous flux $\Phi(t)$ of the core with N number of turns of the winding can be written as,

$$\phi'(t) = N\phi(t) \tag{2.3}$$

Then with combining equations (2.2) and (2.3)

$$\phi(t) = \frac{1}{N} \int_0^t v(t) dt \qquad (2.4)$$

Consider the supply voltage for the transformer is sinusoidal with switching angle θ v(t) = V_m. sin (ω t+ θ)

Then re-write equation (2.4) with sinusoidal supply voltage v(t)

$$\phi(t) = \frac{1}{N} \int_0^t V_m \sin(\omega t + \theta) dt \qquad (2.5)$$

$$\phi(t) = \frac{V_m}{N} \int_0^t \sin(\omega t + \theta) dt$$

$$\phi(t) = \frac{V_m}{N\omega} \cdot \left[-\cos(\omega t + \theta)\right]_0^t$$

$$\phi(t) = \frac{V_m}{N\omega} \cdot \left[-\cos(\omega t + \theta) + \cos\theta\right] + \phi(0)$$

Considering the remanence flux at t=0 is $\phi(0) = \phi_r$ (Remanence flux) Then,

$$\phi(t) = \frac{V_m}{N\omega} \cdot \left[-\cos\left(\omega t + \theta\right) + \cos\theta\right] + \phi_r$$

The maximum flux ϕ_{max} is generated at zero crossing of the input voltage applied; means $\theta = 0$

$$\phi_{max} = \frac{V_m}{N\omega} \cdot [2] + \phi_r$$
$$\phi_{max} = \frac{2V_m}{N\omega} + \phi_r$$

$$\phi_{max} = 2\phi_m + \phi_r \tag{2.6}$$

So it is proven that, the inrush transient forces the flux to build up double the steady state flux plus the remanence flux. This scenario can be illustrated in graphical form as per the Figure 2.1 [3].



Figure 2.1: Graphical interpretation of Inrush current with remanence

2.2 Toroidal Core

Toroidal core is having donut shape with no air gaps in the magnetic path, against the laminated transformer cores. These cores are available in many material types; Silicon steel (SiFe), Nickel iron (NiFe), Perm-alloy, Nano-crystalline (NC) and others [1]. Silicon steel and nickel iron mainly available as tape wound cores or laminated pieces. In this research, only Silicon steel types are considered for toroidal transformer cores.

2.2.1 Silicon steel

Silicon steel is available as tape wound reels with different types/grades, thicknesses, widths and can be purchased based on the requirements of the relevant designs. Standard available steel widths are varying with the steps of 5mm, but still possible to purchase even other sizes in between, based on the demand. Figure 2.2 shows a silicon steel coil before the slitting process done, and in this form it is called as the 'Mother coil'. Mother steel coil is commonly available with 350mm width.



Figure 2.2: Silicon steel mother coils

2.2.2 Silicon steel on toroidal core

Generally the silicon steel contains high permeability (μ) providing low reluctance (R) for a given Magnetic Path Length (MPL). When a transformer core is magnetized to the flux density (B) and the permeability increases as per following basic equation.

$$B = \mu. \,\mathrm{H} \tag{2.7}$$

Where H – Magnetizing force

According to the BH characteristics of a typical silicon steel (see Figure 2.3), it maintains an almost linear relationship between B and H up to a certain magnetic flux density (which maintains maximum permeability), after which the steel is in the saturated region . Start to get closer. Hence, when the density of the magnetic flux increases further in the saturation region, the permeability decreases, approaching the values in the free space or in the air. This area is called the deep saturation of the core. This is a common scenario for all types of silicon steel, except for the difference in density of the magnetic flux where saturation begins.

Electrical steel comprises of various grades and have different classifications. One of the standard classifications is from its AISI grade. AISI stands for American Iron and Steel Institute [1].

Mainly the grain oriented steel type AISI M-5 is used for conventional low inrush designs to retain better performance, even together with fully air-gapped core. See Figure 2.3 for the steel characteristic curves taken from Kawasaki Steel data catalogue [4].

In this research also I have used M5 steel for the slotted core. Basically un cut area will be dominate at the normal operation and the slotted area will be dominate at the inrush condition.

2.3 Saturation Inductance and Inrush Current

In this research, it is proven that the most significant parameter affect to the inrush is saturation induction. In many literatures, they say the input winding resistance mainly affects the inrush current, but in practical scenario the designers do not have much allowance to change the winding resistances having a design is normally bound for particular temperature class.

Therefore in this research, the effects of saturation induction to the inrush current is mainly discussed and go through details on the inrush current variation by changing the slot depth of the outer periphery, hence changing the saturation induction.



RESEARCH DESIGN

In chapter 2, it was discussed about the inrush current phenomenon on toroidal transformers and about the factors that affect the magnitude of in rush transients; mainly the saturation inductance and electrical steel characteristics.

In this chapter following aspects are discussed descriptively.

- 1) Existing inrush current mitigation methods
- 2) Proposed slotted core concept for inrush current mitigation
- 3) Calculation of saturation inductance Ls
- 4) Calculation of inrush current

3.1 Existing Inrush Current Mitigation Methods

There are two main methods to mitigate inrush currents, those are external inrush control and others are transformer based in built inrush control systems.

Out of these two categories, most of the high end applications prefer the transformerbased solutions for inrush current mitigation, due to their higher reliability [1]. Followings are some of the methods used by designers/manufacturers for mitigation of inrush currents in toroidal transformers.

3.1.1 NTC thermistor in primary winding

The main advantage of this method is, here the transformer can be designed in higher flux density utilizing the magnetization curve to its maximum possible point. Also the transformer efficiency, weight, dimensions could also be to its optimum and also that lead avoiding complex manufacturing processes, hence finally be economical.

The Negative Temperature Coefficient (NTC) thermistors are thermally sensitive semiconductor resistors which exhibit inverse characteristic between the resistance and the absolute temperature, as shown in Figure 3.1. In typical operation of the NTC

thermistor, this is connected in series with the transformer input winding and initially holding high resistance at lower ambient temperature. But after the transformer is powered up, the resistance of the NTC thermistor can be brought down either by a change in the ambient temperature or by self-heating resulting from current flowing through the device [1].



THERMISTOR CURVE

Figure 3.1: Typical characteristic curve for NTC

Referring the Figure 3.1, the x-axis is representing the temperature and the resistance by Y axis. That shows how the resistance drops with the temperature rise.

The main dis-advantages of this method are the addition of primary resistance to normal operation of the transformer and the heat dissipation and ultimately leading to low efficiency of the total equipment. And the next drawback is, it will not do the intended function in successive power interruptions, because due to the thermal inertia the thermistor may hold high temperature - low resistance stage in the next power up. The other drawback is the reliability. Transformer itself is highly reliable but adding the NTC thermistor in series with the supply makes the combination unreliable.

3.1.2 Use of NGOSS

The typical B-H curve for silicon steel presents steep magnetization characteristic after they exceed the maximum unsaturated flux density. This characteristic is far great especially with GOSS types, while it is not that much critical for NGOSS types.

Generally toroidal transformers are wound using high grade GOSS for its common intended performances, but the said steep magnetization curves of GOSS and high design flux density makes easy to saturate the core. See Figure 3.2 and Figure 3.3 for magnetization characteristics with corresponding loss curves for GOSS (AISI M-5) [4].



Figure 3.2: Magnetization characteristics for GOSS-AISI Grade M5



Figure 3.3: Core loss curve for GOSS-AISI Grade M5

As a result, designers are using NGOSS for low inrush designs due to lower steep characteristics in magnetization [1]. NGOSS transformers have to be designed in low flux density and then its narrow magnetization characteristics can be used to keep it unsaturated, compared to the GOSS types. Following Figures 3.4 to 3.5 illustrates magnetization characteristics with corresponding loss curves for NGOSS (35H300) [4], for easy understanding of above mentioned point.



Figure 3.4: Magnetization characteristics for NGOSS 35H300



Figure 3.5: Core loss curve for NGOSS 35H300

Selection of NGOSS does reduce the inrush current to some extent, but when the application is critical on inrush current, the designers also tend to use above NGOSS without annealing process [1]. Annealing is a special heat treatment process done to regain the magnetic properties back to steel core, after it has been lost in the core manufacturing process.

The main drawback of this method is the less efficiency of the transformer due to high core losses (see Figure 3.5) and high excitation current. These designs are obviously bulky and weight is more than the standard GOSS designs.

However reliability point of view this method is better than the method described in previous section 3.1.1.

3.1.3 Cut core toroidal transformers

Introducing a cut (or a small air-gap) to the magnetic path of the toroidal core will change magnetization characteristics of the steel; basically this will increase the unsaturation characteristic even at the high flux densities. Based on the BH characteristics, it will reduce the slop of the curve (or reduce permeability) and bring the knee point to the right side of magnetization curve, while increasing the magnetizing force.

Also the other main purpose is reducing the remanence flux (ϕ_r). As per the equation 2.6 derived for inrush current (also Figure 2.1), the remanence flux (or remanence flux density, B_r) plays an important role in the inrush current. The Figures 3.6 illustrates how the remanence flux density get reduced (by ΔB) together with an air gap in the toroidal core [5].



Figure 3.6: BH loops before and after core cut

There are several advantages and disadvantages of this method.

An advantage of this method is, this method does not change the core losses with respect to the uncut core. Note in Figure 3.6, the areas within the BH loops for with and without air-gap are almost same.

Also this inrush mitigation method is more reliable than the external NTC thermistor option described in previous section 3.1.1.

Regarding the disadvantage; the gapped cores need more Magneto Motive Force (MMF) to magnetise the core than the normal toroidal core, hence it draws higher current in the off-load condition. Due to that reason, the gapped core transformer consumes lot more reactive power loss. Therefore this cannot be designed at its optimum flux density and hence should be designed approximately 30% lower value. Also core vibration due to loose lamination and noise issues could be an issue in the end application [1].

Based on the discussed inrush current mitigation methods, the gapped core option is mostly used in applications due to its reliability and other advantages. But still it is necessary to overcome its disadvantages, and hence the slotted core method introduced with this research.

3.2 Proposed Slotted Core Concept for Inrush Current Mitigation

3.2.1 Scope of the Research

Together with the discussions this research will be confined into the following scope.

- Design flux density varied from 0.9T to 1.6T and experiments conducted only for 230V mains input.
- 2) Slot width kept constant as blade thickness of 1.5mm
- 3) Slot depth varied as 5, 10, 15, 20, 25mm.
- 4) Considered only the steel types M5 steel.
- 5) Considered transformer power range 1kVA

3.2.2 Methodology

In this method, a slot will be added to the outer periphery of the toroidal core as below. As discussed in the previous chapter, what we supposed to control is the saturation inductance of the core, hence the inrush current.





Figure 3.7: Slotted core

Here the thickness of the slot will be constant as the cutting blade thickness.

And the slot depth will be varied throughout the research in order to get the optimum value.

3.2.3 Simulation of flux distribution

In toroidal transformers, centre of the core will be magnetized at the no load condition, as in below FEMM simulation. As per the theory the majority of flux will concentrate on the lowest magnetic path length (means close to the inner core),



Figure 3.8: No load condition

But at the inrush condition, flux will be distributed in the complete core and the core will get saturated. That inrush condition also can be simulated as below.



Figure 3.9: Inrush condition

3.2.4 Development of slotted core

Toroidal core has made with a steel strip wound on a circular bobbin. So before the core cutting, first of all core varnishing has to be done to avoid peel off strips. In order to make this slot, existing core cutting machines (Band saw machines) has to be modified with circular saw and need a special jig for slot cutting.





Figure 3.10: Machine modification for slot cutting

Without this machine modification we cannot cut the slot because the existing band saw can cut only full way through the complete core. So this modification is essential to avoid practical issues with slot cutting.

Then finally slot cutting can be done as below Figure 3.10.



Figure 3.11: Slotted core

Then as per the research scope , slot depths are made for 5 levels as below. (5,10,15,20,25mm depths)



Figure 3.12: Different slot sizes

3.2.5 Transformer winding

In this research transformers were designed for 1.6T flux density and throughout the research flux density will be changed for 8 levels 0.9T to 1.6T by increasing no of turns for inrush testing.

When considering 5 different slot depths and 8 flux densities, all together 40 samples will be tested for inrush current.



Figure 3.13: Sample windings

3.2.6 Theoretical inrush calculation

Slot depth is the main design parameter in designing process with slotted core designs. First, the equation 3.1 is showing the general relationship between the maximum inrush current and the impedance of the product [6] [7].

$$I_{max} = \frac{V_m}{\sqrt{(\omega L_s)^2 + R^2}} \cdot (1 + \cos\theta + \frac{B_s - B_r}{B_n})$$
(3.1)

- Imax Maximum inrush current (peak current)
- V_m Maximum input voltage (peak voltage)
- R Winding resistance
- L_s Saturation inductance
- θ Switching angle
- Br Residual flux density
- B_s Saturation flux density
- B_n Nominal design flux density

According to the equation 3.1, it is obvious that increasing the Saturation inductance (L_s) and Winding resistance (R) will be the main option to minimize the inrush current. When comes to winding resistance, in practical situation the designer does not have much allowance to changed resistance having the product itself should complied with certain thermal class. Hence changing the winding resistance is not an option to control the inrush current. Therefore, increasing the Saturation inductance will be the only option in this regards.

So in this case when the slot depth will be increased, saturation inductance increases. But at some point it will be effect for the core saturation and it counter effects to increase inrush current. So there will be an **optimum depth** for a transformer to meet the specified inrush current. The following test has been done to the 1000VA transformer, where the inrush current is measured with different slot depths.



Figure 3.14: 1.6T flux sample test results

With the above test results we can see that for a particular flux density there is an optimum slot depth where we can have the minimum inrush current.

Since the inrush current depends on the flux density, the experiment has followed for 8 levels of flux densities.



Figure 3.15: 0.9T flux sample test results
Above is the test result for different gap sizes, for 0.9T flux density. There we can see the effect of flux density for the inrush current of the transformer.

This flux density was changed by increasing no of turns of the already wound samples. Even at that lower flux, the inrush curve shows that there is an **optimum slot depth** for the minimum inrush. Here below is the test result for the 40 samples.



Figure 3.16: Sample test results

By analysing above test results, we can see that we can control the inrush current by controlling the design flux density and the slot depth. So according to the customers requirement we can select those figures and get the optimum design output.

3.3 Calculation of saturation inductance Ls

According to the concept, as the slotted core subjected to deep saturating condition, the outer slotted area will retain in the "Just unsaturated" stage, while the centre uncut core area will be saturated.

Hence the inductance of the **inner uncut core area** can be considered as the inductance of saturated core (air choke).

$$L_{uncut} = \frac{4\pi \times 10^{-7} \cdot N^2 \cdot A \cdot \mu_r}{\text{MPL}_{uncut}}$$
(3.2)

- Luncut Saturated Inductance of the inner uncut core area.
- A Cross sectional area of core
- μ_r Relative Permeability
- MPL Magnetic path length
- OD/ID Outer/Inner diameter of core
- N Number of turns

Where MPL is calculated by,

$$MPL = \frac{\pi x (OD - ID)}{\ln(\frac{OD}{ID})}$$

Considering the 1000VA transformer, with 10mm slot depth and total core size 180 x 75 x 35.

Uncut core dimension (OD x ID x H)	: 160 x 75 x 35 mm
Cut core dimension (OD x ID x H)	: 180 x 160 x 35 mm
Number of turns	: 371 turns

The parameters for the centre "uncut core";

Saturated (uncut) core area	$= 1399.24 \text{ mm}^2$
Relative permeability	= 1 (Air)
MPL	= 351.40 mm

Substituting the uncut core data into equation 3.2

$$L_{uncut} = 0.6887 mH$$

Then the inductance of the **outer slotted core area** can be calculated from the equation 3.3

$$L_{cut} = \frac{4\pi \times 10^{-7} N^2 A. \mu_r}{MPL + \mu l_a}$$
(3.3)

 L_{cut} - Slotted area un-saturated Inductance l_g - Air gap

The parameters for the slotted area.

Un-saturated Slotted core area	$= 1350 \text{ mm}^2$
Relative permeability	= 102.00
MPL	= 532.09 mm
Air gap	= 1.5 mm

Substituting the cut core data into equation 3.3

 $L_{cut} = 0.0089H$

Then the total Saturation inductance L_s is;

$$L_S = L_{uncut} + L_{cut}$$
$$L_S = 0.0096H$$

It shows that the inductance of the uncut saturated core area (L_{uncut}) is negligible on the resultant inductance L_s , and hence on the inrush current.

3.4 Calculation of inrush current

Recall the equation 3.1

$$I_{max} = \frac{V_m}{\sqrt{(\omega L_s)^2 + R^2}} \cdot \left(1 + \cos\theta + \frac{B_s - B_r}{B_n}\right)$$
(3.1)

Having this research is concentrate only on the Maximum inrush current, which occurs at the zero crossing. Then apply $\theta = 0$

$$I_{max} = \frac{V_m}{\sqrt{(\omega L_s)^2 + R^2}} \cdot \left(1 + 1 + \frac{B_s - B_r}{B_n}\right)$$

$$I_{max} = \frac{V_m}{\sqrt{(\omega L_s)^2 + R^2}} \cdot \left(2 + \frac{B_s - B_r}{B_n}\right)$$

Also applying $V_m = \sqrt{2} V_{rms}$

$$I_{max} = \frac{\sqrt{2}V_{rms}}{\sqrt{(\omega L_s)^2 + R^2}} \cdot \left(2 + \frac{B_s - B_r}{B_n}\right)$$
(3.4)

As per the experimental data, the value of $\frac{B_s - B_r}{B_n}$ stays almost constant, irrespective to the slot size. This is proven as following.

Consider the BH loops of two slotted core transformers of 1000VA, which are identical except having different slot depths of 10mm and 20mm.



Figure 3.17: BH loops at different air-gaps

According to Figure 3.16, the change of the remanence flux is almost negligible even for high variation of slot size. Hence the ratio between the saturated flux density (B_s) and remanence flux density (B_r) is considered fixed as following, in this research.

Means, $B_r = 0.75 B_s$

Therefore we can calculate,

$$B_s - B_r = 0.25 B_s$$
 (3.5)

Then the slotted core is subjected to deep saturation level and studied its BH loop characteristics. Refer Figure 3.17.



Figure 3.18: BH loop at deep saturation

Accordingly it is observed the design starts saturation when the nominal voltage gets nearly 2.5 times, means closer to 600V (230V nominal).

Therefore we can derive, $B_s: B_n = 2.5:1$ (3.6)

From equations (3.5) and (3.6), it is possible to derive,

$$\frac{B_s - B_r}{B_n} \approx 0.65 \tag{3.7}$$

Then substituting the equation 3.7, into equation 3.4.

$$I_{max} = \frac{\sqrt{2}V_{rms}}{\sqrt{(\omega L_s)^2 + R^2}} . (2 + 0.65)$$

$$I_{max} = \frac{3.75 \, V_{rms}}{\sqrt{(\omega L_s)^2 + R^2}} \tag{3.8}$$

Accordingly it is possible to calculate theoretical value for the I_{max} , together with the calculated saturation inductance L_s and calculated winding resistance (R = 0.635 Ω)

$$I_{max} = 243.12 \text{ A}$$

But the measured inrush current under the laboratory facility will be vary from this value. The reason for this deviation is the line inductance for the test bench supply system. The source impedance to the transformer makes a great effect to the above deviation, over the other factors. So in order to correct it we have to do line inductance correction for the calculated inrush value.

By adding 0.006H as the line inductance correction, we will have the theoretical value

$I_{max} = 151.67 A$

The actual value measured was 151A , which is almost equal for the theoretical value.

EXPERIMENTAL DATA COLLECTION

In chapter 3, slotted core method had been discussed together with 1000VA transformer. This chapter discusses on further details of experimental data collected for slotted core designs, which has slot sizes from 5mm to 25mm and flux densities from 0.9T to 1.6T. All together 40 samples were tested in order to identify the relationship between inrush current, flux density and slot depth.

4.1 Inrush Current Measurement on Samples

The transformers were tested applying alternating rated voltage 230V/50Hz across the primary winding. Then the inrush current transient waveforms are taken to an Oscilloscope (Tektronix DPO3000) connected via a current probe (Tektronix A621) to the circuit. Test set up for this arrangement is shown in Figure 4.1 [1].



Figure 4.1: Test setup for inrush current measurement

In this experiment, the inrush current data collected with two methods. First method is repeating the test several times (minimum 60 times per design), creating the possibility to switch the input wave form at zero crossing, and hence creating the maximum inrush current. The second method is switching the input via zero-point detecting circuit (made with SIEMENS 3RF2050-1AA02), which does monitor and

detect the zero crossing of the input wave form and ensure to switch ON the transformer at that point.

Both the options provided almost same maximum inrush current value, for each scenario to be discussed in section 4.2.

4.2 Finding the optimum slot depth or the minimum inrush current.

In this case, each design was tested for inrush current, varying slot depth and the flux density. Here optimum slot depth will be the minimum slot depth which can get at a higher flux density. Since core cutting time directly reflects to the manufacturing cost and because transformer size depends on the flux density, we can choose the optimum slot size with those two parameters. Here below are the test results for 40 samples.

Sample No	Slot	A (Slot)	Flux	Turns	Inrush@1.6T
8	25	1312.5	1.6	371	251
16	20	1050.0	1.6	371	202
24	15	787.5	1.6	371	164
32	10	525.0	1.6	371	151
40	5	262.5	1.6	371	210

Table 4.1: 1.6T sample test results

Table 4.2: 1.5T sample test results

Sample No	Slot	A (Slot)	Flux	Turns	Inrush@1.5T
7	25	1312.5	1.5	396	220
15	20	1050.0	1.5	396	150
23	15	787.5	1.5	396	110
31	10	525.0	1.5	396	124
39	5	262.5	1.5	396	206

Sample No	Slot	A (Slot)	Flux	Turns	Inrush@1.4T
6	25	1312.5	1.4	424	198
14	20	1050.0	1.4	424	127
22	15	787.5	1.4	424	104
30	10	525.0	1.4	424	114
38	5	262.5	1.4	424	204

 Table 4.3: 1.4T sample test results

 Table 4.4:
 1.3T sample test results

Sample No	Slot	A (Slot)	Flux	Turns	Inrush@1.3T
5	25	1312.5	1.3	457	187
13	20	1050.0	1.3	457	125
21	15	787.5	1.3	457	100
29	10	525.0	1.3	457	112
37	5	262.5	1.3	457	203

 Table 4.5:
 1.2T sample test results

Sample No	Slot	A (Slot)	Flux	Turns	Inrush@1.2T
4	25	1312.5	1.2	495	182
12	20	1050.0	1.2	495	121
20	15	787.5	1.2	495	97
28	10	525.0	1.2	495	108
36	5	262.5	1.2	495	200

 Table 4.6:
 1.1T sample test results

Sample No	Slot	A (Slot)	Flux	Turns	Inrush@1.1T
3	25	1312.5	1.1	540	168
11	20	1050.0	1.1	540	118
19	15	787.5	1.1	540	90
27	10	525.0	1.1	540	100
35	5	262.5	1.1	540	194

 Table 4.7: 1.0T sample test results

Sample No	Slot	A (Slot)	Flux	Turns	Inrush@1.0T
2	25	1312.5	1	594	165
10	20	1050.0	1	594	114
18	15	787.5	1	594	83
26	10	525.0	1	594	93
34	5	262.5	1	594	186

Sample No	Slot	A (Slot)	Flux	Turns	Inrush@0.9T
1	25	1312.5	0.9	660	149
9	20	1050.0	0.9	660	110
17	15	787.5	0.9	660	70
25	10	525.0	0.9	660	89
33	5	262.5	0.9	660	175

 Table 4.8: 0.9T sample test results

All together we can analyse test results as in Figure 3.16



Figure 3.16: Sample test results

ANALYSIS OF DATA

In this chapter, it is mainly focused to build up a methodology to calculate inrush current towards developing a design tool. As discussed in chapter 3, basically the equation 3.1 can be used for inrush calculation. But together with the experimental data collected in chapter 4, there are certain characteristics can be built and embedded in to the calculation towards handling the design parameters.

In this chapter the following aspects will be discussed together with the data obtained in chapter 4 and the inrush current calculation method discussed in the chapter 3.

- 1) Theoretical inrush calculation
- 2) Optimum slot depth calculation for a specified inrush current.
- 3) Design tool development.

5.1 Theoretical inrush calculation.

As discussed in chapter 3, below equation (5.1) can be uses for theoretical inrush calculation. As input parameters we have operating voltage (Vrms), winding resistance (R) and saturation inductance(L_s) calculated according to the slot depth and core size as discussed in chapter 3.

$$I_{max} = \frac{3.75 \, V_{rms}}{\sqrt{(\omega L_s)^2 + R^2}} \tag{5.1}$$

5.2 Optimum slot depth calculation for specified inrush current

Using experimental data collected as explained in chapter 4, we can derive these equations for different flux densities.

X= Slot depth Y= Inrush current

Flux(T)	Equation for inrush with gap
1.6	y = 0.0022x4 - 0.1727x3 + 5.245x2 - 64.383x + 421
1.5	y = -0.0007x4 + 0.0147x3 + 1.3367x2 - 37.767x + 360
1.4	y = 0.0041x4 - 0.2693x3 + 7.0967x2 - 85.067x + 483
1.3	y = 0.0028x4 - 0.196x3 + 5.71x2 - 74.8x + 457
1.2	y = 0.0032x4 - 0.2213x3 + 6.26x2 - 79.567x + 467
1.1	y = 0.002x4 - 0.1613x3 + 5.27x2 - 73.367x + 448
1	y = 0.0014x4 - 0.126x3 + 4.565x2 - 67.65x + 425
0.9	y = -0.0035x4 + 0.1627x3 - 1.3733x2 - 18.567x + 284

Here we can observe the trend will be differ with the design flux. And depending on the design flux we can choose the relevant trend for the relationship of the slot depth and inrush current. Here there will be two slot depths for a specific inrush value. When considering manufacturing costs, the lowest slot depth will be the optimum value for the specific flux density.

5.3 Development of Design Tool for Slotted Core

This section will discuss on development of a design tool for slotted core, integrating the equations and characteristics derived in the previous sections.

As discussed in the chapter 3, all the designs considered in this research are done for 1kva transformers which has different design flux densities and different slot depths. Hence in this design tool the designer will only need to input the core dimensions, primary voltage and number of turns. Then the tool will calculate design flux by itself. Also we have to enter primary resistance which we can get from the toroid design tool. The Figure 5.1 shows the simplified flow chart for the calculation tool.



Then the flow chart can be presented as a design tool, which can be programmed with different software programming languages. The following Figure 5.5 is showing the program which is done on the Flow chart, with Microsoft Excel.



Figure 5.2: Design tool

As shown in the Figure 5.2, the designer will only have to input only the dimensions of the cores and the resistance of the input winding, and the design tool will calculate the optimum slot depth according to the customer specified inrush current which designer has to meet at the end.

5.4 Design tool validation

In this section, it shows the performance of the developed tool comparing together with the measured inrush current values. Three samples were done for design tool validation and these samples were manufactured according to the design tool outcome. Sample 1:- Customer specified inrush is 120A pk-pk, for 1KVA t/x.

- New core dimensions will be OD 170mm ,ID 75mm , H 40mm
- According to the tool, optimum slot depth = 10.4mm
- According to the tool, theoretical maximum inrush current will be 136A pk-pk.

Measured inrush value= 116A pk-pk

Sample 2:- Customer specified inrush is 150A pk-pk, for 1KVA t/x.

- New core dimensions will be OD 180mm ,ID 85mm , H 40mm
- According to the tool, optimum slot depth = 7.9mm
- According to the tool, theoretical maximum inrush current will be 165A pk-pk
- Measured inrush value= 136A pk-pk

Sample 3:- Customer specified inrush is 180A pk-pk , for 1KVA t/x.

- New core dimensions will be OD 190mm ,ID 95mm , H 40mm
- According to the tool, optimum slot depth = 6.2mm
- According to the tool, theoretical maximum inrush current will be 191A pk-pk
- Measured inrush value= 173A pk-pk

It is observed that there is only a small deviation between the calculated and measured inrush current values. Means it is evident that the characteristic equations built up for inrush calculation is with high accuracy towards calculating inrush current. Also it is noted that the deviations are always positive, means the calculated inrush current values are always higher, than the measured inrush current values.

One of the main reasons for that will be the source impedance, which will never be zero in real applications. Note, the above measured values are taken from a source with very low impedance, hence the deviations are minimal. Therefore considering calculated values as the maximum inrush current is obviously safe, considering all the applications.

Meantime the inrush current values shows that the slotted core method does have a good control over the inrush current value, rather than the conventional transformer based inrush current mitigation methods. Hence it can be concluded that, together with the slotted core method, it is possible to calculate the inrush current values within 10%, including the manufacturing tolerances.

5.5 Comparison of electrical performance with conventional inrush mitigation method.

Normal core(170*75*40, M5)	Cut core(170*75*40, M5)	Slotted core(170*75*40, M5 , 10.4mm slot)
Inrush measured = 210 A pk- pk	Inrush measured = 112 A pk- pk	Inrush measured = 116 A pk- pk
Core loss = $17W$	Core loss = $25W$	Core loss = 23W (improved by 2W)
Voltage regulation = 6%	Voltage regulation = 10%	Voltage regulation = 8% (Regulation improved)
No load current = 68 mA	No load current = 84 mA	No load current = 78mA (improved by 6mA)

 Table 5.1: Electrical parameters comparison

When comparing with other transformer based inrush mitigation methods, here it is observed there are lot of improvements in performance while having a good control in inrush current.

5.6 Comparison of manufacturing cost with conventional cut core method.

- Huge saving on core manufacturing cost over cut core method.
 - 3 days of glue curing time is eliminated
 - Core cutting time 2.5mm/min improved to 3.2mm/min (hence reduces machine time and labour time)
 - Gluing process has eliminated (Save labour and material)
 - No need steel bands to keep tight the core

Table 5.2:	Conventional	cut core	costing

Conventional Cut-core				
		Unit		
Description	Unit	price(USD)	Qty	Cost(USD)
Copper	kg	8.3	1.05	8.72
M5 steel	kg	2.40	3.12	7.49
Glue LOCKTITE	g	0.07	15.00	1.11
Steel band	pcs	1.10	2.00	2.20
Material Cost				19.51
Machine cost (35mm cut @ 2.5mm/min)	min	2.30	14.00	32.20
Total cost				51.71

Slotted core				
		Unit		
Description	Unit	price(USD)	Qty	Cost(USD)
Copper	kg	8.3	1.05	8.72
M5 steel	kg	2.40	3.12	7.49
Glue LOCKTITE	g	0.07	0.00	0.00
Steel band	pcs	1.10	0.00	0.00
Material Cost				16.20
Machine cost (10mm cut @ 3.2mm/min)	min	2.30	3.13	7.19
Total cost				23.39

Table 5.3: Slotted core costing

With the proposed slotted core method, core fixing glue and steel bands can be eliminated. Also with the machine modification and the small cut, the machine time is also reduced hence the machine cost reduces. Therefore when considering with the conventional cut core method, there is a huge saving on manufacturing cost in the new slotted core method.

CONCLUSION AND SUGGESTIONS FOR FUTURE RESEARCH

6.1 Conclusion

In this research study, it emphasized that the transformer based inrush mitigation methods are more reliable over the external equipment based (i.e. connecting NTC thermistor, start-up resistive load) inrush mitigation methods. Further, this research established the use of slotted cores as the best option over the existing transformer based solutions to mitigate inrush current and several other drawbacks of the conventional solutions.

The proposed method has the advantages of higher performance; lower inrush current, lower no-load current, mechanically stable reinforced structure, easier manufacturing and hence reduced material wastage. So the proposed method saves costs and also the resources. The slotted core is highly reliable on inrush mitigation for the 1kVA transformers, the reduction in inrush current was 40-50% compared to the corresponding conventional transformers.

The main disadvantage of slotted core was the increment of the active core loss, but obviously that has to be sacrificed in order to limit inrush current. But it is still lesser than the loss of the conventional cut core method.

6.2 Suggestions for Future Research

Followings are the future research suggestions, on the slotted core method discussed.

- This research is confined to particular steel type which is M5 steel grade, and transformer power range. So the same calculation methodology can be used to expand the ranges of above parameters while introducing new steel types.
- 2) This research has done experiments only for 230V main input. But it will be useful building the same concept for the other common input voltages of other countries / applications (110V, 120V, 200V, 400V etc.), expanding the design calculation.
- Also there will be more easier and economical manufacturing methods slotted cores; like introducing laser cutting, etc.

Also it will be worth experimenting for other constructional methods, which could be economical and might be high performing.

REFERENCE LIST

- [1] H.K. Ekanayake, "Methodology to limit inrush current of toroidal transformers", Charted Engineering IESL Sri Lanka, 2012
- [2] Rasim Dogan, Saeed Jazebi, "Investigation of Transformer-Based Solutions for the Reduction of Inrush and Phase-Hop Currents", IEEE Transactions on Power Electronics, Vol. 31, Iss 5, pp 3506 – 3516, July 2015
- [3] Francisco de L, Brian G, "Transformer Based Solutions to Power Quality Problems", Plitron Manufacturing Inc. Canada, 2001
- [4] KAWASAKI STEEL CORPORATION, "PLASMA CORE RGHPJ RGH AND RG CORE", Grain-Oriented Magnetic Steel Strip, Japan: 1991.
- [5] Colonel MW, T. Mclyman, "Transformer and inductor design handbook", Third Edition, Kg Magnetics, Inc. California, USA, 2004, pp. 96-100
- [6] Saeed Jazebi, Nicholas Wu, "Enhanced analytical method for the calculation of the maximum inrush current of single phase power transformer", IEEE Transactions on Power Delivery, Vol. 30, Iss 6, pp 2590 - 2599, June 2015
- [7] Yunfei Wang, Sami G., "Analytical formula to estimate the maximum inrush current", IEEE Translations on Power Delivery, Vol. 23, No 2, pp 1266 1268, April 2008

APPENDICES

Appendix A – Design simulations with ToroidEZE programme with 1.6T to 0.9T flux density

AA-182040 1.6T Design

Pe Weight : 5.54 kg Coil Weight : 2.52 kg Tot. Weight : 8.192 kg	
Teduction : 1.6.7 Lond Long : 27.25.9 Tet Deven : 1000.93	
Induction : 1.0 1 DOG DOAD : 37.55 H TOC. FOWER : 1000 VA	
Prequency : 50 Hz Sec Loss : 20.84 W Temp. Rise : 52/62 deg.C	
Excitation: 43.4 mA Pri Logs : 16.52 W Optimized : 1:0.88 Wdg+	
Core/Coil : 2.2:1 kg Window Pill : 89.8 % Wire Pill : 32.2 %	
· · ·	
Windings. Primary Sec 1	
0 0	
Rated Volts ms. 230v 230v	
Rated Amps rms. 4.54A 4.35A	
Duty Cycle % 100%	
VA rms 1000	
Conductor. Cu Cu	
Turns. 371ts 384ts	
Wire Gauge. 1.600mm 1.500mm	
Pilarø	
Ohms @ 20°C. 0.592 0.912	
Winding grams. 1222 1294	
Pull-Load Volts 230.1v	
No-Load Volts 238.1v	
Regulation %. 1.5% 3.3%	
Watts Loss Hot. 16.52 20.84	
Ingulation Tape	
Width. mm. 13 -	
Thickness. mm. 0.1 -	
Layers. 4 -	
Screening Tape	
Width. mm	
Thickness. mm	
Layers	
A/mm^2. 2.26 2.46	
Bare Wire Fill % 16.89% 15.36%	

ToroidEZE-AL v.2.6.4

AA-182040 1.5T Design

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Design File : ED 1kva - 1.5.tfx

Core : 180 x 75 x 3 Pe Weight : 5.54 kg Induction : 1.499 3 Prequency : 50 Hz Excitation: 33.4 mJ Core/Coil : 2.1:1)	35 mm 30-M5 9 7 4 ¢g	5	Iron Loss Coil Weight Load Loss Sec Loss Pri Loss Window Fill	: 4.86 W : 2.69 kg : 40.15 W : 22.44 W : 17.71 W : 93.1 %	Finished Din's : 194 x 44 x 5 Tot. Weight : 8.366 kg Tot. Power : 1000 VA Temp. Rise : 53/63 deg.C Optimized : 1:0.82 Wdg+ Wire Fill : 34.5 %	7 ы
Windingø.	Primary 0	Sec 1				
Rated Volts rms.	230v	230v				
Rated Amps rms.	4.54A	4.35A				
Duty Cycle %.	-	100%				
	-	-				
VA ims.	-	1000				
Conductor.	Cu	Cu				
Turne.	396tø	411tø				
Wire Gauge.	1.600mm	1.500mm	1			
Filarø.	-	-				
	-	-				
Ohms @ 20°C.	0.63	0.971				
	-	-				
Winding grams.	1301	1388				
	-	-				
Pull-Load Volts.		230.27				
No-Load Volts.	-	238.7v				
Regulation %.	1.6%	3.6%				
	-	-				
Watts Loss Hot.	17.71	22.44				
	-	-				
Insulation Tape.	-	-				
width. mm.	13	-				
Thickness. mm.	0.1	-				
Layers.	4	-				
0	-	-				
Screening Tape.	-	-				
Width. mm.	-	-				
Thickness. mm.	-	-				
Layers.	-	-				
N/ 00	-	-				
A/mm 2. Down Wiwe Rill A	2.26	2.46				
Bare Wire Fill %	18.02%	16.44%				

ToroidEZE-AL v.2.6.4

AA-182040 1.4T Design

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Design File : ED 1kva - 1.4.tfx

Core : 180 x 75 x 3 Pe Weight : 5.54 kg Induction : 1.4 7 Prequency : 50 Hz Excitation: 27.3 mJ Core/Coil : 1.9:1)	35 mm 30-M5 9 19	5	Iron Loss Coil Weight Load Loss Sec Loss Pri Loss Window Fill	: 4.18 W : 2.93 kg : 44.22 W : 24.67 W : 19.55 W : 96.7 %	Pinished Din's Tot. Weight Tot. Power Temp. Rise Optimised Wire Pill	: 194 x 42 x 58 mm : 8.607 kg : 1000 VA : 55/66 dæg.C : 1:0.76 Wdg+ : 37 %
Windingø.	Primary	Sec 1				
Rated Volts yms.	0 230v	0 230v				
Rated Amps rms.	4.56A	4.354				
Duty Cycle %.	-	100%				
	-	-				
VA rms.	-	1000				
Conductor.	Cu	Cu				
Tume.	424tø	442tø				
Wire Gauge.	1.600mm	1.500mm	1			
Filarø.	-	-				
	-	-				
Ohms @ 20°C.	0.686	0.95				
	-	-				
Winding grams.	1415	1514				
	-					
Full-Load Volts.		230.40				
No-Load volts.	1 0%	239.80				
Regulation 5.	1.8%	5.9%				
Watte Loss Hot.	19.55	24.67				
	-	-				
Ingulation Tape.		-				
Width. mm.	13	-				
Thickness. mm.	0.1	-				
Layers.	4	-				
-	-	-				
Screening Tape.	-	-				
Width. mm.	-	-				
Thickness. mm.	-	-				
Layers.	-	-				
	-	-				
A/mm^2.	2.27	2.46				
Bare Wire Fill %	19.3%	17.68%				

ToroidEZE-AL v.2.6.4

AA-182040 1.3T Design Design File : ED lkva - 1.3.tfx

Core : 180 x 75 x 1 Pe Weight : 5.54 kg	35 mm 30-M5 9		Iron Lo <i>ss</i> Coil Weight	:	3.59 W 3.19 kg	Finished Din's Tot. Weight	:	195 x 40 x 59 mm 8.874 kg
Induction : 1.299	7		Load Loss		48.86 W	Tot. Power		1000 VA
Prequency : 50 Hz			Sec Loss		27.49 W	Temp. Rise		58/70 deg.C
Excitation: 23 mA			Pri Loss		21.37 W	Optimized		1:0.69 Wdg+
Core/Coil : 1.7:1)	kg		Window Fill	•	100.8 %	Wire Fill	•	39.9 %
Windingø.	Primary 0	Sec 1 0						
Rated Volts mms.	230v	230v						
Rated Amps rms.	4.58A	4.35A						
Duty Cycle %.	-	100%						
	-	-						
VA rms.	-	1000						
Conductor.	Cu	Cu						
Turnø.	457tø	478tø						
Wire Gauge.	1.600mm	1.500mm	1					
Filare.	-	-						
0	-	-						
Onms @ 20°C.	0.738	1.05						
Winding mana	1500	1672						
winding grams.	1922	10/1						
Pull-Load Volte.		230.27						
No-Load Volts.		240.6v						
Regulation %.	1.9%	4.3%						
	-	-						
Watts Loss Hot.	21.37	27.49						
	-	-						
Ingulation Tape.	-	-						
Width. mm.	13	-						
Thickness. mm.	0.1	-						
Layers.	4	-						
	-	-						
Screening Tape.	-	-						
Width. mm.	-	-						
Thickness. mm.	-	-						
Layers.	-	-						
	-							
A/mm ² .	2.29	2.46						
Bare Wire Fill %	20.8%	19.12%						

ToroidEZE-AL v.2.6.4

AA-182040 1.2T Design Design File : ED lkva - 1.2.tfx

Core : 180 x 75 x 35 mm 30-M5	Iron Loss	: 3.09 W	Finished Din's	: 196 x 38 x 61 mm
Pe Weight : 5.54 kg	Coil Weight	: 3.47 kg	Tot. Weight	: 9.15 kg
Induction : 1.199 7	Load Loss	: 53.64 W	Tot. Power	: 1000 VA
Frequency : 50 Hz	Sec Loss	: 30.02 W	Temp. Rise	: 61/73 deg.C
Excitation: 19.6 mA	Pri Loss	: 23.62 W	Optimized	: 1:0.64 Wdg+
Core/Coil : 1.6:1 kg	Window Fill	: 105.4 %	Wire Fill	: 43.3 %

Windingø.	Primary	Sec 1
	0	0
Rated Volts rms.	230v	230v
Rated Amps rms.	4.59A	4.35A
Duty Cycle %.	-	100%
	-	-
VA ims.	-	1000
Conductor.	Cu	Cu
Turne.	495tø	520tø
Wire Gauge.	1.600mm	1.500mm
Filare.		-
	-	-
Ohms @ 20°C.	0.802	1.14
	-	-
Winding grams.	1656	1812
	-	-
Pull-Load Volts.		230.3v
No-Load Volts.	-	241.6v
Regulation %.	2.1%	4.7%
	-	-
Watts Loss Hot.	23.62	30.02
	-	-
Ingulation Tape.	-	-
Width. mm.	13	-
Thickness. mm.	0.1	-
Layers.	4	-
	-	-
Screening Tape.	-	-
Width. mm.	-	-
Thickness. mm.	-	-
Layers.	-	-
	-	-
A/mm^2.	2.29	2.46
Bare Wire Fill %	22.53%	20.8%

ToroidEZE-AL v.2.6.4

AA-182040 1.1T Design

Design File : ED 1kva - 1.1.tfx

Core : 180 x 75 x 1	35 mm 30-M5		Iron Loss		2.64 W		Finished Din's		197 x 35 x 63 mm
Pe Weight : 5.54 b	9		Coil Weight		3.88 km		Tot. Weight		9.561 kg
Induction : 1.099	7		Load Loas		61.31 W		Tot. Power		1000 VA
Frequency : 50 Hz	•		Sec Loss		34.47 W		Temp. Rise		66/79 deg.C
Excitation: 16.6 m	۵		Pri Loga		26.84 W		Optimized		1:0.57 Wda+
Core/Coil : 1.4:1)	- ka		Window Fill		110.7 %	1	Wire Fill	÷	47.4 %
	5			_				_	
Windings.	Primary	Sec 1							
	0	0							
Rated Volts mms.	230v	230v							
Rated Amps rms.	4.63A	4.35A							
Duty Cycle %.	-	100%							
	-	-							
VA rms.	-	1000							
Conductor.	Cu	Cu							
Turnø.	540tø	571tø							
Wire Gauge.	1.600mm	1.500mm	1						
Filarø.	-	-							
	-	-							
Ohms @ 20°C.	0.887	1.29							
	-	-							
Winding grams.	1828	2049							
	-	-							
Full-Load Volts.	-	230.3v							
No-Load Volts.	-	243.2v							
Regulation %.	2.4%	5.3%							
	-	-							
Watts Loss Hot.	26.84	34.47							
	-	-							
Ingulation Tape.	•	-							
Width. mm.	13	-							
Thickness. mm.	0.1	-							
Layers.	4	-							
	-	-							
Screening Tape.	-	-							
Width. mm.	-	-							
Thickness. mm.		-							
Layers.	-	-							
	-	-							
A/mm^2.	2.3	2.46							
Bare Wire Fill %	24.59%	22.84%							

ToroidEZE-AL v.2.6.4

AA-182040 1.0T Design

Design File : ED 1kva - 1.0.tfx

Core : 180 x 75 x 3 Re Weight : 5.54 kg	5 mm 30-M5	5	Iron Loss Coil Weight	:	2.24 W 3.41 kg	Finished Din's	и : 195 ж 38 ж 61 mm - 9.09 ber
Induction : 0.999 1			Load Loag		74.69 W	Tot. Romer	1000 VA
Prequency : 50 Hz			Sec Loss		40.47 W	Temp. Rise	: 79/94 deg.C
Excitation: 13.9 m2			Pri Loga		34.21 W	Ontimized	1:0.65 Wdg+
Core/Coil : 1.6:1 k	ar		Window Fill		104.7 %	Wire Fill	: 41.4 %
	.,			_			
Windings.	Primary	Sec 1					
	0	0					
Rated Volts rms.	230v	230v					
Rated Amps rms.	4.52A	4.35A					
Duty Cycle %.	-	100%					
	-	-					
VA 1ms.	-	1000					
Conductor.	Cu	Cu					
Turne.	594tø	636tø					
Wire Gauge.	1.400mm	1.400mm					
Filars.	-	-					
	-	-					
Ohms @ 20°C.	1.24	1.58					
	-	-					
Winding grams.	1499	1911					
	-	-					
Pull-Load Volts.	-	230.3v					
No-Load Volts.	-	246.3v					
Regulation %.	3.1%	6.5%					
	-	-					
Watts Loss Hot.	34.21	40.47					
	-	-					
Insulation Tape.	-	-					
Width. mm.	13	-					
Thickness. mm.	0.1	-					
Layers.	4	-					
	-	-					
Screening Tape.	-	-					
Width. mm.	-	-					
Thickness. mm.	-	-					
Layerø.	-	-					
	-	-					
A/mm^2.	2.94	2.83					
Bare Wire Fill %	20.7%	20.7%					

ToroidEZE-AL v.2.6.4

AA-182040 0.9T Design

Design File : ED 1kva - 0.9.tfx

Core : 180 x 75 x 3	5 mm 30-M5	5	Iron Loss	: 1.84 W	Finished Din's : 197 x 35 x 63 : Tat Mainht : 0 602 hr	nm
Fe Weight : 5.54 kg Induction : 0.899 7 Frequency : 50 Hz Excitation: 11.5 mA Core/Coil : 1.4.1 kg			Load Loss : 104.9 W Sec Loss : 54.42 W Pri Loss : 50.48 W Windew Pill : 111.6 %	: 3.92 kg : 104.9 W : 54.42 W : 50.49 W	Tot Perer : 1000 Wh	
					Temp Bise : 100/120 der C	: 100/120 deg.C
					Ontimized 110,57 Wdge	
				111.6 %	Wire Fill 49.2 %	
	-9					
Windings.	Primary	Sec 1				
	0	0				
Rated Volts rms.	230v	230v				
Rated Amps rms.	4.91A	4.35A				
Duty Cycle %.	-	100%				
	-	-				
VA rms.	-	1000				
Conductor.	Cu	Cu				
Turnø.	660tø	724tø				
Wire Gauge.	1.400mm	1.400mm				
Filarø.	-	-				
	-	-				
Ohms @ 20°C.	1.4	1.85				
	-	-				
Winding grams.	1692	2228				
	-	-				
Pull-Load Volts.		230.6v				
No-Load Volts.	-	252.3v				
Regulation %.	4.3%	8.6%				
	-	-				
Watts Loss Hot.	50.48	54.42				
	-	-				
Ingulation Tape.	-	-				
Width. mm.	13	-				
Thickness. mm.	0.1	-				
Layerø.	4	-				
	-	-				
Screening Tape.	-	-				
Width. mm.	-	-				
Thickness. mm.	-	-				
Layerø.	-	-				
	-	-				
A/mm^2.	3.13	2.83				
Bare Wire Fill %	23%	25.23%				

ToroidEZE-AL v.2.6.4

Appendix B – Test equipment details

Mixed Signal Oscilloscope (DPO3000)



- TekVPI[®] Probe Interface Supports Active, Differential, and Current Probes for Automatic Scaling and Units
- 9 in. (229 mm) WVGA Widescreen Color Display
- Small Footprint and Lightweight Only 5.8 in. (147 mm) deep and 9 lb. (4 kg)

fine timing resolution, the MSO/DPO3000 offers a deep record length of 5 Mpoints standard on all channels

With Wave Inspector® controls for rapid waveform navigation, automated serial and parallel bus analysis, and automated power analysis - the MSO/DPO3000 Oscilloscope Series from Tektronix provides the feature-rich tools you need to simplify and speed debug of your complex design.

Tektronix[®]

Data Sheet



 $\label{eq:Discover} Discover - Fast waveform capture rate - over 50 000 wfm/s - maximizes the probability of capturing elusive glitches and other infrequent events.$

Comprehensive Features Speed Every Stage of Debug

The MSO/DPO3000 Series offers a robust set of features to speed every stage of debugging your design – from quickly discovering an anomaly and capturing it, to searching your waveform record for the event and analyzing its characteristics and your device's behavior.

Discover

To debug a design problem, first you must know it exists. Every design engineer spends time looking for problems in their design, a time-consuming and frustrating task without the right debug tools.

The MSO/DPO3000 Series offers the industry's most complete visualization of signals, providing fast insight into the real operation of your device. A fast waveform capture rate – greater than 50,000 waveforms per second – enables you to see glitches and other infrequent transients within seconds, revealing the true nature of device faults. A digital phosphor display with intensity grading shows the history of a signal's activity by intensifying areas of the signal that occur more frequently, providing a visual display of just how often anomalies occur.



Capture – Triggering on a specific transmit data packet going across an RS-232 bus. A complete set of triggers, including triggers for specific serial packet content, ensures you quickly capture your event of interest.

Capture

Discovering a device fault is only the first step. Next, you must capture the event of interest to identify root cause.

The MSO/DPO3000 Series provides a complete set of triggers – including runt, logic, pulse width/glitch, setup/hold violation, serial packet, and parallel data – to help quickly find your event. With up to a 5 Mpoint record length, you can capture many events of interest, even thousands of serial packets, in a single acquisition for further analysis while maintaining high resolution to zoom in on fine signal details.

From triggering on specific packet content to automatic decode in multiple data formats, the MSO/DPO3000 Series provides integrated support for the industry's broadest range of serial buses – I²C, SPI, CAN, LIN, RS-232/422/485/UART, and I²S/LJ/RJ/TDM. The ability to decode up to two serial and/or parallel buses simultaneously means you gain insight into system-level problems quickly.

To further help troubleshoot system-level interactions in complex embedded systems, the MSO3000 Series offers 16 digital channels in addition to its analog channels. Since the digital channels are fully integrated into the oscilloscope, you can trigger across all input channels, automatically time-correlating all analog, digital, and serial signals. The MagniVu[™] high-speed acquisition enables you to acquire fine signal detail (up to 121.2 ps resolution) around the trigger point for precision measurements. MagniVu[™] setup and hold measurements, clock delay, signal skew, and glitch characterization.



Search – I²C decode showing results from a Wave Inspector search for Address value 50. Wave Inspector controls provide unprecedented efficiency in viewing and navigating waveform data.

Search

Finding your event of interest in a long waveform record can be time consuming without the right search tools. With today's record lengths pushing beyond a million data points, locating your event can mean scrolling through thousands of screens of signal activity.

The MSO/DPO3000 Series offers the industry's most comprehensive search and waveform navigation with its innovative Wave Inspector® controls. These controls speed panning and zooming through your record. With a unique force-feedback system, you can move from one end of your record to the other in just seconds. User marks allow you to mark any location that you may want to reference later for further investigation. Or, automatically search your record for criteria you define. Wave Inspector will instantly search your entire record, including analog, digital, and serial bus data. Along the way it will automatically mark every occurrence of your defined event so you can quickly move between events.

Mixed Signal Oscilloscopes - MSO3000 Series, DPO3000 Series



Analyze – FFT analysis of a pulsed signal. A comprehensive set of integrated analysis tools speeds verification of your design's performance.

Analyze

Verifying that your prototype's performance matches simulations and meets the project's design goals requires analyzing its behavior. Tasks can range from simple checks of rise times and pulse widths to sophisticated power loss analysis and investigation of noise sources.

The MSO/DPO3000 Series offers a comprehensive set of integrated analysis tools including waveform- and screen-based cursors, 29 automated measurements, advanced waveform math including arbitrary equation editing, FFT analysis, and trend plots for visually determining how a measurement is changing over time. Specialized application support for serial bus analysis, power supply design, and video design and development is also available.

For extended analysis, National Instrument's LabVIEW SignalExpress ™ Tektronix Edition provides over 200 built-in functions including time and frequency domain analysis, limit testing, data logging, and customizable reports.

Data Sheet



Wave Inspector controls provide unprecedented efficiency in viewing, navigating, and analyzing waveform data. Zip through your 5 Mpoint record by turning the outer pan control (1). Get from the beginning to end in seconds. See something of interest and want to see more details? Just turn the inner zoom control (2).

Wave Inspector® Navigation and Search

A 5 Mpoint record length represents thousands of screens of information. The MSO/DPO3000 Series enables you to find your event in seconds with Wave Inspector, the industry's best tool for navigation and search. Wave Inspector offers the following innovative controls:

Zoom/Pan

A dedicated, two-tier front-panel control provides intuitive control of both zooming and panning. The inner control adjusts the zoom factor (or zoom scale); turning it clockwise activates zoom and goes to progressively higher zoom factors, while turning it counterclockwise results in lower zoom factors and eventually turning zoom off. No longer do you need to navigate through multiple menus to adjust your zoom view. The outer control pans the zoom box across the waveform to quickly get to the portion of waveform you are interested in. The outer control also utilizes force-feedback to determine how fast to pan on the waveform. The farther you turn the outer control, the faster the zoom box moves. Pan direction is changed by simply turning the control the other way.

Play/Pause

A dedicated **Play/Pause** front-panel button scrolls the waveform across the display automatically while you look for anomalies or an event of interest. Playback speed and direction are controlled using the intuitive pan control. Once again, turning the control further makes the waveform scroll faster and changing direction is as simple as turning the control the other way.



Search step 2: Wave Inspector automatically searches through the record and marks each event with a hollow white triangle. You can then use the **Previous** and **Next** buttons to jump from one event to the next.

User Marks

Press the **Set Mark** front-panel button to place one or more marks on the waveform. Navigating between marks is as simple as pressing the **Previous** (\leftarrow) and **Next** (\rightarrow) buttons on the front panel.

Search Marks

The Search button allows you to automatically search through your long acquisition looking for user-defined events. All occurrences of the event are highlighted with search marks and are easily navigated to, using the front-panel Previous (\leftarrow) and Next (\rightarrow) buttons. Search types include edge, pulse width/glitch, runt, logic, setup and hold, rise/fall time parallel bus, and I²C, SPI, CAN, LIN, RS-232/422/485/UART, and I²S/LJ/RJ/TDM packet content.

Mixed Signal Oscilloscopes - MSO3000 Series, DPO3000 Series



Digital phosphor technology enables greater than 50,000 wfm/s waveform capture rate and real-time intensity grading on the MSO/DPO3000 Series.

Digital Phosphor Technology

The MSO/DPO3000 Series' digital phosphor technology provides you with fast insight into the real operation of your device. Its fast waveform capture rate – greater than 50,000 wfm/s – gives you a high probability of quickly seeing the infrequent problems common in digital systems: runt pulses, glitches, timing issues, and more.

Waveforms are superimposed with one another and waveform points that occur more frequently are intensified. This quickly highlights the events that over time occur more often or, in the case of infrequent anomalies, occur less often.

With the MSO/DPO3000 Series, you can choose infinite persistence or variable persistence, determining how long the previous waveform acquisitions stay on-screen. This allows you to determine how often an anomaly is occurring.

Mixed Signal Design and Analysis (MSO Series)

The MSO3000 Series Mixed Signal Oscilloscopes provide 16 digital channels. These channels are tightly integrated into the oscilloscope's user interface, simplifying operation and making it possible to solve mixed-signal issues easily.



The MSO Series provides 16 integrated digital channels enabling you to view and analyze time-correlated analog and digital signals.



With the color-coded digital waveform display, groups are created by simply placing digital channels together on the screen, allowing the digital channels to be moved as a group. You can set threshold values for each pod of eight channels, enabling support for up to two different logic families.

Color-coded Digital Waveform Display

The MSO3000 Series has redefined the way you view digital waveforms. One common problem shared by both logic analyzers and mixed-signal oscilloscopes is determining if data is a one or a zero when zoomed in far enough that the digital trace stays flat all the way across the display. The MSO3000 Series has color-coded digital traces, displaying ones in green and zeros in blue.
Mixed Signal	Oscilloscopes	- MSO3000	Series.	DPO3000	Series

Vertical System An						
Tertical Oystelli All	alog Channels					
Characteristic	MSO3012 DPO3012	MSO3014 DPO3014	MSO3032 DPO3032	MSO3034 DPO3034	DPO3052	MSO3054 DPO3054
Input Channels	2	4	2	4	2	4
Analog Bandwidth (-3 dB)	100 MHz	100 MHz	300 MHz	300 MHz	500 MHz	500 MHz
Calculated Rise Time 5 mV/div (typical)	3.5 ns	3.5 ns	1.17 ns	1.17 ns	700 ps	700 ps
Hardware Bandwidth	20 M	Hz		20	MHz, 150 MHz	
Input Coupling			AC, D	C, GND		
Input Impedance			1 MΩ ±1%, 75 G	$\Omega \pm 1\%$, 50 $\Omega \pm 1\%$		
Input Sensitivity Range, 1 MΩ			1 mV/div	to 10 V/div		
Input Sensitivity Range,			1 mV/div	to 1 V/div		
Vertical Resolution			8 bits (11 bit	s with Hi Res)		
Maximum Input Voltage 1 MQ			300 V _{RMS} with	peaks $\leq \pm 450$ V		
Maximum Input			5 V_{RMS} with p	beaks $\leq \pm 20$ V		
DC Gain Accuracy			+1.5% for 5 m	V/div and above		
20 Gain Addinady			±2.0% fc	or 2 mV/div		
Channel-to-Channel		≥100 [.] 1 at	≤100 MHz and ≥30.1	at >100 MHz un t	o the rated BW	
Isolation		_10011 0				
(Any Two Channels at						
Equal Vertical Scale)						
Offset Range			Horiz	ontal System /	Analog Channels	
Range	1 MΩ	50 Ω, 75 Ω	Chara	cteristic	All MSO3000 Models	
1 mV/div to 99.5 mV/div	±1 V	±1 V	Maria			
100 mV/div to 995 mV/div	±10 V	+5.1/	Maximu	um Sample Rate	2.5 GS/s	
1 V/div	. 400 \/	±5 V	(all cha	um Sample Rate nnels)	2.5 GS/s	
1.01 Vidiu to 10 Vidiu	±100 V	±5 V	(all cha	um Sample Rate nnels) um Record Length	2.5 GS/s 5 Mpoints	
1.01 V/div to 10 V/div	±100 V ±100 V	±5 V NA	(all cha Maximu Maximu (all cha Maximu	um Sample Rate nnels) um Record Length nnels) um Duration of	2.5 GS/s 5 Mpoints	
1.01 V/div to 10 V/div Vertical System Dio	±100 V ±100 V	±5 V ±5 V NA	Maximi (all cha Maximi (all cha (all cha Maximi Time C	um Sample Rate nnels) um Record Length nnels) um Duration of aptured at Highest	2.5 GS/s 5 Mpoints 2 ms	
1.01 V/div to 10 V/div Vertical System Dig Characteristic	±100 V ±100 V	±5 V ±5 V NA	Maximu (all cha (all cha (all cha Maximu (all cha Maximu Time C Sample	um Sample Rate nnels) um Record Length nnels) um Duration of aptured at Highest e Rate	2.5 GS/s 5 Mpoints 2 ms	
1.01 V/div to 10 V/div Vertical System Dig Characteristic	±100 V ±100 V ital Channels All MSO3000 Mode 16 Digital (D15 to D0)	±5 V ±5 V NA	(all cha (all cha (all cha (all cha Time C Sample (all cha	um Sample Rate nnels) um Record Length nnels) um Duration of aptured at Highest Rate nnels)	2.5 GS/s 5 Mpoints 2 ms	
1.01 V/div to 10 V/div Vertical System Dig Characteristic Input Channels Thresholds	±100 V ±100 V ±100 V ital Channels All MSO3000 Mode 16 Digital (D15 to D0) Threshold per set of 8	±3 V ±5 V NA	Maximi (all cha (all cha (all cha Maximu Time C Sample (all cha Time-ba	um Sample Rate nnels) um Record Length nnels) um Duration of aptured at Highest Rate nnels) ase Range (s/div)	2.5 GS/s 5 Mpoints 2 ms 1 ns to 1000 s	
1.01 V/div to 10 V/div Vertical System Dig Characteristic Input Channels Thresholds Threshold Selections	±100 V ±100 V ital Channels All MSO3000 Mode 16 Digital (D15 to D0) Threshold per set of 8 c TL. CMOS. ECL. PEC	±3 v ±5 V NA	Maximu (all cha Maximu (all cha Maximu Time C Sample (all cha (all cha Time-b Time-b Rance	um Sample Rate nnels) um Record Length nnels) um Duration of aptured at Highest Rate nnels) ase Range (s/div) ase Delay Time	2.5 GS/s 5 Mpoints 2 ms 1 ns to 1000 s -10 divisions to 5000 s	
1.01 V/div to 10 V/div Vertical System Dig Characteristic Input Channels Thresholds Threshold Selections User-defined Threshold Rance	±100 V ±100 V ±100 V tital Channels All MSO3000 Mode 16 Digital (D15 to D0) Threshold per set of 8 o TTL, CMOS, ECL, PEC -15 V to +25 V	±3 V ±5 V NA	Maximu (all cha Maximu (all cha Maximu Time C Sample (all cha Time-br Range Channe Deskey	Im Sample Rate nnels) Im Record Length nnels) Im Duration of aptured at Highest Rate nnels) ase Range (s/div) ase Delay Time el-to-Channel v Range	2.5 GS/s 5 Mpoints 2 ms 1 ns to 1000 s -10 divisions to 5000 s ±100 ns	
1.01 V/div to 10 V/div Vertical System Dig Characteristic Input Channels Thresholds Threshold Selections User-defined Threshold Range Maximum Input Voltace	±100 V ±100 V ±100 V ital Channels All MSO3000 Mode 16 Digital (D15 to D0) Threshold per set of 8 o TTL, CMOS, ECL, PEC -15 V to +25 V -20 V to +30 V	±3 V ±5 V NA	Maximu (all cha Maximu (all cha Maximu Time C Sample (all cha Time-br Time-br Range Channe Deskey Time-br	Im Sample Rate nnels) Im Record Length nnels) Im Duration of aptured at Highest r Rate nnels) ase Range (s/div) ase Delay Time gl-to-Channel v Range ase Accuracy	2.5 GS/s 5 Mpoints 2 ms 1 ns to 1000 s -10 divisions to 5000 s ±100 ns ±10 ppm over any ≥1 ms inter	val
1.01 V/div to 10 V/div Vertical System Dig Characteristic Input Channels Thresholds Threshold Selections User-defined Threshold Range Maximum Input Voltage Threshold Accuracy	±100 V ±100 V ±100 V ital Channels All MSO3000 Mode 16 Digital (D15 to D0) Threshold per set of 8 of TTL, CMOS, ECL, PEC -15 V to +25 V -20 V to +30 V ±(100 mV +3% of thress	ts v ts v hannels L, User Defined hold setting)	Maximu (all cha Maximu (all cha Time C Sample (all cha Time-ba Channe Deskev Time-ba	Im Sample Rate nnels) Im Record Length nnels) Im Duration of aptured at Highest Rate nnels) ase Range (s/div) ase Delay Time sl-to-Channel v Range ase Accuracy	2.5 GS/s 5 Mpoints 2 ms 1 ns to 1000 s -10 divisions to 5000 s ±100 ns ±10 ppm over any ≥1 ms inter	val
1.01 V/div to 10 V/div Vertical System Dig Characteristic Input Channels Thresholds Threshold Selections User-defined Threshold Range Maximum Input Voltage Threshold Accuracy Maximum Input Dynamic	±100 V ±100 V ±100 V ital Channels All MSO3000 Mode 16 Digital (D15 to D0) Threshold per set of 8 d TTL, CMOS, ECL, PEC -15 V to +25 V -20 V to +30 V ±(100 mV +3% of thress 50 V _{PP} (threshold settin	±3 V ±5 V NA Is channels L, User Defined hold setting) g dependent)	Maximu (all cha Maximu Time C Sample (all cha Time-br Sample (all cha Time-br Time-br Channe Deskey Time-br Horiz	Im Sample Rate nnels) Im Record Length nnels) Im Duration of aptured at Highest Rate nnels) ase Range (s/div) ase Delay Time el-to-Channel V Range ase Accuracy ontal System I	2.5 GS/s 5 Mpoints 2 ms 1 ns to 1000 s -10 divisions to 5000 s ±100 ns ±10 ppm over any ≥1 ms inter Digital Channels	val
1.01 V/div to 10 V/div Vertical System Dig Characteristic Input Channels Thresholds Threshold Selections User-defined Threshold Range Maximum Input Voltage Threshold Accuracy Maximum Input Dynamic Range	±100 V ±100 V ital Channels All MSO3000 Mode 16 Digital (D15 to D0) Threshold per set of 8 TTL, CMOS, ECL, PEC -15 V to +25 V -20 V to +30 V ±(100 mV +3% of thress 50 V _{PP} (threshold setting)	±3 V ±5 V NA Is channels L, User Defined hold setting) g dependent)	(all cha Maximu (all cha Maximu Time C Sample (all cha Time-ba Range Channe Deskev Time-ba Horizz Chara	Im Sample Rate nnels) Im Record Length nnels) Im Duration of aptured at Highest Rate nnels) ase Range (s/div) ase Delay Time el-to-Channel v Range ase Accuracy ontal System I cteristic	2.5 GS/s 5 Mpoints 2 ms 1 ns to 1000 s -10 divisions to 5000 s ±100 ns ±10 ppm over any ≥1 ms inter Digital Channels All MSO3000 Models	rval
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1.01 V/div to 10 V/div Vertical System Dig Characteristic Input Channels Thresholds Threshold Selections User-defined Threshold Range Maximum Input Voltage Threshold Accuracy Maximum Input Dynamic Range Minimum Voltage Swing Input Impedance Probe Loading Vertical Resolution	±100 V ±100 V ±100 V ital Channels All MSO3000 Mode 16 Digital (D15 to D0) Threshold per set of 8 (d) TTL, CMOS, ECL, PEC -15 V to +25 V -20 V to +30 V ±(100 mV +3% of thress 50 V _{PP} (threshold settin 500 mV _{PP} 101 kΩ 8 pF 1 bit	±3 V ±5 V NA Is channels L, User Defined hold setting) g dependent)	Maximu (all cha Maximu, (all cha Maximu, Time C Sample (all cha Time-bi Range Charne Deskev Time-bi Range Charne Deskev Maximu, (Main, 4 Maximu, (Main, Maximu, (Magani)	Im Sample Rate nnels) Im Record Length nnels) Im Duration of aptured at Highest e Rate nnels) ase Range (s/div) ase Delay Time el-to-Channel v range ase Accuracy ontal System I cteristic Im Sample Rate all channels) Im Record Length Vu, all channels)	2.5 GS/s 5 Mpoints 2 ms 1 ns to 1000 s -10 divisions to 5000 s ±100 ns ±10 pm over any ≥1 ms inter Digital Channels All MSO3000 Models 500 MS/s (2 ns resolution) 5 Mpoints 8.25 GS/s (121.2 ps resolution) 10 kpoints centered on the trig	val
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Current Probes (DPO3000)



Datasheet

Specifications

All specifications are guaranteed unless noted otherwise. All specifications apply to all models unless noted otherwise.

Characteristic	A621	A622
Frequency range	5 Hz to 50 kHz	DC to 100 kHz
Maximum input current	2000 A peak	100 A peak
Output	1 mV/A, 10 mV/A, 100 mV/A	10 mV/A, 100 mV/A
Maximum conductor diameter	54 mm (2.13 in.)	11.8 mm (0.46 in.)
Termination	BNC ¹	BNC ¹
Maximum bare-wire voltage	600 V (CAT III)	600 V (CAT III)
Safety	UL3111-2-032, CSA1010.2.032, EN61010-2-032, IEC61010-2-032	UL3111-2-032, CSA1010.2.032, EN61010-2-032, IEC61010-2-032

Ordering information

A621	2000 A AC Current probe/BNC.
A622	100 A AC/DC Current probe/BNC.

Recommended accessories

Adapter, lead; discrete – MLD, 2, 18 AWG, dual insul, BNC, female X 4 mm dual insul; banana jack X dual insul plug, shield banana

Options

012-1450-xx

Service options

Opt. R3	Repair Service 3 Years (including warranty)
Opt. R5	Repair Service 5 Years (including warranty)
CE	

Tektronix is registered to ISO 9001 and ISO 14001 by SRI Quality System Registrar.



Number of NC contacts / for main contacts Operating current • at AC-1/ at 400 V/rated value • at AC-51 / rated value • at AC-51 / rated value Operating current / minimum Operating current / minimum Operating voltage • at 50 Hz / at AC / rated value · at 60 Hz / at AC / rated value Working area related to the operating voltage • at 60 Hz / for AC · at the thyristor / for main contacts / maximum perating temperature Active power loss / total / typical	1
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Derating temperature *C Active power loss / total / typical W Resistance against the impulse current / rated value A I2t-level / maximum A ² ·s Control circuit: Type of voltage / of the controlled supply voltage Control supply voltage / 1 . · for DC . · initial rated value V · final rated value V · for DC / final value for signal <o>-recognition V Relative symmetrical tolerance / of the supply voltage % frequency mA . Control current . . · at minimum control supply voltage / for DC mA . Fuse assignments . .</o>	10
Active power loss / total / typical W Resistance against the impulse current / rated value A I2t-level / maximum A ² ·s Control circuit: Type of voltage / of the controlled supply voltage Control supply voltage / 1 · for DC · initial rated value V · final rated value V · for DC / final value for signal<0>-recognition V Relative symmetrical tolerance / of the supply voltage % frequency mA # Fuse assignments I I	40
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Control circuit: Type of voltage / of the controlled supply voltage Control supply voltage / 1 • for DC • initial rated value • final rated value V • final rated value • for DC / final value for signal<0>-recognition V Relative symmetrical tolerance / of the supply voltage frequency Control current • at minimum control supply voltage / for DC • for DC / rated value Fuse assignments	1,800
Type of voltage / of the controlled supply voltage Image: Supply voltage / 1 • for DC • initial rated value V • final rated value V Image: Supply voltage V • for DC / final value for signal<0>-recognition V Image: Supply voltage Image: Supply voltage • for DC / final value for signal<0>-recognition V Image: Supply voltage Image: Supply voltage • for DC / final value for signal<0>-recognition V Image: Supply voltage Image: Supply voltage frequency Control current • at minimum control supply voltage / for DC mA Image: Supply voltage Image: Supply voltage • for DC / rated value mA Image: Supply voltage Image: Supp	
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• initial rated value V • initial rated value V • final rated value V Control supply voltage V • for DC / final value for signal<0>-recognition V Relative symmetrical tolerance / of the supply voltage frequency % Control current * • at minimum control supply voltage / for DC mA * for DC / rated value mA	
• final rated value V Control supply voltage V • for DC / final value for signal<0>-recognition V Relative symmetrical tolerance / of the supply voltage frequency % Control current MA • at minimum control supply voltage / for DC mA • for DC / rated value mA	15
Control supply voltage V • for DC / final value for signal<0>-recognition V Relative symmetrical tolerance / of the supply voltage frequency % Control current • at minimum control supply voltage / for DC mA • for DC / rated value mA	24
• for DC / final value for signal<0>-recognition V Relative symmetrical tolerance / of the supply voltage frequency % Control current • at minimum control supply voltage / for DC • for DC / rated value mA Fuse assignments Image: state stat	
Relative symmetrical tolerance / of the supply voltage % frequency % Control current * • at minimum control supply voltage / for DC mA • for DC / rated value mA Fuse assignments *	5
Control current mA • at minimum control supply voltage / for DC mA • for DC / rated value mA	10
at minimum control supply voltage / for DC mA for DC / rated value mA Fuse assignments	
for DC / rated value mA Fuse assignments	2
Fuse assignments	15
	https://www.automation.siemens.com/cd- static/material/info/3RF20_eng.pdf

I V DE OT MOUNTING		screw fixing
Type of fixing/fixation / series installation	_	Yes
Design of the thread / of the screw for fastening of the operating	-	M4
resource Tightening torque / of the screw for fastening of the operating	N∙m	1.5
resource		45
Wiath		45
Height		20
		40
Connections:		
Design of the electrical connection / for main current circuit		screw-type terminals
Design of the thread / of the connection screw / for main contacts		M4
Tightening torque / for main contacts / with screw-type terminals		
• minimum	N∙m	2
• maximum	N∙m	2.5
Tightening torque (Ibf-in) / for main contacts / with screw-type terminals		
• minimum	lbf∙in	7
• maximum	lbf∙in	10.3
Type of the connectable conductor cross-section		
for main contacts		
• solid		2x (1.5 2.5 mm²), 2x (2.5 6 mm²)
 finely stranded 		
with conductor end processing		2x (1 2.5 mm²), 2x (2.5 6 mm²), 1x 10 mm²
for AWG conductors		
for main contacts		2x (14 10)
 for auxiliary and control contacts 		1x (AWG 20 12)
 for auxiliary and control contacts 		
• solid		1x (0.5 2.5 mm²), 2x (0.5 1.0 mm²)
 finely stranded 		
with conductor end processing		1x (0.5 2.5 mm²), 2x (0.5 1.0 mm²)
without conductor final cutting	_	1x (0.5 2.5 mm²), 2x (0.5 1.0 mm²)
Conductor cross section that can be connected		
• for main contacts		
• solid	mm²	1.5 6
stranded wire		
with conductor end processing	mm²	1 10
for auxiliary and control contacts		

• solid	mm²	0.5 2.5			
stranded wire					
with conductor end processing /	mm²	0.5 2.5			
without conductor final cutting	mm²	0.5 2.5			
AWG number / as coded connectable conductor cross-section / for main contacts		14 10			
Design of the electrical connection / for auxiliary and control current circuit		screw-type	terminals		
Design of the thread / of the connection screw / of the auxiliary and control pins		M3			
AWG number / as coded connectable conductor cross-section					
 for auxiliary and control contacts 		20 12			
Skinning length / of the cable / for main contacts	mm	10			
Skinning length / of the cable / for auxiliary and control contacts	mm	7			
Tightening torque / for auxiliary and control contacts					
with screw-type terminals	N∙m	0.5 0.6			
Tightening torque (lbf·in) / for auxiliary and control contacts					
with screw-type terminals	lbf∙in	4.5 5.3			
Certificates/approvals:					
General Product Approval	EMC	D	eclaration o onformity	of Test Certificat	es
CSA COST UR	С-ТІСК		EG-Konf.	Certificates/Tes Report	st
Environmental Confirmations					
Further information:					
Information- and Downloadcenter (Catalogs, Brochures,) http://www.siemens.com/industrial-controls/catalogs					
Industry Mall (Online ordering system) http://www.siemens.com/industrial-controls/mall					
CAx-Online-Generator http://www.siemens.com/cax					
Service&Support (Manuals, Certificates, Characteristics, FAQs,) http://support.automation.siemens.com/WW/view/en/3RF2050-1AA02/	all				
Image database (product images, 2D dimension drawings, 3D mod http://www.automation.siemens.com/bilddb/cax_en.aspx?mlfb=3RF208	dels, device	circuit diagra	ıms,)		
http://www.siemens.com/industrial-controls/catalogs Industry Mall (Online ordering system) http://www.siemens.com/industrial-controls/mall CAx-Online-Generator http://www.siemens.com/cax Service&Support (Manuals, Certificates, Characteristics, FAQs,) http://support.automation.siemens.com/WW/view/en/3RF2050-1AA02/ Image database (product images, 2D dimension drawings, 3D mon http://www.automation.siemens.com/bilddb/cax_en.aspx?mlfb=3RF205	all dels, device a 50-1AA02	circuit diagra	ıms,)		
3RF2050-1AA02 2age 4/5 03/06	/2013			subject to modif © Copyright Siemens A	icatior G 201



Measuring equipment (WT230/WT210)





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Specifications

The latest product information is available at our web site http://www.yokogawa.com/tm/. Review the specifications to determine which model is right for you.

Parameter	Voltage	Current
Input type	Floatir	g input
	Resistance voltage divider	Shunt input system
Rated values (ranges)	15/30/60/150/300/600 V	Direct input: 5/10/20/50/100/200 mA (WT210 only)1
		; 0.5/1/2/5/10/20 A (WT210/WT230)
		External input (optional): 2.5/5/10 V or 50/100/200 mV
Measuring instrument loss	Input resistance: Approximately 2 MΩ	Direct input: Approximately 500 mΩ + approximately 0.1 µH (5-200 mA; WT210)
(input resistance)	Input capacitance: Approximately 13 pF	Approximately 6 mΩ + 10 mΩ (max) ² + approximately 0.1 µH (0.5-20 A; WT21
		Approximately 6 mΩ approximately 0.1 µH (0.5-20 A; WT230)
		External input: Approximately 100 kΩ (2.5/5/10 V), approximately 20 kΩ (50/100/200 mV)
Maximum instantaneous allowed input	Peak voltage of 2.8 kV or rms value of 2.0 kV (whichever is less)	0.5-20 A (WT210/WT230): Peak current of 450 A or rms value of 300 A (whichever is less)
(1 cycle, 20 ms duration)		5-200 mA (WT210): Peak current of 150 A or rms value of 100 A (whichever is less)
		External input: Peak value of 10 times range or less
Maximum instantaneous allowed input	Peak voltage of 2.0 kV or rms value of 1.5 kV (whichever is less)	0.5-20 A (WT210/WT230): Peak current of 150 A or rms value of 40 A (whichever is less)
(1 second duration)		5-200 mA (W1210): Peak current of 30 A or rms value of 20 A (whichever is less)
Manimum application offered lands	Deals uplicate of 4 E M/ or rese value of 4 0 M/ (whichever is least)	External input: Peak value or 10 times range or less
Maximum continuous allowed input	Peak voltage of 1.5 kV of this value of 1.0 kV (whichever is less)	5.200 mA (WT210)/ Peak current of 20 A or rms value of 20 A (whichever is less)
		External input: Peak value of 5 times range or less
Maximum continuous common mode voltage	600 Vrms (with output connector protective cover). CAT II / 400 Vrms (with	out output connector protective cover) CAT II
(with 50/60 Hz input)		
CMRR	50/60 Hz, -80 dB or higher (±0.01% of range or less) with voltage	input terminals shorted and current input terminals open and external input terminals shorted
600 Vrms across input terminal and case	Reference value (up to 100 kHz): ±((Maximum range rating)/(Rat	ge rating) × 0.001 × f% of rng) or less (voltage range and 0.5-20 A current range and external
	input range ³)	
	±((Maximum range rating)/(Range rating) × 0.0002 × f% of rng) of	r less (WT210; 5-200 mA range)
	Note: 0.01% or higher. f is in kHz. 3 Decuple the above-formula	about the external input range.
Input terminal type	Plug-in terminal (safety terminal)	Direct input: Large binding post
		External input: BNC connector (insulation type)
A/D converter	Simultaneous conversion of voltage and current inputs	
	Resolution: 16 bits	
	Maximum conversion speed: Approximately 20 µs (approximately	/ 51 kHz)
Range switching	Ranges can be set manually, automatically, or through online co	trois
	Auto-range function	
	Range raising: When a measurement exceeds 130% of the ratin	, or when the peak value exceeds approximately 300% of the rating
	Range lowering: When a measurement falls to 30% or less of the	rating, and the peak value falls to approximately 300% or less of the rating for the low range
Measurement mode switching	Any of the following, selected manually or through online control:	RMS (true rms value measurements for both voltage and current). V MEAN (calibration of
	average-value-rectified rms value for voltage; true rms value mea	surement for current), DC (simple averages for both voltage and current)
lote: Current direct input and external second	or input cannot both he used at the same time. When you operate o	urrent input terminale and external input terminale, places ha careful

Measurement Function

Paran	neter		Voltage/current				Active powe	r
System		Digital sampling; su				n of averages method		
Frequency range					DC, and 0.5	Hz to 100 kHz		
Crest factor				3 (with rat	ed input) 300 (w	ith minimum effective inp	out)	
Accuracy (three mor	oths after calibration)	DC:	±(0.2% or rdg + 0.2% of	f rng)*		DC:	±(0.3% or rdg + 0.2	% of rng)*
(Conditions)		0.5 Hz ≤ f < 45 Hz:	±(0.1% of rdg + 0.2% of	f rng)		0.5 Hz ≤ f < 45 Hz:	±(0.3% of rdg + 0.2	% of rng)
Temperature: 23	3±5°C	45 Hz \leq f \leq 66 Hz:	±(0.1% of rdg + 0.1% of	f rng)		45 Hz \leq f \leq 66 Hz:	±(0.1% of rdg + 0.1	% of rng)
Humidity: 30-75	% RH	66 Hz < f ≤ 1 kHz:	±(0.1% of rdg + 0.2% of	f rng)		66 Hz < f ≤ 1 kHz:	±(0.2% of rdg + 0.2	% of rng)
Input waveform:	Sinewave	1 kHz < f ≤ 10 kHz:	$\pm((0.07\times f)\%$ of rdg + 0.	.3% of rng)		1 kHz < f ≤ 10 kHz:	±(0.1% of rdg + 0.3	% of rng)
Power factor: co	i\$φ = 1						±((0.067 × (f-1))%	6 of rdg)
In-phase voltag	e: 0 V DC	10 kHz < f ≤ 100 kHz:	±((0.5% of rdg + 0.5% of	of rng)		10 kHz < f ≤ 100 kHz:	±(0.5% of rdg + 0.5	% of rng)
Frequency filter:	ON at 200 Hz or less		$\pm ((0.04 \times (f-10))\% \text{ of }$	rdg)			±((0.09 × (f-10))%	6 of rdg)
Scaling: OFF								
Display digits: 5	digits							
After CAL is exe	ecuted							
Note: In the accuracy calcu	lation formula, f is in kHz.	* Add $\pm 10~\mu A$ to the cur	rent DC accuracy.			* Add $\pm 10 \ \mu$ A × voltage	reading to the power	DC accuracy.
Power factor effect						For cos q = 0		
						$45 \text{ Hz} \le f \le 66 \text{ Hz}: \pm 0.2$	2% of VA (VA is a read	ing value of apparent power)
						Reference data (up to	100 kHz): ±((0.2 + 0.2	× f)% of VA)
						Indicated value toleran	ice for 0 < cosφ < 1	
Note: In the accuracy calcu	lation formula, f is in kHz.					Add (tan x (effect when a	cosφ = 0)% of power readi	ing to the above power accuracy.
						Note: ϕ is the phase an	ngle between voltage a	and current.
Effective input range		1-130% of voltage/current range rating (for accuracy at 110-130%, add the reading tolerance × 0.5 to the above accuracy)						
Accuracy (12 months	after calibration)	Add the accuracy's read	ding tolerance (three mont	ths after calibi	ation) \times 0.5 to th	e accuracy three months	s after calibration.	
Line filter function		A low-pass filter can be inserted in the input circuit for measurement. The cutoff frequency (fc) is 500 Hz.						
Accuracy with line filte	r on	Voltage and current: Ad	d 0.2% of rdg at 45-66 Hz	. Add 0.5% of	rdg below 45 H;	Ζ.		
		Power: Add 0.3% of rdg	at 45-66 Hz. Add 1% of r	dg below 45 H	z.			
Temperature coefficie	nt	±0.03% of range/°C at 5	5-18°C and 28-40°C.					
Display updating inter	vals	0.1/0.25/0.5/1/2/5 secon	nds					
Lead/lag detecting		Lead/lag is detected co	rrectly when phase differe	nce equal to o	r greater than \pm	5° with both voltage and	current inputs as sine	waves equal to or greater than
		50% of rated range-valu	ue, and the frequency is be	etween 20 Hz	to 2 kHz.			
Measurement lower li	mit frequency	Data updating rate	0.1 second	0.25 second	0.5 second	1 second 2 sec	onds 5 seconds	
		Measurement lower limit frequ	uency 25 Hz	10 Hz	5 Hz	2.5 Hz 1.5 H	z 0.5 Hz	
								rng: Range rdg: Rea
				0		tion Functions		
Frequency we	asurements			U U	ommunica	ation Functions	(Optional for	the w1210)
leasurement inputs:	V1, V2, V3, A1,	A2, or A3 (select one)	GF	P-IB or serial i	nterface (RS-232-C)	(select one)	
leasurement system:	Reciprocal syste	m		GI	'-IB Electrical and	1 mechanical enecific	cations:	
ileasurement ilequ	100 ms: 25 Hz :	< f < 100 kHz			Liectricar and	Conform to IEE	E Standard 488-1	978 (JIS C1901-1987).
	250 ms: 10 Hz	≤ f ≤ 100 kHz			Functional sp	pecifications:		
	500 ms: 5 Hz :	≤ f ≤ 100 kHz				SH1, AH1, T5,	L4, SR1, RL1, PR	0, DC1, DT1, C0
	1 sec: 2.5 Hz	≤t≤ 100 kHz <t< 50="" khz<="" td=""><td></td><td></td><td>Protocol:</td><td>Conforms to IE</td><td>EE Standard 488.</td><td>2-1992.</td></t<>			Protocol:	Conforms to IE	EE Standard 488.	2-1992.
	5 sec: 0.5 Hz	≤i≤ ou krłz <f< 20="" khz<="" td=""><td></td><td></td><td>Addresses</td><td>0-30 talker/liste</td><td>ue ner addresses car</td><td>n be set</td></f<>			Addresses	0-30 talker/liste	ue ner addresses car	n be set
Accuracy:	±(0.06% of rdg)			Se	rial interface	(RS-232-C)		
Conditions:	Input equal to a	at least 30% of volta	age/current rated ran	ige.	Transmission m	iode: Asynchronous		
	Frequency filter	function ON at 200 H	z and below.		Baud rates:	1200, 2400, 48	00, 9600 bps	
	Frequency filter	cutorr frequency: 500	HZ					

5

Specifications

Calculation Functions

		Single- phase 3- wire	Three-phase 3-wire (2 voltages, 2 currents)	Three-phase 3-wire (3 voltages, 3 currents)	Three- phase 4- wire
Voltage ∑V		(V1 + V3)/2	(V1 + V2 + V3)/3	
Current ∑A		(A1 + A3)/2	(A1 + A2 + A3)/3	3
Active power ∑W		W1 + W3	3		W1+W2+W3
Reactive power var, ∑var	vari =√(VA² - W²)	var1 + va	ar3		var1 + var2 + var3
Apparent power VA, ∑VA	VAi = Vi × Ai	VA1 + VA3	√3/2 (VA1 + VA3)	√5/3 (VA1 + VA2 + VA3)	VA1 + VA2 + VA3
Power factor PF, ∑PF	Pfi = Wi/VAi	ΣW/ΣVA			
Phase angle deg, Σdeg	degi = cos ⁻¹ (Wi/VAi)	cos¹ (∑V	V/∑VA)		

- Total
 Cost (WVA)
 Owe (Letter)

 Notes
 ...
 ...
 ...

 1. This phase single (keg) are calculated from voltage, current, and active power.
 ...
 ...

 The phase single (keg) are calculated from voltage, current, and active power.
 ...
 ...

 2. If either voltage or current fails to 0.5% of the range rating or less, then the apparent power (Var) and reactive power (Var) and elsplayed as zero, and errors are displayed for power factor (PF) and phase angle (deg).
 ...

 3. The sign of the var of each phase is calculated with a negative sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and with a positive sign if the current input leads the voltage input, and the sign sign and the sign sign if the current input leads the voltage input, and with a positive sind t
- Display Functions

6

Diopidy i c	-						
Display unit: Display areas:	7-se 3	gment LED (light-emitting diode)					
Display area		Displayed information					
A	V, A, W, VA,	var (for each element), integration elapsed time					
В	V, A, W, PF,	deg (for each element, percentage (content percentage, THD)					
С	V, A, W, V/A	V, A, W, V/AHz, Vpk, Apk, ±Wh, ±Ah (for each element), MATH					
Measurement paramet	ers Maximum d	lisplay Display resolution					
V, A, W, VA, var	9999	99 0.001%					
PF	±1.00	0.01%					
deg	±180	.0 0.1*					
±Wh, ±Ah	99999	99 0.0001%					
VHz, AHz	9999	99 Input frequency/20,000					
Display digits: 4	or 5 digits (se	ectable by user).					
Jnits: Jisplay updati Response timu Vaximum disp Uisplay scaling Effective di Setting ran Veraging fun Setting ran Veraging fun There are t Exponentia Moving average Auto-range m An LED tur MAX hold fun AX hold fun AX hold fun AX hold function System:	m, k maintervalses: Maxas filter filter filter filter maintervalses filter filter filter filter filter maintervalses filter maintervalses filter filt	M. V, A. W. VA, var, Hz, hz, deg, % i 10, 2550 (51/25) seconds: intum 22 times the display updating interval (time required isplay value to enter accuracy range of final value with line off, when range rating abruptly changes from 0% to 100%, from 100% to 0%) % of voltage/current range rating ut Vrms, Arms, and Ah, 0.5% of range rating. that 10.5% is zero suppression. cted automatically according to the digits in the voltage and ant ranges. In to 9999 ing methods (selectable by user): Insee can be set and exponential average is used, the attenuation cted. In cases where a moving average is used, the number of selected from 8, 16, 32, and 64. the input value is outside the range set for the auto-range. ed to hold V, A, W, VA, var, Vpk, and Apk at maximum values. In a functions, it is possible to perform efficiency (WT230 only) input crest factor measurements. as well as arithmetic ulations on DISPLAY A and B measurements. In addition, it soible to display average active power for time-converted grated power.					
Integration	n Functio	ons					
Display resolu	tion: The	minimum display resolution changes together with the					
Maximum disp Modes:	lay: -999 Star	9/3/2009 Value. 199 to 9999999 MWh/MAh Idard integration mode (timer mode), continuous integration (concert mode), manual integration mode.					
Timer:	Auto	imatic integration start/stop based on timer setting. ing range: 000 h:00 min:00 sec to 10000 h:00 min:00 sec time is set to zero, manual mode is automatically set.)					
Count over flo	w: Whe to at oper ±(di	in the integrated value exceeds 999999 MWh/MAh or falls least-99999 MWh/MAh, the elapsed time is saved and the ration is stopped. splay accuracy + 0.1% of rdg)					
Timer accurac Remote contro	y: ±0.0 bl: Star exte optio	2% ting, stopping, and resetting can be controlled through rnal contact signals. This function is only available when on /DA4, /DA12 or /CMP is installed.					

Internal Memory Functions

Aeasurement data			
Stored data	Normal measurement	Harmonic measurement	
WT210 (760401)	Data for 600 samples	Data for 30 samples	
WT230 (760502)	Data for 300 samples	Data for 30 samples	
WT230 (760503)	Data for 200 samples	Data for 30 samples	
Store interval:	Display updating interv and 59 seconds	/al and 1 second to 99 hoเ	urs, 59 minutes
Recall interval:	Display updating interv	al and 1 second to 99 hou	urs, 59 minutes
	and 59 seconds	occurd incremente \	
anel setting information:	Four different patterns	of panel setting information	n can be written
	read.		
Harmonic Mea: System: Measurement freque Maximum display: Josplay digits: Measurement paran Measurement eleme Sampling speed, wir The values for the as shown below. The values for the as shown below. The values for the as shown below. To site states the transmitted of the shown below. The values for the as shown below. The values for the as shown below. The values for the transmitted of the shown below. The values for the shown below. The values for the values for the values for the shown below. The values for the values for the values for the values for the shown below. The values for the values for th	surement Function PLL synchronization necy range: Fundamental frequent 99999 4 or 5 digits (selectab) Factory default setting W3. deg1, deg2, deg3 v01age, rms ourrent, 4 harmonic distortion ra- narmonic distortion ra- to ra- se parameters current, 4 harmonic distortion ra- se parameters vary acco Sampling speed f × 01 de2 f × 026 Hz f × 04 Hz 1024 I ength: 32 bits Rectangular erval: 0.2506/51/2/5 second/ according to the con- parameters transferrer erval: Note: For nth-order co × 100/mm ² /W/b to the	n (optional) cy in range of 40-440 Hz e by user). is 5 digits. T210, Vi V. 2, V3, A1, A1 (V/T230), individual harmonic oc tidive power, fundamenta te, individual harmonic oc ording to the input fundament te element. is orders ording to the input fundament (Vindsw width 2 periods of f 4 periods of f 4 periods of f 16 periods of f 10 periods of f	2, A3, W1, W2 onic levels, rm if requency PF ntent international frequency of the number of 30 30 and police output the number of 30 and police output reading on the number of a curracy.
	× (10/(m+1))%) to the	n+mth order and n-mth o	rder.
	tional)		
lumber of outpute:	±5 V FS (maximum ap	12 option: 4 parameters	with /DA4 ontio
Number of outputs: Dutput data selection: Accuracy: D/A converter: Response time: Jpdating interval: femperature coeffici Dutput type	\pm 5 V FS (maximum ap 12 parameters with /DA Can be set separately \pm (equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme ient: \pm 0.05% °C of FS	Al2 option; 4 parameters v for each channel. + 0.2% of FS) display updating interval nt's display updating inter	rval
Aumber of outputs: Dutput data selection: Accuracy: D/A converter: Response time: Jpdating interval: Temperature coeffici Dutput type Frequency	±5 V FS (maximum ag 12 parameters with /D/ Can be set separately ±(equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme ient: ±0.05% °C of FS	(a) Contracting (±1.5 V) for experimental (±12 option; 4 parameters v for each channel. (+ 0.2% of FS) (display updating interval nt's display updating interval)	rval
Aumber of outputs: Dutput data selection: Accuracy: J/A converter: Response time: Jpdating interval: Temperature coeffici Dutput type Frequency	±5 V FS (maximum ag 12 parameters with /D/2 Can be set separately ±(equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme tent: ±0.05% °C of FS	In Continuency 27.5 v) for ex 132 option; 4 parameters v for each channel. + 0.2% of FS) display updating interval nt's display updating inter	rval
Jumber of outputs: Dutput data selection: Nocuracy: J/A converter: Response time: Jpdating interval: Temperature coeffici Dutput type Frequency	±5 V FS (maximum ag 12 parameters with /D/ Can be set separately ±(equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme tent: ±0.05% C of FS	In Oximately 27.5 V) for ex 12 option, 4 parameters v for each channel. + 0.2% of FS) display updating interval nts display updating inter	rval
Jumber of outputs: Jupt data selection: Vacuracy: Al converter: Response time: Jordania interval: (emperature coeffici Juptut type Frequency D/A	±5 V FS (maximum ag 12 parameters with /b/ Can be set separately (equipment accuracy 12-bit resolution Maximum finequipme ent: ±0.05% C of FS	provintienty 27.3 ° John et al. 2010 for an et al. 2010 for a parameters v 10 ° 2010 for a parameters v	vith /DA4 option
Jumber of outputs: Jupt data selection: Vacuracy: Alk converter: Response time: Jordating interval: femperature coeffici Juptut type Frequency D/A	±5 V FS (maximum ag 12 parameters with /0/ Can be set separately (equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme ent: ±0.05% C of FS	provinitely 27.3 ° Die for 60 million and the for 60 million and the for 60 million and the for 60 million and the display updating interval nt's display updating int's display updating interval nt's display updating	vith /DA4 option
Jumber of outputs; Jupt data selection; Vacuracy: JA converter: Response time: Jedating interval: Jedating interval: Jedating interval: Frequency D/A D/A Integration	±5 V FS (maximum ag 12 parameters with /0/ Can be set separately (equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme ent: ±0.05% C of FS	provinitely 27.3 ° Die for 60 million 2000 million for 60 million 2000 million display updating interval nt's display updating interval	vith /DA4 option
Jumber of outputs, Jupt data selection: Vacuracy: Sesponse time: Sesponse time: Jemperature coeffici Jutput type Frequency Difference Integration DiA output 2004 Sov	+5 V FS (maximum ag 12 parameters with /0/ Can be set separately (equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme ent: ±0.05% C of FS	Provinces 2 - 2 - 5 - 0 - Die tropion - 1 - parameters v for annuelle - - + 0 - 2% of FS) display updating interval nt's display updating interval nt's display updating interval 	ve
Jumber of outputs: Jupt data selection: Vacuracy: A converter: Response time: Jupt data selection: Jupt to coeffici Dutput type Frequency Dia Integration DiA output 7 av 5 ov	+5 V FS (maximum ag 12 parameters with /0/ Can be set separately (equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme ent: ±0.05% C of FS	province of 27.3 of John et al. (1.3 of J	ue
Jumber of outputs: Juppt data selection: Vacuracy: A converter: Response time: Jedding interval: Gruperature coeffici Jupding therval: Prequency D/A Integration D/A cutput 7.0v 5.0v	15 V FS (maximum ag 12 parameters with /0/ Can be set separately (equipment accuracy 12-bit resolution Maximum 2 times the methods of the set ent: ±0.05% C of FS (odpat) 	Differentiative 27.5 °	vith /DA4 option rval
Jumber of outputs, Jumber of outputs, Vacoureters, Sesponse times, Sesponse ti	±5 V FS (maximum ag 12 parameters with /0/ Can be set separately (equipment accuracy 12-bit resolution Maximum 2 times the Same as the equipme ent: ±0.05% C of FS	productingly 27.5 ° D DF to 2010 of 1 parameters v for 0.2% of FS) display updating interval nt's display updating interval nt's display updating interval nt's display updating interval New Yorks Toole Deplay val ON- of nating For nated input to integra	ue ton time
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